

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
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2				*****
3				*
4				*Testcase IEEE ADD
5				* Test case capability includes IEEE exceptions trappable and
6				* otherwise. Test results, FPCR flags, the Condition code, and any
7				* DXC are saved for all tests.
8				*
9				*
10				* *****
11				** IMPORTANT! **
12				* *****
13				*
14				* This test uses the Hercules Diagnose X'008' interface
15				* to display messages and thus your .tst runtest script
16				* MUST contain a "DIAG8CMD ENABLE" statement within it!
17				*
18				*
19				*****
21				*****
22				*
23				* bfp-016-add.asm
24				*
25				* This assembly-language source file is part of the
26				* Hercules Binary Floating Point Validation Package
27				* by Stephen R. Orso
28				*
29				* Copyright 2016 by Stephen R Orso.
30				* Runtest *Compare dependency removed by Fish on 2022-08-16
31				* PADCSECT macro/usage removed by Fish on 2022-08-16
32				*
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56				* PROFITS; OR BUSINESS INTERRUPTION) HOWEVER CAUSED AND ON ANY THEORY

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				57 * OF LIABILITY, WHETHER IN CONTRACT, STRICT LIABILITY, OR TORT
				58 * (INCLUDING NEGLIGENCE OR OTHERWISE) ARISING IN ANY WAY OUT OF THE USE
				59 * OF THIS SOFTWARE, EVEN IF ADVISED OF THE POSSIBILITY OF SUCH DAMAGE.
				60 *
				61 *****
				63 *****
				64 *
				65 * Tests the following three conversion instructions
				66 * ADD (short BFP, RRE)
				67 * ADD (long BFP, RRE)
				68 * ADD (extended BFP, RRE)
				69 * ADD (short BFP, RXE)
				70 * ADD (long BFP, RXE)
				71 *
				72 * Test data is compiled into this program. The test script that runs
				73 * this program can provide alternative test data through Hercules R
				74 * commands.
				75 *
				76 * Test Case Order
				77 * 1) Short BFP basic tests, including traps and NaN propagation
				78 * 2) Short BFP finite number tests, incl. traps and scaling
				79 * 3) Short BFP FPC-controlled rounding mode exhaustive tests
				80 * 4) Long BFP basic tests, including traps and NaN propagation
				81 * 5) Long BFP finite number tests, incl. traps and scaling
				82 * 6) Long BFP FPC-controlled rounding mode exhaustive tests
				83 * 7) Extended BFP basic tests, including traps and NaN propagation
				84 * 8) Extended BFP finite number tests, incl. traps and scaling
				85 * 9) Extended BFP FPC-controlled rounding mode exhaustive tests
				86 *
				87 * Three input test sets are provided each for short, long, and
				88 * extended BFP inputs. Test values are the same for each precision
				89 * for most tests. Overflow and underflow each require precision-
				90 * dependent test values.
				91 *
				92 * Also tests the following floating point support instructions
				93 * LOAD (Short)
				94 * LOAD (Long)
				95 * LFPC (Load Floating Point Control Register)
				96 * SRNMB (Set BFP Rounding Mode 3-bit)
				97 * STORE (Short)
				98 * STORE (Long)
				99 * STFPC (Store Floating Point Control Register)
				100 *
				101 *****

[illegible]

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00000000		00000000		145 USING *,R15
00000000		00019E00		146 USING HELPERS,R12
				147 *
				148 * Above works on real iron (R15=0 after sysclear)
				149 * and in z/CMS (R15 points to start of load module)
				150 *
				152 *****
				153 *
				154 * Low core definitions, Restart PSW, and Program Check Routine.
				155 *
				156 *****
00000000		00000000	0000008E	158 ORG STRTLABL+X'8E' Program check interruption code
0000008E	0000			159 PCINTCD DS H
				160 *
		00000150	00000001	161 PCOLDPSW EQU STRTLABL+X'150' z/Arch Program check old PSW
				162 *
00000090		00000090	000001A0	163 ORG STRTLABL+X'1A0' z/Arch Restart PSW
000001A0	00000001 80000000			164 DC X'0000000180000000',AD(START)
				165 *
000001B0		000001B0	000001D0	166 ORG STRTLABL+X'1D0' z/Arch Program check NEW PSW
000001D0	00000000 00000000			167 DC X'0000000000000000',AD(PROGCHK)
				168 *
				169 * Program check routine. If Data Exception, continue execution at
				170 * the instruction following the program check. Otherwise, hard wait.
				171 * No need to collect data. All interesting DXC stuff is captured
				172 * in the FPCR.
				173 *
000001E0		000001E0	00000200	174 ORG STRTLABL+X'200'
00000200				175 PROGCHK DS 0H Program check occurred...
00000200	9507 F08F		0000008F	176 CLI PCINTCD+1,X'07' Data Exception?
00000204	A774 0004		0000020C	177 JNE PCNOTDTA ..no, hardwait (not sure if R15 is ok)
00000208	B2B2 F150		00000150	178 LPSWE PCOLDPSW ..yes, resume program execution
0000020C	900F F23C		0000023C	180 PCNOTDTA STM R0,R15,SAVEREGS Save registers
00000210	58C0 F27C		0000027C	181 L R12,AHELPERS Get address of helper subroutines
00000214	4DD0 C000		00019E00	182 BAS R13,PGMCK Report this unexpected program check
00000218	980F F23C		0000023C	183 LM R0,R15,SAVEREGS Restore registers
0000021C	12EE			185 LTR R14,R14 Return address provided?
0000021E	077E			186 BNZR R14 Yes, return to z/CMS test rig.
00000220	B2B2 F228		00000228	187 LPSWE PROGPSW Not data exception, enter disabled wait
00000228	00020000 00000000			188 PROGPSW DC 0D'0',X'0002000000000000',XL6'00',X'DEAD' Abnormal end
00000238	B2B2 F2F8		000002F8	189 FAIL LPSWE FAILPSW Not data exception, enter disabled wait
0000023C	00000000 00000000			190 SAVEREGS DC 16F'0' Registers save area
0000027C	00019E00			191 AHELPERS DC A(HELPERS) Address of helper subroutines

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				193 *****
				194 *
				195 * Main program. Enable Advanced Floating Point, process test cases.
				196 *
				197 *****
00000280				199 START DS 0H
00000280	B600 F308		00000308	200 STCTL R0,R0,CTLR0 Store CR0 to enable AFP
00000284	9604 F309		00000309	201 OI CTLR0+1,X'04' Turn on AFP bit
00000288	B700 F308		00000308	202 LCTL R0,R0,CTLR0 Reload updated CR0
				203 *
0000028C	41A0 F314		00000314	204 LA R10,SHORTNF Point to short BFP non-finite inputs
00000290	4DD0 F3A4		000003A4	205 BAS R13,SBFPNF Add short BFP non-finites
00000294	41A0 F324		00000324	206 LA R10,SHORTF Point to short BFP finite inputs
00000298	4DD0 F45E		0000045E	207 BAS R13,SBFPF Add short BFP finites
0000029C	41A0 F334		00000334	208 LA R10,RMSHORTS Point to short BFP rounding mode tests
000002A0	4DD0 F504		00000504	209 BAS R13,SBFPRM Add short BFP for rounding tests
				210 *
000002A4	41A0 F344		00000344	211 LA R10,LONGNF Point to long BFP non-finite inputs
000002A8	4DD0 F586		00000586	212 BAS R13,LBFPNF Add long BFP non-finites
000002AC	41A0 F354		00000354	213 LA R10,LONGF Point to long BFP finite inputs
000002B0	4DD0 F63C		0000063C	214 BAS R13,LBFPPF Add long BFP finites
000002B4	41A0 F364		00000364	215 LA R10,RMLONGS Point to long BFP rounding mode tests
000002B8	4DD0 F6E2		000006E2	216 BAS R13,LBFPRM Add long BFP for rounding tests
				217 *
000002BC	41A0 F374		00000374	218 LA R10,XTNDNF Point to extended BFP non-finite inputs
000002C0	4DD0 F760		00000760	219 BAS R13,XBFPNF Add extended BFP non-finites
000002C4	41A0 F384		00000384	220 LA R10,XTNDF Point to ext'd BFP finite inputs
000002C8	4DD0 F7EA		000007EA	221 BAS R13,XBFPF Add ext'd BFP finites
000002CC	41A0 F394		00000394	222 LA R10,RMXTNDS Point to ext'd BFP rounding mode tests
000002D0	4DD0 F860		00000860	223 BAS R13,XBFPRM Add ext'd BFP for rounding tests
				224 *
				225 *****
				226 * Verify test results...
				227 *****
				228 *
000002D4	58C0 F27C		0000027C	229 L R12,AHELPERS Get address of helper subroutines
000002D8	4DD0 C0A0		00019EA0	230 BAS R13,VERISUB Go verify results
000002DC	12EE			231 LTR R14,R14 Was return address provided?
000002DE	077E			232 BNZR R14 Yes, return to z/CMS test rig.
000002E0	B2B2 F2E8		000002E8	233 LPSWE GOODPSW Load SUCCESS PSW

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000002E8				235 DS 0D Ensure correct alignment for PSW
000002E8	00020000 00000000			236 GOODPSW DC X'0002000000000000',AD(0) Normal end - disabled wait
000002F8	00020000 00000000			237 FAILPSW DC X'0002000000000000',XL6'00',X'0BAD' Abnormal end
				238 *
00000308	00000000			239 CTLR0 DS F
0000030C	00000000			240 FPCREGNT DC X'00000000' FPCR, trap all IEEE exceptions, zero flags
00000310	F8000000			241 FPCREGTR DC X'F8000000' FPCR, trap no IEEE exceptions, zero flags
				242 *
				243 * Input values parameter list, four fullwords for each test data set
				244 * 1) Count,
				245 * 2) Address of inputs,
				246 * 3) Address to place results, and
				247 * 4) Address to place DXC/Flags/cc values.
				248 *
00000314				249 SHORTNF DS 0F Input pairs for short BFP non-finite tests
00000314	0000000A			250 DC A(SBFPNFCT)
00000318	000008CC			251 DC A(SBFPNFIN)
0000031C	00001000			252 DC A(SBFPNFOT)
00000320	00001700			253 DC A(SBFPNFFL)
				254 *
00000324				255 SHORTF DS 0F Input pairs for short BFP finite tests
00000324	00000006			256 DC A(SBFPCT)
00000328	000008F4			257 DC A(SBFPIN)
0000032C	00001E00			258 DC A(SBFPOUT)
00000330	00001F00			259 DC A(SBFPFLGS)
				260 *
00000334				261 RMSHORTS DS 0F Input pairs for short BFP rounding testing
00000334	00000008			262 DC A(SBFPRMCT)
00000338	00000924			263 DC A(SBFPINRM)
0000033C	00002000			264 DC A(SBFPRMO)
00000340	00002300			265 DC A(SBFPRMOF)
				266 *
00000344				267 LONGNF DS 0F Input pairs for long BFP non-finite testing
00000344	0000000A			268 DC A(LBFPNFCT)
00000348	00000964			269 DC A(LBFPNFIN)
0000034C	00004000			270 DC A(LBFPNFOT)
00000350	00004D00			271 DC A(LBFPNFFL)
				272 *
00000354				273 LONGF DS 0F Input pairs for long BFP finite testing
00000354	00000006			274 DC A(LBFPCT)
00000358	000009B8			275 DC A(LBFPIN)
0000035C	00005400			276 DC A(LBFPOUT)
00000360	00005600			277 DC A(LBFPFLGS)
				278 *
00000364				279 RMLONGS DS 0F Input pairs for long BFP rounding testing
00000364	00000008			280 DC A(LBFPRMCT)
00000368	00000A18			281 DC A(LBFPINRM)
0000036C	00005700			282 DC A(LBFPRMO)
00000370	00005C00			283 DC A(LBFPRMOF)
				284 *
00000374				285 XTNDNF DS 0F Inputs for ext'd BFP non-finite testing
00000374	0000000A			286 DC A(XBFPNFCT)
00000378	00000A98			287 DC A(XBFPNFIN)
0000037C	00008000			288 DC A(XBFPNFOT)
00000380	00008D00			289 DC A(XBFPNFFL)
				290 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				304 *****	
				305 *	
				306 * Perform Add using provided short BFP inputs. This set of tests	
				307 * checks NaN propagation, operations on values that are not finite	
				308 * numbers, and other basic tests. This set generates results that can	
				309 * be validated against Figure 19-13 on page 19-16 of SA22-7832-10.	
				310 *	
				311 * That Figure has separate rows and columns for Normal and Tiny	
				312 * operands. Although the results are effectively the same for Normal	
				313 * and Tiny in any combination, the input data includes Normal and	
				314 * Tiny values.	
				315 *	
				316 * Four results are generated for each input: one RRE with all	
				317 * exceptions non-trappable, a second RRE with all exceptions trappable,	
				318 * a third RXE with all exceptions non-trappable, a fourth RXE with all	
				319 * exceptions trappable,	
				320 *	
				321 * The sum, FPCR, and condition code are stored for each result.	
				322 *	
				323 *****	
000003A4				325 SBFPNF DS 0H	BFP Short non-finite values tests
000003A4	9823 A000		00000000	326 LM R2,R3,0(R10)	Get count and address of add values
000003A8	9878 A008		00000008	327 LM R7,R8,8(R10)	Get address of result area and flag area.
000003AC	1222			328 LTR R2,R2	Any test cases?
000003AE	078D			329 BZR R13	..No, return to caller
000003B0	0DC0			330 BASR R12,0	Set top of loop
				331 *	
000003B2	9845 A000		00000000	332 LM R4,R5,0(R10)	Get count and start of addend values
				333 *	..which are the same as the augends
000003B6	0D60			334 BASR R6,0	Set top of inner loop
				335 *	
000003B8	7880 3000		00000000	336 LE FPR8,0(,R3)	Get short BFP augend
000003BC	7810 5000		00000000	337 LE FPR1,0(,R5)	Get short BFP addend
000003C0	B29D F30C		0000030C	338 LFPC FPCREGNT	Set exceptions non-trappable
000003C4	B30A 0081			339 AE BR FPR8,FPR1	Add FPR0/FPR1 RRE
000003C8	7080 7000		00000000	340 STE FPR8,0(,R7)	Store short BFP sum
000003CC	B29C 8000		00000000	341 STFPC 0(R8)	Store resulting FPCR flags and DXC
000003D0	B222 0000			342 IPM R0	Get condition code and program mask
000003D4	8800 001C		0000001C	343 SRL R0,28	Isolate CC in low order byte
000003D8	4200 8003		00000003	344 STC R0,3(,R8)	Save condition code in results table
				345 *	
000003DC	7880 3000		00000000	346 LE FPR8,0(,R3)	Get short BFP augend
000003E0	7810 5000		00000000	347 LE FPR1,0(,R5)	Get short BFP addend
000003E4	B29D F310		00000310	348 LFPC FPCREGTR	Set exceptions trappable
000003E8	B30A 0081			349 AE BR FPR8,FPR1	Add FPR0/FPR1 RRE
000003EC	7080 7004		00000004	350 STE FPR8,4(,R7)	Store short BFP sum
000003F0	B29C 8004		00000004	351 STFPC 4(R8)	Store resulting FPCR flags and DXC
000003F4	B222 0000			352 IPM R0	Get condition code and program mask
000003F8	8800 001C		0000001C	353 SRL R0,28	Isolate CC in low order byte
000003FC	4200 8007		00000007	354 STC R0,7(,R8)	Save condition code in results table
				355 *	
00000400	7880 3000		00000000	356 LE FPR8,0(,R3)	Get short BFP augend
00000404	7810 5000		00000000	357 LE FPR1,0(,R5)	Get short BFP addend
00000408	B29D F30C		0000030C	358 LFPC FPCREGNT	Set exceptions non-trappable

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
0000040C	ED80 5000 000A		00000000	359	AEB	FPR8,0(,R5)	Add FPR0/FPR1 RXE
00000412	7080 7008		00000008	360	STE	FPR8,8(,R7)	Store short BFP sum
00000416	B29C 8008		00000008	361	STFPC	8(R8)	Store resulting FPCR flags and DXC
0000041A	B222 0000			362	IPM	R0	Get condition code and program mask
0000041E	8800 001C		0000001C	363	SRL	R0,28	Isolate CC in low order byte
00000422	4200 800B		0000000B	364	STC	R0,11(,R8)	Save condition code in results table
				365 *			
00000426	7880 3000		00000000	366	LE	FPR8,0(,R3)	Get short BFP augend
0000042A	B29D F310		00000310	367	LFPC	FPCREGTR	Set exceptions trappable
0000042E	ED80 5000 000A		00000000	368	AEB	FPR8,0(,R5)	Add FPR0/FPR1 RXE
00000434	7080 700C		0000000C	369	STE	FPR8,12(,R7)	Store short BFP sum
00000438	B29C 800C		0000000C	370	STFPC	12(R8)	Store resulting FPCR flags and DXC
0000043C	B222 0000			371	IPM	R0	Get condition code and program mask
00000440	8800 001C		0000001C	372	SRL	R0,28	Isolate CC in low order byte
00000444	4200 800F		0000000F	373	STC	R0,15(,R8)	Save condition code in results table
				374 *			
00000448	4150 5004		00000004	375	LA	R5,4(,R5)	Point to next addend value
0000044C	4170 7010		00000010	376	LA	R7,4*4(,R7)	Point to next Add result area
00000450	4180 8010		00000010	377	LA	R8,4*4(,R8)	Point to next Add FPCR area
00000454	0646			378	BCTR	R4,R6	Loop through right-hand values
				379 *			
00000456	4130 3004		00000004	380	LA	R3,4(,R3)	Point to next input augend
0000045A	062C			381	BCTR	R2,R12	Loop through left-hand values
0000045C	07FD			382	BR	R13	All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				384	*****			
				385	*			
				386	* Perform Add using provided short BFP input pairs. This set of			
				387	* tests triggers IEEE exceptions Overflow, Underflow, and Inexact and			
				388	* collects both trap and non-trap results.			
				389	*			
				390	* Four results are generated for each input: one RRE with all			
				391	* exceptions non-trappable, a second RRE with all exceptions trappable,			
				392	* a third RXE with all exceptions non-trappable, a fourth RXE with all			
				393	* exceptions trappable,			
				394	*			
				395	* The sum, FPCR, and condition code are stored for each result.			
				396	*			
				397	*****			
0000045E	9823 A000		00000000	399	SBFPF	LM	R2,R3,0(R10)	Get count and address of test input values
00000462	9878 A008		00000008	400		LM	R7,R8,8(R10)	Get address of result area and flag area.
00000466	1222			401		LTR	R2,R2	Any test cases?
00000468	078D			402		BZR	R13	..No, return to caller
0000046A	0DC0			403		BASR	R12,0	Set top of loop
				404	*			
0000046C	B29D F30C		0000030C	405		LFPC	FPCREGNT	Set exceptions non-trappable
00000470	7880 3000		00000000	406		LE	FPR8,0(,R3)	Get short BFP augend
00000474	7810 3004		00000004	407		LE	FPR1,4(,R3)	Get short BFP addend
00000478	B30A 0081			408		AEBR	FPR8,FPR1	Add FPR8/FPR1 RRE non-trappable
0000047C	7080 7000		00000000	409		STE	FPR8,0(,R7)	Store short BFP sum
00000480	B29C 8000		00000000	410		STFPC	0(R8)	Store resulting FPCR flags and DXC
00000484	B222 0000			411		IPM	R0	Get condition code and program mask
00000488	8800 001C		0000001C	412		SRL	R0,28	Isolate CC in low order byte
0000048C	4200 8003		00000003	413		STC	R0,3(,R8)	Save condition code in results table
				414	*			
00000490	B29D F310		00000310	415		LFPC	FPCREGTR	Set exceptions trappable
00000494	7880 3000		00000000	416		LE	FPR8,0(,R3)	Reload short BFP augend
				417	*			
00000498	B30A 0081			418		AEBR	FPR8,FPR1	Add FPR8/FPR1 RRE trappable
0000049C	7080 7004		00000004	419		STE	FPR8,4(,R7)	Store short BFP sum
000004A0	B29C 8004		00000004	420		STFPC	4(R8)	Store resulting FPCR flags and DXC
000004A4	B222 0000			421		IPM	R0	Get condition code and program mask
000004A8	8800 001C		0000001C	422		SRL	R0,28	Isolate CC in low order byte
000004AC	4200 8007		00000007	423		STC	R0,7(,R8)	Save condition code in results table
				424	*			
000004B0	B29D F30C		0000030C	425		LFPC	FPCREGNT	Set exceptions non-trappable
000004B4	7880 3000		00000000	426		LE	FPR8,0(,R3)	Reload short BFP augend
000004B8	ED80 3004 000A		00000004	427		AEB	FPR8,4(,R3)	Add FPR8 by addend RXE non-trappable
000004BE	7080 7008		00000008	428		STE	FPR8,8(,R7)	Store short BFP sum
000004C2	B29C 8008		00000008	429		STFPC	8(R8)	Store resulting FPCR flags and DXC
000004C6	B222 0000			430		IPM	R0	Get condition code and program mask
000004CA	8800 001C		0000001C	431		SRL	R0,28	Isolate CC in low order byte
000004CE	4200 800B		0000000B	432		STC	R0,11(,R8)	Save condition code in results table
				433	*			
000004D2	B29D F310		00000310	434		LFPC	FPCREGTR	Set exceptions trappable
000004D6	7880 3000		00000000	435		LE	FPR8,0(,R3)	Reload short BFP augend
000004DA	ED80 3004 000A		00000004	436		AEB	FPR8,4(,R3)	Add FPR8 by addend RXE trappable
000004E0	7080 700C		0000000C	437		STE	FPR8,12(,R7)	Store short BFP sum
000004E4	B29C 800C		0000000C	438		STFPC	12(R8)	Store resulting FPCR flags and DXC

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000004E8	B222 0000			439	IPM	R0	Get condition code and program mask
000004EC	8800 001C		0000001C	440	SRL	R0,28	Isolate CC in low order byte
000004F0	4200 800F		0000000F	441	STC	R0,15(,R8)	Save condition code in results table
				442 *			
000004F4	4130 3008		00000008	443	LA	R3,2*4(,R3)	Point to next input value pair
000004F8	4170 7010		00000010	444	LA	R7,4*4(,R7)	Point to next sum result set
000004FC	4180 8010		00000010	445	LA	R8,4*4(,R8)	Point to next FPCR result set
00000500	062C			446	BCTR	R2,R12	Convert next input value.
00000502	07FD			447	BR	R13	All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				449 *****
				450 *
				451 * Perform Add using provided short BFP input pairs. This set of
				452 * tests exhaustively tests all rounding modes available for Add.
				453 * The rounding mode can only be specified in the FPC.
				454 *
				455 * All five FPC rounding modes are tested because the preceeding tests,
				456 * using rounding mode RNTE, do not often create results that require
				457 * rounding.
				458 *
				459 * Two results are generated for each input and rounding mode: one RRE
				460 * and one RXE. Traps are disabled for all rounding mode tests.
				461 *
				462 * The sum, FPCR, and condition code are stored for each test.
				463 *
				464 *****
00000504	9823 A000		00000000	466 SBFPRM LM R2,R3,0(R10) Get count and address of test input values
00000508	9878 A008		00000008	467 LM R7,R8,8(R10) Get address of result area and flag area.
0000050C	1222			468 LTR R2,R2 Any test cases?
0000050E	078D			469 BZR R13 ..No, return to caller
00000510	1711			470 XR R1,R1 Zero register 1 for use in IC/STC/indexing
00000512	0DC0			471 BASR R12,0 Set top of test case loop
				472
00000514	4150 0005		00000005	473 LA R5,FPCMCT Get count of FPC modes to be tested
00000518	0D90			474 BASR R9,0 Set top of rounding mode outer loop
				475 *
0000051A	4315 F8C3		000008C3	476 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
				477 *
0000051E	B29D F30C		0000030C	478 LFPC FPCREGNT Set exceptions non-trappable, clear flags
00000522	B2B8 1000		00000000	479 SRNMB 0(R1) Set FPC Rounding Mode
00000526	7880 3000		00000000	480 LE FPR8,0(,R3) Get short BFP augend
0000052A	7810 3004		00000004	481 LE FPR1,4(,R3) Get short BFP addend
0000052E	B30A 0081			482 AEBr FPR8,FPR1 Add RRE FPR8/FPR1 non-trappable
00000532	7080 7000		00000000	483 STE FPR8,0(,R7) Store short BFP sum
00000536	B29C 8000		00000000	484 STFPC 0(R8) Store resulting FPCR flags and DXC
0000053A	B222 0000			485 IPM R0 Get condition code and program mask
0000053E	8800 001C		0000001C	486 SRL R0,28 Isolate CC in low order byte
00000542	4200 8003		00000003	487 STC R0,3(,R8) Save condition code in results table
				488 *
00000546	B29D F30C		0000030C	489 LFPC FPCREGNT Set exceptions non-trappable, clear flags
0000054A	B2B8 1000		00000000	490 SRNMB 0(R1) Set FPC Rounding Mode
0000054E	7880 3000		00000000	491 LE FPR8,0(,R3) Get short BFP augend
00000552	ED80 3004 000A		00000004	492 AEB FPR8,4(,R3) Add RXE FPR8 by addend non-trappable
00000558	7080 7004		00000004	493 STE FPR8,4(,R7) Store short BFP sum
0000055C	B29C 8004		00000004	494 STFPC 4(R8) Store resulting FPCR flags and DXC
00000560	B222 0000			495 IPM R0 Get condition code and program mask
00000564	8800 001C		0000001C	496 SRL R0,28 Isolate CC in low order byte
00000568	4200 8007		00000007	497 STC R0,7(,R8) Save condition code in results table
				498 *
0000056C	4170 7008		00000008	499 LA R7,2*4(,R7) Point to next sum result set
00000570	4180 8008		00000008	500 LA R8,2*4(,R8) Point to next FPCR result area
				501 *
00000574	0659			502 BCTR R5,R9 Iterate to next FPC mode for this input
				503 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				516 *****	
				517 *	
				518 * Perform Add using provided long BFP inputs. This set of tests	
				519 * checks NaN propagation, operations on values that are not finite	
				520 * numbers, and other basic tests. This set generates results that can	
				521 * be validated against Figure 19-13 on page 19-16 of SA22-7832-10.	
				522 *	
				523 * That Figure has separate rows and columns for Normal and Tiny	
				524 * operands. Although the results are effectively the same for Normal	
				525 * and Tiny in any combination, the input data includes Normal and	
				526 * Tiny values.	
				527 *	
				528 * Four results are generated for each input: one RRE with all	
				529 * exceptions non-trappable, a second RRE with all exceptions trappable,	
				530 * a third RXE with all exceptions non-trappable, a fourth RXE with all	
				531 * exceptions trappable,	
				532 *	
				533 * The sum, FPCR, and condition code are stored for each result.	
				534 *	
				535 *****	
00000586				537 LBFPNF DS 0H	BFP long non-finite values tests
00000586	9823 A000		00000000	538 LM R2,R3,0(R10)	Get count and address of augend values
0000058A	9878 A008		00000008	539 LM R7,R8,8(R10)	Get address of result area and flag area.
0000058E	1222			540 LTR R2,R2	Any test cases?
00000590	078D			541 BZR R13	..No, return to caller
00000592	0DC0			542 BASR R12,0	Set top of loop
				543 *	
00000594	9845 A000		00000000	544 LM R4,R5,0(R10)	Get count and start of addend values
				545 *	..which are the same as the augends
00000598	0D60			546 BASR R6,0	Set top of inner loop
				547 *	
0000059A	6880 3000		00000000	548 LD FPR8,0(,R3)	Get long BFP augend
0000059E	6810 5000		00000000	549 LD FPR1,0(,R5)	Get long BFP addend
000005A2	B29D F30C		0000030C	550 LFPC FPCREGNT	Set exceptions non-trappable
000005A6	B31A 0081			551 ADBR FPR8,FPR1	Add FPR0/FPR1 RRE
000005AA	6080 7000		00000000	552 STD FPR8,0(,R7)	Store long BFP sum
000005AE	B29C 8000		00000000	553 STFPC 0(R8)	Store resulting FPCR flags and DXC
000005B2	B222 0000			554 IPM R0	Get condition code and program mask
000005B6	8800 001C		0000001C	555 SRL R0,28	Isolate CC in low order byte
000005BA	4200 8003		00000003	556 STC R0,3(,R8)	Save condition code in results table
				557 *	
000005BE	6880 3000		00000000	558 LD FPR8,0(,R3)	Get long BFP augend
000005C2	6810 5000		00000000	559 LD FPR1,0(,R5)	Get long BFP addend
000005C6	B29D F310		00000310	560 LFPC FPCREGTR	Set exceptions trappable
000005CA	B31A 0081			561 ADBR FPR8,FPR1	Add FPR0/FPR1 RRE
000005CE	6080 7008		00000008	562 STD FPR8,8(,R7)	Store long BFP remainder
000005D2	B29C 8004		00000004	563 STFPC 4(R8)	Store resulting FPCR flags and DXC
000005D6	B222 0000			564 IPM R0	Get condition code and program mask
000005DA	8800 001C		0000001C	565 SRL R0,28	Isolate CC in low order byte
000005DE	4200 8007		00000007	566 STC R0,7(,R8)	Save condition code in results table
				567 *	
000005E2	6880 3000		00000000	568 LD FPR8,0(,R3)	Get long BFP augend
000005E6	B29D F30C		0000030C	569 LFPC FPCREGNT	Set exceptions non-trappable
000005EA	ED80 5000 001A		00000000	570 ADB FPR8,0(,R5)	Add FPR0/FPR1 RXE

LOC	OBJECT CODE	ADDR1	ADDR2	STMT				
				595	*****			
				596	*			
				597	* Perform Add using provided long BFP input pairs. This set of			
				598	* tests triggers IEEE exceptions Overflow, Underflow, and Inexact and			
				599	* collects non-trap and trap results.			
				600	*			
				601	* Four results are generated for each input: one RRE with all			
				602	* exceptions non-trappable, a second RRE with all exceptions trappable,			
				603	* a third RXE with all exceptions non-trappable, a fourth RXE with all			
				604	* exceptions trappable,			
				605	*			
				606	* The sum, FPCR, and condition code are stored for each result.			
				607	*			
				608	*****			
0000063C	9823 A000		00000000	610	LBFPF	LM	R2,R3,0(R10)	Get count and address of test input values
00000640	9878 A008		00000008	611		LM	R7,R8,8(R10)	Get address of result area and flag area.
00000644	1222			612		LTR	R2,R2	Any test cases?
00000646	078D			613		BZR	R13	..No, return to caller
00000648	0DC0			614		BASR	R12,0	Set top of loop
				615	*			
0000064A	B29D F30C		0000030C	616		LFPC	FPCREGNT	Set exceptions non-trappable
0000064E	6880 3000		00000000	617		LD	FPR8,0(,R3)	Get short BFP augend
00000652	6810 3008		00000008	618		LD	FPR1,8(,R3)	Get short BFP addend
00000656	B31A 0081			619		ADBR	FPR8,FPR1	Add FPR8/FPR1 RRE non-trappable
0000065A	6080 7000		00000000	620		STD	FPR8,0(,R7)	Store short BFP sum
0000065E	B29C 8000		00000000	621		STFPC	0(R8)	Store resulting FPCR flags and DXC
00000662	B222 0000			622		IPM	R0	Get condition code and program mask
00000666	8800 001C		0000001C	623		SRL	R0,28	Isolate CC in low order byte
0000066A	4200 8003		00000003	624		STC	R0,3(,R8)	Save condition code in results table
				625	*			
0000066E	B29D F310		00000310	626		LFPC	FPCREGTR	Set exceptions trappable
00000672	6880 3000		00000000	627		LD	FPR8,0(,R3)	Reload short BFP augend
				628	*			
00000676	B31A 0081			629		ADBR	FPR8,FPR1	Add FPR8/FPR1 RRE trappable
0000067A	6080 7008		00000008	630		STD	FPR8,8(,R7)	Store short BFP sum
0000067E	B29C 8004		00000004	631		STFPC	4(R8)	Store resulting FPCR flags and DXC
00000682	B222 0000			632		IPM	R0	Get condition code and program mask
00000686	8800 001C		0000001C	633		SRL	R0,28	Isolate CC in low order byte
0000068A	4200 8007		00000007	634		STC	R0,7(,R8)	Save condition code in results table
				635	*			
0000068E	B29D F30C		0000030C	636		LFPC	FPCREGNT	Set exceptions non-trappable
00000692	6880 3000		00000000	637		LD	FPR8,0(,R3)	Reload short BFP augend
00000696	ED80 3008 001A		00000008	638		ADB	FPR8,8(,R3)	Add FPR8/FPR1 RXE non-trappable
0000069C	6080 7010		00000010	639		STD	FPR8,16(,R7)	Store short BFP sum
000006A0	B29C 8008		00000008	640		STFPC	8(R8)	Store resulting FPCR flags and DXC
000006A4	B222 0000			641		IPM	R0	Get condition code and program mask
000006A8	8800 001C		0000001C	642		SRL	R0,28	Isolate CC in low order byte
000006AC	4200 800B		0000000B	643		STC	R0,11(,R8)	Save condition code in results table
				644	*			
000006B0	B29D F310		00000310	645		LFPC	FPCREGTR	Set exceptions trappable
000006B4	6880 3000		00000000	646		LD	FPR8,0(,R3)	Reload short BFP augend
000006B8	ED80 3008 001A		00000008	647		ADB	FPR8,8(,R3)	Add FPR8/FPR1 RXE trappable
000006BE	6080 7018		00000018	648		STD	FPR8,24(,R7)	Store short BFP sum
000006C2	B29C 800C		0000000C	649		STFPC	12(R8)	Store resulting FPCR flags and DXC

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000006C6	B222 0000			650	IPM	R0	Get condition code and program mask
000006CA	8800 001C		0000001C	651	SRL	R0,28	Isolate CC in low order byte
000006CE	4200 800F		0000000F	652	STC	R0,15(,R8)	Save condition code in results table
				653 *			
000006D2	4130 3010		00000010	654	LA	R3,2*8(,R3)	Point to next input value pair
000006D6	4170 7020		00000020	655	LA	R7,4*8(,R7)	Point to next quotient result pair
000006DA	4180 8010		00000010	656	LA	R8,4*4(,R8)	Point to next FPCR result area
000006DE	062C			657	BCTR	R2,R12	Convert next input value.
000006E0	07FD			658	BR	R13	All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				660 *****
				661 *
				662 * Perform Add using provided long BFP input pairs. This set of
				663 * tests exhaustively tests all rounding modes available for Add.
				664 * The rounding mode can only be specified in the FPC.
				665 *
				666 * All five FPC rounding modes are tested because the preceeding tests,
				667 * using rounding mode RNTE, do not often create results that require
				668 * rounding.
				669 *
				670 * Two results are generated for each input and rounding mode: one RRE
				671 * and one RXE. Traps are disabled for all rounding mode tests.
				672 *
				673 * The sum, FPCR, and condition code are stored for each result.
				674 *
				675 *****
000006E2	9823 A000		00000000	677 LBFPRM LM R2,R3,0(R10) Get count and address of test input values
000006E6	9878 A008		00000008	678 LM R7,R8,8(R10) Get address of result area and flag area.
000006EA	1222			679 LTR R2,R2 Any test cases?
000006EC	078D			680 BZR R13 ..No, return to caller
000006EE	1711			681 XR R1,R1 Zero register 1 for use in IC/STC/indexing
000006F0	0DC0			682 BASR R12,0 Set top of test case loop
				683
000006F2	4150 0005		00000005	684 LA R5,FPCMCT Get count of FPC modes to be tested
000006F6	0D90			685 BASR R9,0 Set top of rounding mode loop
				686 *
000006F8	4315 F8C3		000008C3	687 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
				688 *
000006FC	B29D F30C		0000030C	689 LFPC FPCREGNT Set exceptions non-trappable, clear flags
00000700	B2B8 1000		00000000	690 SRNMB 0(R1) Set FPC Rounding Mode
00000704	6880 3000		00000000	691 LD FPR8,0(,R3) Get long BFP augend
00000708	6810 3008		00000008	692 LD FPR1,8(,R3) Get long BFP addend
0000070C	B31A 0081			693 ADBR FPR8,FPR1 Add RRE FPR8/FPR1 non-trappable
00000710	6080 7000		00000000	694 STD FPR8,0(,R7) Store long BFP sum
00000714	B29C 8000		00000000	695 STFPC 0(R8) Store resulting FPCR flags and DXC
00000718	B222 0000			696 IPM R0 Get condition code and program mask
0000071C	8800 001C		0000001C	697 SRL R0,28 Isolate CC in low order byte
00000720	4200 8003		00000003	698 STC R0,3(,R8) Save condition code in results table
				699 *
00000724	B29D F30C		0000030C	700 LFPC FPCREGNT Set exceptions non-trappable, clear flags
00000728	B2B8 1000		00000000	701 SRNMB 0(R1) Set FPC Rounding Mode
0000072C	6880 3000		00000000	702 LD FPR8,0(,R3) Reload long BFP augend
00000730	ED80 3008 001A		00000008	703 ADB FPR8,8(,R3) Add RXE FPR8 by addend non-trappable
00000736	6080 7008		00000008	704 STD FPR8,8(,R7) Store long BFP sum
0000073A	B29C 8004		00000004	705 STFPC 4(R8) Store resulting FPCR flags and DXC
0000073E	B222 0000			706 IPM R0 Get condition code and program mask
00000742	8800 001C		0000001C	707 SRL R0,28 Isolate CC in low order byte
00000746	4200 8007		00000007	708 STC R0,7(,R8) Save condition code in results table
				709 *
0000074A	4170 7010		00000010	710 LA R7,2*8(,R7) Point to next sum result set
0000074E	4180 8008		00000008	711 LA R8,2*4(,R8) Point to next FPCR result area
				712 *
00000752	0659			713 BCTR R5,R9 Iterate to next FPC mode
				714 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				726 *****	
				727 *	
				728 * Perform Add using provided extended BFP inputs. This set of tests	
				729 * checks NaN propagation, operations on values that are not finite	
				730 * numbers, and other basic tests. This set generates results that can	
				731 * be validated against Figure 19-13 on page 19-16 of SA22-7832-10.	
				732 *	
				733 * That Figure has separate rows and columns for Normal and Tiny	
				734 * operands. Although the results are effectively the same for Normal	
				735 * and Tiny in any combination, the input data includes Normal and	
				736 * Tiny values.	
				737 *	
				738 * Two results are generated for each input: one RRE with all	
				739 * exceptions non-trappable, and a second RRE with all exceptions	
				740 * trappable. Extended BFP Add does not have an RXE format.	
				741 *	
				742 * The sum, FPCR, and condition code are stored for each result.	
				743 *	
				744 *****	
00000760				746 XBFPNF DS 0H	BFP extended non-finite values tests
00000760	9823 A000		00000000	747 LM R2,R3,0(R10)	Get count and address of augend values
00000764	9878 A008		00000008	748 LM R7,R8,8(R10)	Get address of result area and flag area.
00000768	1222			749 LTR R2,R2	Any test cases?
0000076A	078D			750 BZR R13	..No, return to caller
0000076C	0DC0			751 BASR R12,0	Set top of loop
				752 *	
0000076E	9845 A000		00000000	753 LM R4,R5,0(R10)	Get count and start of addend values
				754 *	..which are the same as the augends
00000772	0D60			755 BASR R6,0	Set top of inner loop
				756 *	
00000774	6880 3000		00000000	757 LD FPR8,0(,R3)	Get extended BFP augend part 1
00000778	68A0 3008		00000008	758 LD FPR10,8(,R3)	Get extended BFP augend part 2
0000077C	6810 5000		00000000	759 LD FPR1,0(,R5)	Get extended BFP addend part 1
00000780	6830 5008		00000008	760 LD FPR3,8(,R5)	Get extended BFP addend part 2
00000784	B29D F30C		0000030C	761 LFPC FPCREGNT	Set exceptions non-trappable
00000788	B34A 0081			762 AXBR FPR8,FPR1	Add FPR0/FPR1 RRE
0000078C	6080 7000		00000000	763 STD FPR8,0(,R7)	Store extended BFP sum part 1
00000790	60A0 7008		00000008	764 STD FPR10,8(,R7)	Store extended BFP sum part 2
00000794	B29C 8000		00000000	765 STFPC 0(R8)	Store resulting FPCR flags and DXC
00000798	B222 0000			766 IPM R0	Get condition code and program mask
0000079C	8800 001C		0000001C	767 SRL R0,28	Isolate CC in low order byte
000007A0	4200 8003		00000003	768 STC R0,3(,R8)	Save condition code in results table
				769 *	
000007A4	6880 3000		00000000	770 LD FPR8,0(,R3)	Get extended BFP augend part 1
000007A8	68A0 3008		00000008	771 LD FPR10,8(,R3)	Get extended BFP augend part 2
000007AC	6810 5000		00000000	772 LD FPR1,0(,R5)	Get extended BFP addend part 1
000007B0	6830 5008		00000008	773 LD FPR3,8(,R5)	Get extended BFP addend part 2
000007B4	B29D F310		00000310	774 LFPC FPCREGTR	Set exceptions trappable
000007B8	B34A 0081			775 AXBR FPR8,FPR1	Add FPR0/FPR1 RRE
000007BC	6080 7010		00000010	776 STD FPR8,16(,R7)	Store extended BFP sum part 1
000007C0	60A0 7018		00000018	777 STD FPR10,24(,R7)	Store extended BFP sum part 2
000007C4	B29C 8004		00000004	778 STFPC 4(R8)	Store resulting FPCR flags and DXC
000007C8	B222 0000			779 IPM R0	Get condition code and program mask
000007CC	8800 001C		0000001C	780 SRL R0,28	Isolate CC in low order byte

LOC	OBJECT CODE	ADDR1	ADDR2	STMT			
000007D0	4200 8007		00000007	781	STC	R0,7(,R8)	Save condition code in results table
				782 *			
000007D4	4150 5010		00000010	783	LA	R5,16(,R5)	Point to next addend value
000007D8	4170 7020		00000020	784	LA	R7,32(,R7)	Point to next Add result area
000007DC	4180 8010		00000010	785	LA	R8,16(,R8)	Point to next Add FPCR area
000007E0	0646			786	BCTR	R4,R6	Loop through right-hand values
				787 *			
000007E2	4130 3010		00000010	788	LA	R3,16(,R3)	Point to next augend value
000007E6	062C			789	BCTR	R2,R12	Add until all cases tested
000007E8	07FD			790	BR	R13	All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				792 *****
				793 *
				794 * Perform Add using provided extended BFP input pairs. This set of
				795 * tests triggers IEEE exceptions Overflow, Underflow, and Inexact and
				796 * collects results when the exceptions do not result in a trap and when
				797 * they do.
				798 *
				799 * Two results are generated for each input: one RRE with all
				800 * exceptions non-trappable and a second RRE with all exceptions
				801 * trappable. There is no RXE format for Add in extended precision.
				802 *
				803 * The sum, FPCR, and condition code are stored for each result.
				804 *
				805 *****
000007EA	9823 A000		00000000	807 XBFPF LM R2,R3,0(R10) Get count and address of test input values
000007EE	9878 A008		00000008	808 LM R7,R8,8(R10) Get address of result area and flag area.
000007F2	1222			809 LTR R2,R2 Any test cases?
000007F4	078D			810 BZR R13 ..No, return to caller
000007F6	0DC0			811 BASR R12,0 Set top of loop
				812 *
000007F8	B29D F30C		0000030C	813 LFPC FPCREGNT Set exceptions non-trappable
000007FC	6880 3000		00000000	814 LD FPR8,0(,R3) Get extended BFP augend part 1
00000800	68A0 3008		00000008	815 LD FPR10,8(,R3) Get extended BFP augend part 2
00000804	6810 3010		00000010	816 LD FPR1,16(,R3) Get extended BFP addend part 1
00000808	6830 3018		00000018	817 LD FPR3,24(,R3) Get extended BFP addend part 2
0000080C	B34A 0081			818 AXBR FPR8,FPR1 Add FPR8-10/FPR1-3 RRE non-trappable
00000810	6080 7000		00000000	819 STD FPR8,0(,R7) Store extended BFP sum part 1
00000814	60A0 7008		00000008	820 STD FPR10,8(,R7) Store extended BFP sum part 2
00000818	B29C 8000		00000000	821 STFPC 0(R8) Store resulting FPCR flags and DXC
0000081C	B222 0000			822 IPM R0 Get condition code and program mask
00000820	8800 001C		0000001C	823 SRL R0,28 Isolate CC in low order byte
00000824	4200 8003		00000003	824 STC R0,3(,R8) Save condition code in results table
				825 *
00000828	B29D F310		00000310	826 LFPC FPCREGTR Set exceptions trappable
0000082C	6880 3000		00000000	827 LD FPR8,0(,R3) Reload extended BFP augend part 1
00000830	68A0 3008		00000008	828 LD FPR10,8(,R3) Reload extended BFP augend part 2
				829 * ..addend is still in FPR1-FPR3
00000834	B34A 0081			830 AXBR FPR8,FPR1 Add FPR8-10/FPR1-3 RRE trappable
00000838	6080 7010		00000010	831 STD FPR8,16(,R7) Store extended BFP sum part 1
0000083C	60A0 7018		00000018	832 STD FPR10,24(,R7) Store extended BFP sum part 2
00000840	B29C 8004		00000004	833 STFPC 4(R8) Store resulting FPCR flags and DXC
00000844	B222 0000			834 IPM R0 Get condition code and program mask
00000848	8800 001C		0000001C	835 SRL R0,28 Isolate CC in low order byte
0000084C	4200 8007		00000007	836 STC R0,7(,R8) Save condition code in results table
				837 *
00000850	4130 3020		00000020	838 LA R3,32(,R3) Point to next input value pair
00000854	4170 7020		00000020	839 LA R7,32(,R7) Point to next quotient result pair
00000858	4180 8010		00000010	840 LA R8,16(,R8) Point to next FPCR result area
0000085C	062C			841 BCTR R2,R12 Convert next input value.
				842 *
0000085E	07FD			843 BR R13 All converted; return.

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				845 *****
				846 *
				847 * Perform Add using provided extended BFP input pairs. This set of
				848 * tests exhaustively tests all rounding modes available for Add.
				849 * The rounding mode can only be specified in the FPC.
				850 *
				851 * All five FPC rounding modes are tested because the preceeding tests,
				852 * using rounding mode RNTE, do not often create results that require
				853 * rounding.
				854 *
				855 * Two results are generated for each input and rounding mode: one RRE
				856 * and one RXE. Traps are disabled for all rounding mode tests.
				857 *
				858 * The sum, FPCR, and condition code are stored for each result.
				859 *
				860 *****
00000860	9823 A000		00000000	862 XBFPRM LM R2,R3,0(R10) Get count and address of test input values
00000864	9878 A008		00000008	863 LM R7,R8,8(R10) Get address of result area and flag area.
00000868	1222			864 LTR R2,R2 Any test cases?
0000086A	078D			865 BZR R13 ..No, return to caller
0000086C	1711			866 XR R1,R1 Zero register 1 for use in IC/STC/indexing
0000086E	0DC0			867 BASR R12,0 Set top of test case loop
				868
00000870	4150 0005		00000005	869 LA R5,FPCMCT Get count of FPC modes to be tested
00000874	0D90			870 BASR R9,0 Set top of rounding mode loop
				871 *
00000876	4315 F8C3		000008C3	872 IC R1,FPCMODES-L'FPCMODES(R5) Get next FPC mode
				873 *
0000087A	B29D F30C		0000030C	874 LFPC FPCREGNT Set exceptions non-trappable, clear flags
0000087E	B2B8 1000		00000000	875 SRNMB 0(R1) Set FPC Rounding Mode
00000882	6880 3000		00000000	876 LD FPR8,0(,R3) Get extended BFP augend part 1
00000886	68A0 3008		00000008	877 LD FPR10,8(,R3) Get extended BFP augend part 2
0000088A	6810 3010		00000010	878 LD FPR1,16(,R3) Get extended BFP addend part 1
0000088E	6830 3018		00000018	879 LD FPR3,24(,R3) Get extended BFP addend part 2
00000892	B34A 0081			880 AXBR FPR8,FPR1 Add RRE FPR8/FPR1 non-trappable
00000896	6080 7000		00000000	881 STD FPR8,0(,R7) Store extended BFP sum part 1
0000089A	60A0 7008		00000008	882 STD FPR10,8(,R7) Store extended BFP sum part 2
0000089E	B29C 8000		00000000	883 STFPC 0(R8) Store resulting FPCR flags and DXC
000008A2	B222 0000			884 IPM R0 Get condition code and program mask
000008A6	8800 001C		0000001C	885 SRL R0,28 Isolate CC in low order byte
000008AA	4200 8003		00000003	886 STC R0,3(,R8) Save condition code in results table
				887 *
000008AE	4170 7010		00000010	888 LA R7,16(,R7) Point to next sum result set
000008B2	4180 8004		00000004	889 LA R8,4(,R8) Point to next FPCR result area
				890 *
000008B6	0659			891 BCTR R5,R9 Iterate to next FPC mode
				892 *
				893 * End of FPC modes to be tested. Advance to next test case. We will
				894 * skip eight bytes of FPCR result area so that each set of five result
				895 * FPCR contents pairs starts at a memory address ending in zero for the
				896 * convenience of memory dump review.
				897 *
000008B8	4130 3020		00000020	898 LA R3,2*16(,R3) Point to next input value pair
000008BC	4180 800C		0000000C	899 LA R8,12(,R8) Skip to start of next FPCR result area

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
					904 *****
					905 *
					906 * Table of FPC rounding modes to test sum rounding modes.
					907 *
					908 * The Set BFP Rounding Mode does allow specification of the FPC
					909 * rounding mode as an address, so we shall index into a table of
					910 * BFP rounding modes without bothering with Execute.
					911 *
					912 *****
					914 *
					915 * Rounding modes that may be set in the FPCR. The FPCR controls
					916 * rounding of the sum.
					917 *
					918 * These are indexed directly by the loop counter, which counts down.
					919 * So the modes are listed in reverse order here.
					920 *
000008C4					921 FPCMODES DS 0C
000008C4	07				922 DC AL1(7) RFS, Round for shorter precision
000008C5	03				923 DC AL1(3) RM, Round to -infinity
000008C6	02				924 DC AL1(2) RP, Round to +infinity
000008C7	01				925 DC AL1(1) RZ, Round to zero
000008C8	00				926 DC AL1(0) RNTE, Round to Nearest, ties to even
			00000005	00000001	927 FPCMCT EQU *-FPCMODES Count of FPC Modes to be tested
					928 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				930 *****
				931 *
				932 * Short BFP test data sets for Add testing.
				933 *
				934 * The first test data set is used for tests of basic functionality,
				935 * NaN propagation, and results from operations involving other than
				936 * finite numbers.
				937 *
				938 * The second test data set is used for testing boundary conditions
				939 * using two finite non-zero values. Each possible condition code
				940 * and type of result (normal, scaled, etc) is created by members of
				941 * this test data set.
				942 *
				943 * The third test data set is used for exhaustive testing of final
				944 * results across the five rounding modes available for the Add
				945 * instruction.
				946 *
				947 *****
				949 *****
				950 *
				951 * First input test data set, to test operations using non-finite or
				952 * zero inputs. Member values chosen to validate Figure 19-13 on page
				953 * 19-16 of SA22-7832-10. Each value in this table is tested against
				954 * every other value in the table. Ten entries means 100 result sets.
				955 *
				956 *****
000008CC				958 SBFPNFIN DS 0F Inputs for short BFP non-finite tests
000008CC	FF800000			959 DC X'FF800000' -inf
000008D0	C0000000			960 DC X'C0000000' -2.0
000008D4	80010000			961 DC X'80010000' -Dnice
000008D8	80000000			962 DC X'80000000' -0
000008DC	00000000			963 DC X'00000000' +0
000008E0	00010000			964 DC X'00010000' -Dnice
000008E4	40000000			965 DC X'40000000' +2.0
000008E8	7F800000			966 DC X'7F800000' +inf
000008EC	FFCB0000			967 DC X'FFCB0000' -QNaN
000008F0	7F8A0000			968 DC X'7F8A0000' +SNaN
	0000000A	00000001		969 SBFPNFCT EQU (*-SBFPNFIN)/4 Count of short BFP in list
				971 *****
				972 *
				973 * Second input test data set. These are finite pairs intended to
				974 * trigger overflow, underflow, and inexact exceptions. Each pair is
				975 * added twice, once non-trappable and once trappable. Trappable
				976 * overflow or underflow yields a scaled result. Trappable inexact
				977 * will show whether the Incremented DXC code is returned.
				978 *
				979 * The following test cases are required:
				980 * 1. Overflow
				981 * 2. Underflow - normal inputs

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				982 * 3. Underflow - subnormal inputs
				983 * 4. Normal - from subnormal inputs
				984 * 5. Inexact - incremented
				985 * 6. Inexact - truncated
				986 *
				987 *****
000008F4				989 SBFPIN DS 0F Inputs for short BFP finite tests
				990 *
				991 * Overflow on addition
				992 *
000008F4	7F7FFFFFFF			993 DC X'7F7FFFFFFF' +Nmax
000008F8	7F7FFFFFFF			994 DC X'7F7FFFFFFF' +Nmax
				995 *
				996 * Underflow from sum of normals. We will add a small normal to a
				997 * negative smaller normal to generate a subnormal.
				998 *
000008FC	00FFFFFFF			999 DC X'00FFFFFFF' Very small normal number
00000900	80800000			1000 DC X'80800000' Smaller normal negative
				1001 *
				1002 * Underflow from sum of subnormals. We will add two subnormals.
				1003 *
00000904	00040000			1004 DC X'00040000' Subnormal, < +Dmax
00000908	00000F0F			1005 DC X'00000F0F' Smaller subnormal
				1006 *
				1007 * Normal result from sum of subnormals. We will add two subnormals.
				1008 * The result will be greater than +Nmin
				1009 *
0000090C	007FFFFFFF			1010 DC X'007FFFFFFF' +Dmax
00000910	00000001			1011 DC X'00000001' +Dmin, result will be +Nmin
				1012 *
				1013 * Add a value to 1.0 such that the added digits are to the right of
				1014 * the right-most bit in the stored significand. The result will be
				1015 * inexact, and incremented will be determined by the value of the
				1016 * bits in the addend.
				1017 *
00000914	3F800000			1018 DC X'3F800000' Augend +1, aka 1.0b0
00000918	33F80000			1019 DC X'33F80000' Addend 1.1111b-24
				1020 * ..Above addend is 1.154839992523193359375E-7
				1021 * ..nearest is away from zero, incremented.
				1022 *
0000091C	3F800000			1023 DC X'3F800000' Augend +1, aka 1.0b0
00000920	33780000			1024 DC X'33780000' Addend 1.1111b-25
				1025 * ..Above addend is 5.774199962615966796875E-8
				1026 * ..nearest is toward zero, truncated
				1027 *
	00000006	00000001		1028 SBFPCT EQU (*-SBFPIN)/4/2 Count of short BFP in list
				1030 *****
				1031 *
				1032 * Third input test data set. These are finite pairs intended to
				1033 * test all combinations of rounding mode for the sum and the
				1034 * remainder. Values are chosen to create a requirement to round

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1035 * to the target precision after the computation and to generate
				1036 * varying results depending on the rounding mode in the FPCR.
				1037 *
				1038 * The result set will have cases that represent each of the following
				1039 *
				1040 * 1. Positive, nearest magnitude is toward zero.
				1041 * 2. Negative, nearest magnitude is toward zero.
				1042 * 3. Positive, nearest magnitude is away from zero.
				1043 * 4. Negative, nearest magnitude is away from zero.
				1044 * 5. Positive, tie, nearest even has greater magnitude
				1045 * 6. Negative, tie, nearest even has greater magnitude
				1046 * 7. Positive, tie, nearest even has lower magnitude
				1047 * 8. Negative, tie, nearest even has lower magnitude
				1048 *
				1049 * Round For Shorter precision correctness can be determined from the
				1050 * above test cases.
				1051 *
				1052 *****
00000924				1054 SBFPINRM DS 0F Inputs for short BFP rounding testing
				1055 *
				1056 * Add a value to 1.0 such that the added digits are to the right of
				1057 * the right-most bit in the stored significand. The result will be
				1058 * inexact, and incremented will be determined by the value of the
				1059 * bits in the addend.
				1060 *
00000924	3F800000			1061 DC X'3F800000' Augend +1, aka +1.0b0
00000928	337E0000			1062 DC X'337E0000' Addend +1.111111b-25
0000092C	BF800000			1063 DC X'BF800000' Augend -1, aka -1.0b0
00000930	B37E0000			1064 DC X'B37E0000' Addend -1.111111b-25
				1065 * ..Above addend is 5.91389834880828857421875E-8
				1066 * ..nearest is toward zero, truncated
				1067 *
00000934	3F800000			1068 DC X'3F800000' Augend +1, aka 1.0b0
00000938	33FF0000			1069 DC X'33FF0000' Addend +1.111111b-24
0000093C	BF800000			1070 DC X'BF800000' Augend -1, aka -1.0b0
00000940	B3FF0000			1071 DC X'B3FF0000' Addend -1.111111b-24
				1072 * ..Above addend is 1.187436282634735107421875E-7
				1073 * ..nearest is away from zero, incremented.
				1074 *
00000944	3F800000			1075 DC X'3F800000' Augend +1, aka +1.0b0
00000948	33800000			1076 DC X'33800000' Addend +1.0b-24
0000094C	BF800000			1077 DC X'BF800000' Augend -1, aka -1.0b0
00000950	B3800000			1078 DC X'B3800000' Addend -1.0b-24
				1079 * ..Above addend is 5.9604644775390625E-8
				1080 * ..nearest is a tie, nearest even has lower magnitude
				1081 *
00000954	3F800000			1082 DC X'3F800000' Augend +1, aka +1.0b0
00000958	34400000			1083 DC X'34400000' Addend +1.1b-23
0000095C	BF800000			1084 DC X'BF800000' Augend -1, aka -1.0b0
00000960	B4400000			1085 DC X'B4400000' Addend -1.1b-23
				1086 * ..Above addend is 1.78813934326171875E-7
				1087 * ..nearest is a tie, nearest even has greater magnitude
	00000008	00000001		1088 SBFPRMCT EQU (*-SBFPINRM)/4/2 Count of short BFP rounding tests

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1090 *****
				1091 *
				1092 * Long BFP test data sets for Add testing.
				1093 *
				1094 * The first test data set is used for tests of basic functionality,
				1095 * NaN propagation, and results from operations involving other than
				1096 * finite numbers.
				1097 *
				1098 * The second test data set is used for testing boundary conditions
				1099 * using two finite non-zero values. Each possible condition code
				1100 * and type of result (normal, scaled, etc) is created by members of
				1101 * this test data set.
				1102 *
				1103 * The third test data set is used for exhaustive testing of final
				1104 * results across the five rounding modes available for the Add
				1105 * instruction.
				1106 *
				1107 *****
				1109 *****
				1110 *
				1111 * First input test data set, to test operations using non-finite or
				1112 * zero inputs. Member values chosen to validate Figure 19-13 on page
				1113 * 19-16 of SA22-7832-10. Each value in this table is tested against
				1114 * every other value in the table. Ten entries means 100 result sets.
				1115 *
				1116 *****
00000964				1118 LBFPNFIN DS 0F Inputs for long BFP testing
00000964	FFF00000	00000000		1119 DC X'FFF0000000000000' -inf
0000096C	C0000000	00000000		1120 DC X'C000000000000000' -2.0
00000974	80010000	00000000		1121 DC X'8001000000000000' -Dnice
0000097C	80000000	00000000		1122 DC X'8000000000000000' -0
00000984	00000000	00000000		1123 DC X'0000000000000000' +0
0000098C	00010000	00000000		1124 DC X'0001000000000000' +Dnice
00000994	40000000	00000000		1125 DC X'4000000000000000' +2.0
0000099C	7FF00000	00000000		1126 DC X'7FF0000000000000' +inf
000009A4	FFF8B000	00000000		1127 DC X'FFF8B00000000000' -QNaN
000009AC	7FF0A000	00000000		1128 DC X'7FF0A00000000000' +SNaN
	0000000A	00000001		1129 LBFPNFCT EQU (*-LBFPNFIN)/8 Count of long BFP in list
				1131 *****
				1132 *
				1133 * Second input test data set. These are finite pairs intended to
				1134 * trigger overflow, underflow, and inexact exceptions. Each pair is
				1135 * added twice, once non-trappable and once trappable. Trappable
				1136 * overflow or underflow yields a scaled result. Trappable inexact
				1137 * will show whether the Incremented DXC code is returned.
				1138 *
				1139 * The following test cases are required:
				1140 * 1. Overflow
				1141 * 2. Underflow - normal inputs

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1142 * 3. Underflow - subnormal inputs
				1143 * 4. Normal - from subnormal inputs
				1144 * 5. Inexact - incremented
				1145 * 6. Inexact - truncated
				1146 *
				1147 *****
000009B8				1149 LBFPIN DS 0D Inputs for long BFP finite tests
				1150 *
				1151 * Overflow on addition
				1152 *
000009B8	7FFFFFFF FFFFFFFF			1153 DC X'7FFFFFFF' +Nmax
000009C0	7FFFFFFF FFFFFFFF			1154 DC X'7FFFFFFF' +Nmax
				1155 *
				1156 * Underflow from sum of normals. We will add a small normal to a
				1157 * negative smaller normal to generate a subnormal.
				1158 *
000009C8	001FFFFFF FFFFFFFF			1159 DC X'001FFFFFF' Very small normal number
000009D0	80100000 00000000			1160 DC X'80100000' Smaller normal negative
				1161 *
				1162 * Underflow from sum of subnormals. We will add two subnormals.
				1163 *
000009D8	00080000 00000000			1164 DC X'00080000' Subnormal, < +Dmax
000009E0	0000F0F0 00000000			1165 DC X'0000F0F0' Smaller subnormal
				1166 *
				1167 * Normal result from sum of subnormals. We will add two subnormals.
				1168 * The result will be greater than +Nmin
				1169 *
000009E8	000FFFFFF FFFFFFFF			1170 DC X'000FFFFFF' +Dmax
000009F0	00000000 00000001			1171 DC X'0000000000000001' +Dmin, result will be +Nmin
				1172 *
				1173 * Add a value to 1.0 such that the added digits are to the right of
				1174 * the right-most bit in the stored significand. The result will be
				1175 * inexact, and incremented will be determined by the value of the
				1176 * bits in the addend.
				1177 *
000009F8	3FF00000 00000000			1178 DC X'3FF00000' Augend +1, aka 1.0b0
00000A00	3CAF0000 00000000			1179 DC X'3CAF0000' Addend 1.1111b-53
				1180 * ..Above addend is 2.15105711021124079707078635692596435546875E-16
				1181 * ..nearest is away from zero, incremented.
				1182 *
00000A08	3FF00000 00000000			1183 DC X'3FF00000' Augend +1, aka 1.0b0
00000A10	3C9F0000 00000000			1184 DC X'3C9F0000' Addend 1.1111b-54
				1185 * ..Above addend is 1.075528555105620398535393178462982177734375E-16
				1186 * ..nearest is toward zero, truncated.
				1187 *
	00000006	00000001		1188 LBFPCT EQU (*-LBFPIN)/8/2 Count of long BFP in list
				1190 *****
				1191 *
				1192 * Third input test data set. These are finite pairs intended to
				1193 * test all combinations of rounding mode for the sum and the
				1194 * remainder. Values are chosen to create a requirement to round

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1195 * to the target precision after the computation and to generate
				1196 * varying results depending on the rounding mode in the FPCR.
				1197 *
				1198 * The result set will have cases that represent each of the following
				1199 *
				1200 * 1. Positive, nearest magnitude is toward zero.
				1201 * 2. Negative, nearest magnitude is toward zero.
				1202 * 3. Positive, nearest magnitude is away from zero.
				1203 * 4. Negative, nearest magnitude is away from zero.
				1204 * 5. Positive, tie, nearest even has greater magnitude
				1205 * 6. Negative, tie, nearest even has greater magnitude
				1206 * 7. Positive, tie, nearest even has lower magnitude
				1207 * 8. Negative, tie, nearest even has lower magnitude
				1208 *
				1209 * Round For Shorter precision correctness can be determined from the
				1210 * above test cases.
				1211 *
				1212 *****
00000A18				1214 LBFPINRM DS 0F
				1215 *
				1216 * Add a value to 1.0 such that the added digits are to the right of
				1217 * the right-most bit in the stored significand. The result will be
				1218 * inexact, and incremented will be determined by the value of the
				1219 * bits in the addend.
				1220 *
00000A18	3FF00000	00000000		1221 DC X'3FF0000000000000' Augend +1, aka +1.0b0
00000A20	3C9FC000	00000000		1222 DC X'3C9FC00000000000' Addend +1.111111b-54
00000A28	BFF00000	00000000		1223 DC X'BFF0000000000000' Augend -1, aka -1.0b0
00000A30	BC9FC000	00000000		1224 DC X'BC9FC00000000000' Addend -1.111111b-54
				1225 * ..Above addend is 1.10154940724527250495157204568386077880859375E-16
				1226 * ..nearest is toward zero, truncated.
				1227 *
00000A38	3FF00000	00000000		1228 DC X'3FF0000000000000' Augend +1, aka +1.0b0
00000A40	3CAFE000	00000000		1229 DC X'3CAFE00000000000' Addend +1.111111b-53
00000A48	BFF00000	00000000		1230 DC X'BFF0000000000000' Augend -1, aka -1.0b0
00000A50	BCAFE000	00000000		1231 DC X'BCAFE00000000000' Addend -1.111111b-53
				1232 * ..Above addend is 2.21177243187042904537520371377468109130859375E-16
				1233 * ..nearest is away from zero, incremented.
				1234 *
00000A58	3FF00000	00000000		1235 DC X'3FF0000000000000' Augend +1, aka +1.0b0
00000A60	3CA00000	00000000		1236 DC X'3CA0000000000000' Addend +1.0b-53
00000A68	BFF00000	00000000		1237 DC X'BFF0000000000000' Augend -1, aka -1.0b0
00000A70	BCA00000	00000000		1238 DC X'BCA0000000000000' Addend -1.0b-53
				1239 * ..Above addend is 1.1102230246251565404236316680908203125E-16
				1240 * ..nearest is a tie, nearest even has lower magnitude
				1241 *
00000A78	3FF00000	00000000		1242 DC X'3FF0000000000000' Augend +1, aka +1.0b0
00000A80	3CB80000	00000000		1243 DC X'3CB8000000000000' Addend +1.1b-52
00000A88	BFF00000	00000000		1244 DC X'BFF0000000000000' Augend -1, aka -1.0b0
00000A90	BCB80000	00000000		1245 DC X'BCB8000000000000' Addend -1.1b-52
				1246 * ..Above addend is 3.3306690738754696212708950042724609375E-16
				1247 * ..nearest is a tie, nearest even has greater magnitude
				1248 *
				1249 *

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
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00000008 00000001 1250 LBFPRMCT EQU    (*-LBFPINRM)/8/2  Count of long BFP rounding tests
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LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1252 *****
				1253 *
				1254 * Extended BFP test data sets for Add testing.
				1255 *
				1256 * The first test data set is used for tests of basic functionality,
				1257 * NaN propagation, and results from operations involving other than
				1258 * finite numbers.
				1259 *
				1260 * The second test data set is used for testing boundary conditions
				1261 * using two finite non-zero values. Each possible condition code
				1262 * and type of result (normal, scaled, etc) is created by members of
				1263 * this test data set.
				1264 *
				1265 * The third test data set is used for exhaustive testing of final
				1266 * results across the five rounding modes available for the Add
				1267 * instruction.
				1268 *
				1269 *****
				1271 *****
				1272 *
				1273 * First input test data set, to test operations using non-finite or
				1274 * zero inputs. Member values chosen to validate Figure 19-13 on page
				1275 * 19-16 of SA22-7832-10. Each value in this table is tested against
				1276 * every other value in the table. Ten entries means 100 result sets.
				1277 *
				1278 *****
00000A98				1280 XBFPNFIN DS 0F Inputs for extended BFP testing
00000A98	FFFF0000	00000000		1281 DC X'FFFF0000000000000000000000000000' -inf
00000AA8	C0000000	00000000		1282 DC X'C0000000000000000000000000000000' -2.0
00000AB8	80001000	00000000		1283 DC X'80001000000000000000000000000000' -Dnice
00000AC8	80000000	00000000		1284 DC X'80000000000000000000000000000000' -0
00000AD8	00000000	00000000		1285 DC X'00000000000000000000000000000000' +0
00000AE8	00001000	00000000		1286 DC X'00001000000000000000000000000000' +Dnice
00000AF8	40000000	00000000		1287 DC X'40000000000000000000000000000000' +2.0
00000B08	7FFF0000	00000000		1288 DC X'7FFF0000000000000000000000000000' +inf
00000B18	FFFF8B00	00000000		1289 DC X'FFFF8B00000000000000000000000000' -QNaN
00000B28	7FFF0A00	00000000		1290 DC X'7FFF0A00000000000000000000000000' +SNaN
	0000000A	00000001		1291 XBFPNFCT EQU (*-XBFPNFIN)/16 Count of extended BFP in list
				1293 *****
				1294 *
				1295 * Second input test data set. These are finite pairs intended to
				1296 * trigger overflow, underflow, and inexact exceptions. Each pair is
				1297 * added twice, once non-trappable and once trappable. Trappable
				1298 * overflow or underflow yields a scaled result. Trappable inexact
				1299 * will show whether the Incremented DXC code is returned.
				1300 *
				1301 * The following test cases are required:
				1302 * The following test cases are required:
				1303 * 1. Overflow

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1304 * 2. Underflow - normal inputs
				1305 * 3. Underflow - subnormal inputs
				1306 * 4. Normal - from subnormal inputs
				1307 * 5. Inexact - incremented
				1308 * 6. Inexact - truncated
				1309 *
				1310 *****
00000B38				1312 XBFPIN DS 0F Inputs for extended BFP finite tests
				1313 *
				1314 * Overflow on addition
				1315 *
00000B38	7FFFFFFF FFFFFFFF			1316 DC X'7FFFFFFF' +Nmax
00000B48	7FFFFFFF FFFFFFFF			1317 DC X'7FFFFFFF' +Nmax
				1318 *
				1319 * Underflow from sum of normals. We will add a small normal to a
				1320 * negative smaller normal to generate a subnormal.
				1321 *
00000B58	0001FFFF FFFFFFFF			1322 DC X'0001FFFF' Very small normal
00000B68	80010000 00000000			1323 DC X'80010000' Smaller normal
				1324 *
				1325 * Underflow from sum of subnormals. We will add two subnormals.
				1326 *
00000B78	00008000 00000000			1327 DC X'00008000' Subnormal, < +Dmax
00000B88	00000F0F 00000000			1328 DC X'00000F0F' Smaller subnormal
				1329 *
				1330 * Normal result from sum of subnormals. We will add two subnormals.
				1331 * The result will be greater than +Nmin
				1332 *
00000B98	0000FFFF FFFFFFFF			1333 DC X'0000FFFF' +Dmax
00000BA8	00000000 00000000			1334 DC X'00000000' +Dmin
				1335 * ...result will be +Nmin
				1336 *
				1337 * Add a value to 1.0 such that the added digits are to the right of
				1338 * the right-most bit in the stored significand. The result will be
				1339 * inexact, and incremented will be determined by the value of the
				1340 * bits in the addend.
				1341 *
00000BB8	3FFF0000 00000000			1342 DC X'3FFF0000' +1, aka 1.0b0
00000BC8	3F8EF000 00000000			1343 DC X'3F8EF000' 1.1111b-113
				1344 * ..Above addend is 1.865744633625134732647978631879148339833785...
				1345 * ...97170865731413869070820510387420654296875E-34
				1346 * ..nearest is away from zero, incremented.
				1347 *
00000BD8	3FFF0000 00000000			1348 DC X'3FFF0000' +1, aka 1.0b0
00000BE8	3F8DF000 00000000			1349 DC X'3F8DF000' 1.1111b-114
				1350 * ..Above addend is 9.328723168125673663239893159395741699168929...
				1351 * ...85854328657069345354102551937103271484375E-35
				1352 * ..nearest is toward zero, truncated
				1353 *
	00000006 00000001			1354 XBFPCT EQU (*-XBFPIN)/16/2 Count of extended BFP in list
				1356 *****

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
				1421 *****	
				1422 *	ACTUAL results saved here
				1423 *****	
				1424 *	
				1425 *	Locations for ACTUAL results
				1426 *	
				1427 *	
		00001000	00000001	1428 SBFPNFOT EQU	Integer short non-finite BFP results
				1429 *	..room for 110 tests, 100 used
		00001700	00000001	1430 SBFPNFFL EQU	FPCR flags and DXC from short BFP
				1431 *	..room for 110 tests, 100 used
				1432 *	
		00001E00	00000001	1433 SBFPOUT EQU	Integer short BFP finite results
				1434 *	..room for 16 tests, 6 used
		00001F00	00000001	1435 SBFPFLGS EQU	FPCR flags and DXC from short BFP
				1436 *	..room for 16 tests, 6 used
				1437 *	
		00002000	00000001	1438 SBFPRMO EQU	Short BFP rounding mode test results
				1439 *	..Room for 16, 8 used.
		00002300	00000001	1440 SBFPRMOF EQU	Short BFP rounding mode FPCR results
				1441 *	..Room for 16, 8 used.
				1442 *	..next location starts at X'2500'
				1443 *	
		00004000	00000001	1444 LBFPNFOT EQU	Integer long non-finite BFP results
				1445 *	..room for 100 tests, 100 used
		00004D00	00000001	1446 LBFPNFFL EQU	FPCR flags and DXC from long BFP
				1447 *	..room for 100 tests, 100 used
				1448 *	
		00005400	00000001	1449 LBFPOUT EQU	Integer long BFP finite results
				1450 *	..room for 16 tests, 6 used
		00005600	00000001	1451 LBFPFLGS EQU	FPCR flags and DXC from long BFP
				1452 *	..room for 16 tests, 6 used
				1453 *	
		00005700	00000001	1454 LBFPRMO EQU	Long BFP rounding mode test results
				1455 *	..Room for 16, 8 used.
		00005C00	00000001	1456 LBFPRMOF EQU	Long BFP rounding mode FPCR results
				1457 *	..Room for 16, 8 used.
				1458 *	..next location starts at X'5E00'
				1459 *	
		00008000	00000001	1460 XBFPNFOT EQU	Integer ext'd non-finite BFP results
				1461 *	..room for 100 tests, 100 used
		00008D00	00000001	1462 XBFPNFFL EQU	FPCR flags and DXC from ext'd BFP
				1463 *	..room for 100 tests, 100 used
				1464 *	
		00009400	00000001	1465 XBFPOUT EQU	Extended BFP finite results
				1466 *	..room for 16 tests, 6 used
		00009600	00000001	1467 XBFPFLGS EQU	FPCR flags and DXC from ext'd BFP
				1468 *	..room for 16 tests, 6 used
				1469 *	
		00009700	00000001	1470 XBFPRMO EQU	Ext'd BFP rounding mode test results
				1471 *	..Room for 16, 8 used.
		00009C00	00000001	1472 XBFPRMOF EQU	Ext'd BFP rounding mode FPCR results
				1473 *	..Room for 16, 8 used.
				1474 *	..next location starts at X'9E00'
				1475 *	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				1477 *****
				1478 * EXPECTED results
				1479 *****
				1480 *
00000CF8		00000CF8	0000A000	1481 ORG STRTLABL+X'A000' (past end of actual results)
				1482 *
		0000A000	00000001	1483 SBFPNFOT_GOOD EQU *
0000A000	C1C5C2D9	61C1C5C2		1484 DC CL48'AEBR/AEB NF -inf/-inf'
0000A030	FF800000	FF800000		1485 DC XL16'FF800000FF800000FF800000FF800000'
0000A040	C1C5C2D9	61C1C5C2		1486 DC CL48'AEBR/AEB NF -inf/-2.0'
0000A070	FF800000	FF800000		1487 DC XL16'FF800000FF800000FF800000FF800000'
0000A080	C1C5C2D9	61C1C5C2		1488 DC CL48'AEBR/AEB NF -inf/-Dnice'
0000A0B0	FF800000	FF800000		1489 DC XL16'FF800000FF800000FF800000FF800000'
0000A0C0	C1C5C2D9	61C1C5C2		1490 DC CL48'AEBR/AEB NF -inf/-0'
0000A0F0	FF800000	FF800000		1491 DC XL16'FF800000FF800000FF800000FF800000'
0000A100	C1C5C2D9	61C1C5C2		1492 DC CL48'AEBR/AEB NF -inf/+0'
0000A130	FF800000	FF800000		1493 DC XL16'FF800000FF800000FF800000FF800000'
0000A140	C1C5C2D9	61C1C5C2		1494 DC CL48'AEBR/AEB NF -inf/+Dnice'
0000A170	FF800000	FF800000		1495 DC XL16'FF800000FF800000FF800000FF800000'
0000A180	C1C5C2D9	61C1C5C2		1496 DC CL48'AEBR/AEB NF -inf/+2.0'
0000A1B0	FF800000	FF800000		1497 DC XL16'FF800000FF800000FF800000FF800000'
0000A1C0	C1C5C2D9	61C1C5C2		1498 DC CL48'AEBR/AEB NF -inf/+inf'
0000A1F0	7FC00000	FF800000		1499 DC XL16'7FC00000FF8000007FC00000FF800000'
0000A200	C1C5C2D9	61C1C5C2		1500 DC CL48'AEBR/AEB NF -inf/-QNaN'
0000A230	FFCB0000	FFCB0000		1501 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000A240	C1C5C2D9	61C1C5C2		1502 DC CL48'AEBR/AEB NF -inf/+SNaN'
0000A270	7FCA0000	FF800000		1503 DC XL16'7FCA0000FF8000007FCA0000FF800000'
0000A280	C1C5C2D9	61C1C5C2		1504 DC CL48'AEBR/AEB NF -2.0/-inf'
0000A2B0	FF800000	FF800000		1505 DC XL16'FF800000FF800000FF800000FF800000'
0000A2C0	C1C5C2D9	61C1C5C2		1506 DC CL48'AEBR/AEB NF -2.0/-2.0'
0000A2F0	C0800000	C0800000		1507 DC XL16'C0800000C0800000C0800000C0800000'
0000A300	C1C5C2D9	61C1C5C2		1508 DC CL48'AEBR/AEB NF -2.0/-Dnice'
0000A330	C0000000	C0000000		1509 DC XL16'C0000000C0000000C0000000C0000000'
0000A340	C1C5C2D9	61C1C5C2		1510 DC CL48'AEBR/AEB NF -2.0/-0'
0000A370	C0000000	C0000000		1511 DC XL16'C0000000C0000000C0000000C0000000'
0000A380	C1C5C2D9	61C1C5C2		1512 DC CL48'AEBR/AEB NF -2.0/+0'
0000A3B0	C0000000	C0000000		1513 DC XL16'C0000000C0000000C0000000C0000000'
0000A3C0	C1C5C2D9	61C1C5C2		1514 DC CL48'AEBR/AEB NF -2.0/+Dnice'
0000A3F0	C0000000	C0000000		1515 DC XL16'C0000000C0000000C0000000C0000000'
0000A400	C1C5C2D9	61C1C5C2		1516 DC CL48'AEBR/AEB NF -2.0/+2.0'
0000A430	00000000	00000000		1517 DC XL16'000000000000000000000000000000'
0000A440	C1C5C2D9	61C1C5C2		1518 DC CL48'AEBR/AEB NF -2.0/+inf'
0000A470	7F800000	7F800000		1519 DC XL16'7F8000007F8000007F8000007F800000'
0000A480	C1C5C2D9	61C1C5C2		1520 DC CL48'AEBR/AEB NF -2.0/-QNaN'
0000A4B0	FFCB0000	FFCB0000		1521 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000A4C0	C1C5C2D9	61C1C5C2		1522 DC CL48'AEBR/AEB NF -2.0/+SNaN'
0000A4F0	7FCA0000	C0000000		1523 DC XL16'7FCA0000C00000007FCA0000C0000000'
0000A500	C1C5C2D9	61C1C5C2		1524 DC CL48'AEBR/AEB NF -Dnice/-inf'
0000A530	FF800000	FF800000		1525 DC XL16'FF800000FF800000FF800000FF800000'
0000A540	C1C5C2D9	61C1C5C2		1526 DC CL48'AEBR/AEB NF -Dnice/-2.0'
0000A570	C0000000	C0000000		1527 DC XL16'C0000000C0000000C0000000C0000000'
0000A580	C1C5C2D9	61C1C5C2		1528 DC CL48'AEBR/AEB NF -Dnice/-Dnice'
0000A5B0	80020000	DD800000		1529 DC XL16'80020000DD80000080020000DD800000'
0000A5C0	C1C5C2D9	61C1C5C2		1530 DC CL48'AEBR/AEB NF -Dnice/-0'
0000A5F0	80010000	DD000000		1531 DC XL16'80010000DD00000080010000DD000000'
0000A600	C1C5C2D9	61C1C5C2		1532 DC CL48'AEBR/AEB NF -Dnice/+0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000A630	80010000 DD000000			1533	DC XL16'80010000DD00000080010000DD000000'
0000A640	C1C5C2D9 61C1C5C2			1534	DC CL48'AEBR/AEB NF -Dnice/+Dnice'
0000A670	00000000 00000000			1535	DC XL16'000000000000000000000000000000'
0000A680	C1C5C2D9 61C1C5C2			1536	DC CL48'AEBR/AEB NF -Dnice/+2.0'
0000A6B0	40000000 40000000			1537	DC XL16'40000000400000004000000040000000'
0000A6C0	C1C5C2D9 61C1C5C2			1538	DC CL48'AEBR/AEB NF -Dnice/+inf'
0000A6F0	7F800000 7F800000			1539	DC XL16'7F8000007F8000007F8000007F800000'
0000A700	C1C5C2D9 61C1C5C2			1540	DC CL48'AEBR/AEB NF -Dnice/-QNaN'
0000A730	FFCB0000 FFCB0000			1541	DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000A740	C1C5C2D9 61C1C5C2			1542	DC CL48'AEBR/AEB NF -Dnice/+SNaN'
0000A770	7FCA0000 80010000			1543	DC XL16'7FCA0000800100007FCA000080010000'
0000A780	C1C5C2D9 61C1C5C2			1544	DC CL48'AEBR/AEB NF -0/-inf'
0000A7B0	FF800000 FF800000			1545	DC XL16'FF800000FF800000FF800000FF800000'
0000A7C0	C1C5C2D9 61C1C5C2			1546	DC CL48'AEBR/AEB NF -0/-2.0'
0000A7F0	C0000000 C0000000			1547	DC XL16'C0000000C0000000C0000000C0000000'
0000A800	C1C5C2D9 61C1C5C2			1548	DC CL48'AEBR/AEB NF -0/-Dnice'
0000A830	80010000 DD000000			1549	DC XL16'80010000DD00000080010000DD000000'
0000A840	C1C5C2D9 61C1C5C2			1550	DC CL48'AEBR/AEB NF -0/-0'
0000A870	80000000 80000000			1551	DC XL16'80000000800000008000000080000000'
0000A880	C1C5C2D9 61C1C5C2			1552	DC CL48'AEBR/AEB NF -0/+0'
0000A8B0	00000000 00000000			1553	DC XL16'000000000000000000000000000000'
0000A8C0	C1C5C2D9 61C1C5C2			1554	DC CL48'AEBR/AEB NF -0/+Dnice'
0000A8F0	00010000 5D000000			1555	DC XL16'000100005D000000000100005D000000'
0000A900	C1C5C2D9 61C1C5C2			1556	DC CL48'AEBR/AEB NF -0/+2.0'
0000A930	40000000 40000000			1557	DC XL16'40000000400000004000000040000000'
0000A940	C1C5C2D9 61C1C5C2			1558	DC CL48'AEBR/AEB NF -0/+inf'
0000A970	7F800000 7F800000			1559	DC XL16'7F8000007F8000007F8000007F800000'
0000A980	C1C5C2D9 61C1C5C2			1560	DC CL48'AEBR/AEB NF -0/-QNaN'
0000A9B0	FFCB0000 FFCB0000			1561	DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000A9C0	C1C5C2D9 61C1C5C2			1562	DC CL48'AEBR/AEB NF -0/+SNaN'
0000A9F0	7FCA0000 80000000			1563	DC XL16'7FCA0000800000007FCA000080000000'
0000AA00	C1C5C2D9 61C1C5C2			1564	DC CL48'AEBR/AEB NF +0/-inf'
0000AA30	FF800000 FF800000			1565	DC XL16'FF800000FF800000FF800000FF800000'
0000AA40	C1C5C2D9 61C1C5C2			1566	DC CL48'AEBR/AEB NF +0/-2.0'
0000AA70	C0000000 C0000000			1567	DC XL16'C0000000C0000000C0000000C0000000'
0000AA80	C1C5C2D9 61C1C5C2			1568	DC CL48'AEBR/AEB NF +0/-Dnice'
0000AAB0	80010000 DD000000			1569	DC XL16'80010000DD00000080010000DD000000'
0000AAC0	C1C5C2D9 61C1C5C2			1570	DC CL48'AEBR/AEB NF +0/-0'
0000AAF0	00000000 00000000			1571	DC XL16'000000000000000000000000000000'
0000AB00	C1C5C2D9 61C1C5C2			1572	DC CL48'AEBR/AEB NF +0/+0'
0000AB30	00000000 00000000			1573	DC XL16'000000000000000000000000000000'
0000AB40	C1C5C2D9 61C1C5C2			1574	DC CL48'AEBR/AEB NF +0/+Dnice'
0000AB70	00010000 5D000000			1575	DC XL16'000100005D000000000100005D000000'
0000AB80	C1C5C2D9 61C1C5C2			1576	DC CL48'AEBR/AEB NF +0/+2.0'
0000ABB0	40000000 40000000			1577	DC XL16'40000000400000004000000040000000'
0000ABC0	C1C5C2D9 61C1C5C2			1578	DC CL48'AEBR/AEB NF +0/+inf'
0000ABF0	7F800000 7F800000			1579	DC XL16'7F8000007F8000007F8000007F800000'
0000AC00	C1C5C2D9 61C1C5C2			1580	DC CL48'AEBR/AEB NF +0/-QNaN'
0000AC30	FFCB0000 FFCB0000			1581	DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000AC40	C1C5C2D9 61C1C5C2			1582	DC CL48'AEBR/AEB NF +0/+SNaN'
0000AC70	7FCA0000 00000000			1583	DC XL16'7FCA0000000000007FCA000000000000'
0000AC80	C1C5C2D9 61C1C5C2			1584	DC CL48'AEBR/AEB NF +Dnice/-inf'
0000ACB0	FF800000 FF800000			1585	DC XL16'FF800000FF800000FF800000FF800000'
0000ACC0	C1C5C2D9 61C1C5C2			1586	DC CL48'AEBR/AEB NF +Dnice/-2.0'
0000ACF0	C0000000 C0000000			1587	DC XL16'C0000000C0000000C0000000C0000000'
0000AD00	C1C5C2D9 61C1C5C2			1588	DC CL48'AEBR/AEB NF +Dnice/-Dnice'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000B430	FFCB0000 FFCB0000			1645 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B440	C1C5C2D9 61C1C5C2			1646 DC CL48'AEBR/AEB NF -QNaN/-2.0'
0000B470	FFCB0000 FFCB0000			1647 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B480	C1C5C2D9 61C1C5C2			1648 DC CL48'AEBR/AEB NF -QNaN/-Dnice'
0000B4B0	FFCB0000 FFCB0000			1649 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B4C0	C1C5C2D9 61C1C5C2			1650 DC CL48'AEBR/AEB NF -QNaN/-0'
0000B4F0	FFCB0000 FFCB0000			1651 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B500	C1C5C2D9 61C1C5C2			1652 DC CL48'AEBR/AEB NF -QNaN/+0'
0000B530	FFCB0000 FFCB0000			1653 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B540	C1C5C2D9 61C1C5C2			1654 DC CL48'AEBR/AEB NF -QNaN/+Dnice'
0000B570	FFCB0000 FFCB0000			1655 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B580	C1C5C2D9 61C1C5C2			1656 DC CL48'AEBR/AEB NF -QNaN/+2.0'
0000B5B0	FFCB0000 FFCB0000			1657 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B5C0	C1C5C2D9 61C1C5C2			1658 DC CL48'AEBR/AEB NF -QNaN/+inf'
0000B5F0	FFCB0000 FFCB0000			1659 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B600	C1C5C2D9 61C1C5C2			1660 DC CL48'AEBR/AEB NF -QNaN/-QNaN'
0000B630	FFCB0000 FFCB0000			1661 DC XL16'FFCB0000FFCB0000FFCB0000FFCB0000'
0000B640	C1C5C2D9 61C1C5C2			1662 DC CL48'AEBR/AEB NF -QNaN/+SNaN'
0000B670	7FCA0000 FFCB0000			1663 DC XL16'7FCA0000FFCB00007FCA0000FFCB0000'
0000B680	C1C5C2D9 61C1C5C2			1664 DC CL48'AEBR/AEB NF +SNaN/-inf'
0000B6B0	7FCA0000 7F8A0000			1665 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B6C0	C1C5C2D9 61C1C5C2			1666 DC CL48'AEBR/AEB NF +SNaN/-2.0'
0000B6F0	7FCA0000 7F8A0000			1667 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B700	C1C5C2D9 61C1C5C2			1668 DC CL48'AEBR/AEB NF +SNaN/-Dnice'
0000B730	7FCA0000 7F8A0000			1669 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B740	C1C5C2D9 61C1C5C2			1670 DC CL48'AEBR/AEB NF +SNaN/-0'
0000B770	7FCA0000 7F8A0000			1671 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B780	C1C5C2D9 61C1C5C2			1672 DC CL48'AEBR/AEB NF +SNaN/+0'
0000B7B0	7FCA0000 7F8A0000			1673 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B7C0	C1C5C2D9 61C1C5C2			1674 DC CL48'AEBR/AEB NF +SNaN/+Dnice'
0000B7F0	7FCA0000 7F8A0000			1675 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B800	C1C5C2D9 61C1C5C2			1676 DC CL48'AEBR/AEB NF +SNaN/+2.0'
0000B830	7FCA0000 7F8A0000			1677 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B840	C1C5C2D9 61C1C5C2			1678 DC CL48'AEBR/AEB NF +SNaN/+inf'
0000B870	7FCA0000 7F8A0000			1679 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B880	C1C5C2D9 61C1C5C2			1680 DC CL48'AEBR/AEB NF +SNaN/-QNaN'
0000B8B0	7FCA0000 7F8A0000			1681 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
0000B8C0	C1C5C2D9 61C1C5C2			1682 DC CL48'AEBR/AEB NF +SNaN/+SNaN'
0000B8F0	7FCA0000 7F8A0000			1683 DC XL16'7FCA00007F8A00007FCA00007F8A0000'
		00000064	00000001	1684 SBFPNFOT_NUM EQU (*-SBFPNFOT_GOOD)/64
				1685 *
				1686 *
		0000B900	00000001	1687 SBFPNFFL_GOOD EQU *
0000B900	C1C5C2D9 61C1C5C2			1688 DC CL48'AEBR/AEB NF -inf/-inf FPCR'
0000B930	00000001 F8000001			1689 DC XL16'00000001F800000100000001F8000001'
0000B940	C1C5C2D9 61C1C5C2			1690 DC CL48'AEBR/AEB NF -inf/-2.0 FPCR'
0000B970	00000001 F8000001			1691 DC XL16'00000001F800000100000001F8000001'
0000B980	C1C5C2D9 61C1C5C2			1692 DC CL48'AEBR/AEB NF -inf/-Dnice FPCR'
0000B9B0	00000001 F8000001			1693 DC XL16'00000001F800000100000001F8000001'
0000B9C0	C1C5C2D9 61C1C5C2			1694 DC CL48'AEBR/AEB NF -inf/-0 FPCR'
0000B9F0	00000001 F8000001			1695 DC XL16'00000001F800000100000001F8000001'
0000BA00	C1C5C2D9 61C1C5C2			1696 DC CL48'AEBR/AEB NF -inf/+0 FPCR'
0000BA30	00000001 F8000001			1697 DC XL16'00000001F800000100000001F8000001'
0000BA40	C1C5C2D9 61C1C5C2			1698 DC CL48'AEBR/AEB NF -inf/+Dnice FPCR'
0000BA70	00000001 F8000001			1699 DC XL16'00000001F800000100000001F8000001'
0000BA80	C1C5C2D9 61C1C5C2			1700 DC CL48'AEBR/AEB NF -inf/+2.0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000BAB0	00000001 F8000001			1701	DC XL16'00000001F800000100000001F8000001'
0000BAC0	C1C5C2D9 61C1C5C2			1702	DC CL48'AEBR/AEB NF -inf/+inf FPCR'
0000BAF0	00800003 F8008003			1703	DC XL16'00800003F800800300800003F8008003'
0000BB00	C1C5C2D9 61C1C5C2			1704	DC CL48'AEBR/AEB NF -inf/-QNaN FPCR'
0000BB30	00000003 F8000003			1705	DC XL16'00000003F800000300000003F8000003'
0000BB40	C1C5C2D9 61C1C5C2			1706	DC CL48'AEBR/AEB NF -inf/+SNaN FPCR'
0000BB70	00800003 F8008003			1707	DC XL16'00800003F800800300800003F8008003'
0000BB80	C1C5C2D9 61C1C5C2			1708	DC CL48'AEBR/AEB NF -2.0/-inf FPCR'
0000BBB0	00000001 F8000001			1709	DC XL16'00000001F800000100000001F8000001'
0000BBC0	C1C5C2D9 61C1C5C2			1710	DC CL48'AEBR/AEB NF -2.0/-2.0 FPCR'
0000BBF0	00000001 F8000001			1711	DC XL16'00000001F800000100000001F8000001'
0000BC00	C1C5C2D9 61C1C5C2			1712	DC CL48'AEBR/AEB NF -2.0/-Dnice FPCR'
0000BC30	00080001 F8000801			1713	DC XL16'00080001F800080100080001F8000801'
0000BC40	C1C5C2D9 61C1C5C2			1714	DC CL48'AEBR/AEB NF -2.0/-0 FPCR'
0000BC70	00000001 F8000001			1715	DC XL16'00000001F800000100000001F8000001'
0000BC80	C1C5C2D9 61C1C5C2			1716	DC CL48'AEBR/AEB NF -2.0/+0 FPCR'
0000BCB0	00000001 F8000001			1717	DC XL16'00000001F800000100000001F8000001'
0000BCC0	C1C5C2D9 61C1C5C2			1718	DC CL48'AEBR/AEB NF -2.0/+Dnice FPCR'
0000BCF0	00080001 F8000C01			1719	DC XL16'00080001F8000C0100080001F8000C01'
0000BD00	C1C5C2D9 61C1C5C2			1720	DC CL48'AEBR/AEB NF -2.0/+2.0 FPCR'
0000BD30	00000000 F8000000			1721	DC XL16'00000000F800000000000000F8000000'
0000BD40	C1C5C2D9 61C1C5C2			1722	DC CL48'AEBR/AEB NF -2.0/+inf FPCR'
0000BD70	00000002 F8000002			1723	DC XL16'00000002F800000200000002F8000002'
0000BD80	C1C5C2D9 61C1C5C2			1724	DC CL48'AEBR/AEB NF -2.0/-QNaN FPCR'
0000BDB0	00000003 F8000003			1725	DC XL16'00000003F800000300000003F8000003'
0000BDC0	C1C5C2D9 61C1C5C2			1726	DC CL48'AEBR/AEB NF -2.0/+SNaN FPCR'
0000BDF0	00800003 F8008003			1727	DC XL16'00800003F800800300800003F8008003'
0000BE00	C1C5C2D9 61C1C5C2			1728	DC CL48'AEBR/AEB NF -Dnice/-inf FPCR'
0000BE30	00000001 F8000001			1729	DC XL16'00000001F800000100000001F8000001'
0000BE40	C1C5C2D9 61C1C5C2			1730	DC CL48'AEBR/AEB NF -Dnice/-2.0 FPCR'
0000BE70	00080001 F8000801			1731	DC XL16'00080001F800080100080001F8000801'
0000BE80	C1C5C2D9 61C1C5C2			1732	DC CL48'AEBR/AEB NF -Dnice/-Dnice FPCR'
0000BEB0	00000001 F8001001			1733	DC XL16'00000001F800100100000001F8001001'
0000BEC0	C1C5C2D9 61C1C5C2			1734	DC CL48'AEBR/AEB NF -Dnice/-0 FPCR'
0000BEF0	00000001 F8001001			1735	DC XL16'00000001F800100100000001F8001001'
0000BF00	C1C5C2D9 61C1C5C2			1736	DC CL48'AEBR/AEB NF -Dnice/+0 FPCR'
0000BF30	00000001 F8001001			1737	DC XL16'00000001F800100100000001F8001001'
0000BF40	C1C5C2D9 61C1C5C2			1738	DC CL48'AEBR/AEB NF -Dnice/+Dnice FPCR'
0000BF70	00000000 F8000000			1739	DC XL16'00000000F800000000000000F8000000'
0000BF80	C1C5C2D9 61C1C5C2			1740	DC CL48'AEBR/AEB NF -Dnice/+2.0 FPCR'
0000BFB0	00080002 F8000C02			1741	DC XL16'00080002F8000C0200080002F8000C02'
0000BFC0	C1C5C2D9 61C1C5C2			1742	DC CL48'AEBR/AEB NF -Dnice/+inf FPCR'
0000BFF0	00000002 F8000002			1743	DC XL16'00000002F800000200000002F8000002'
0000C000	C1C5C2D9 61C1C5C2			1744	DC CL48'AEBR/AEB NF -Dnice/-QNaN FPCR'
0000C030	00000003 F8000003			1745	DC XL16'00000003F800000300000003F8000003'
0000C040	C1C5C2D9 61C1C5C2			1746	DC CL48'AEBR/AEB NF -Dnice/+SNaN FPCR'
0000C070	00800003 F8008003			1747	DC XL16'00800003F800800300800003F8008003'
0000C080	C1C5C2D9 61C1C5C2			1748	DC CL48'AEBR/AEB NF -0/-inf FPCR'
0000C0B0	00000001 F8000001			1749	DC XL16'00000001F800000100000001F8000001'
0000C0C0	C1C5C2D9 61C1C5C2			1750	DC CL48'AEBR/AEB NF -0/-2.0 FPCR'
0000C0F0	00000001 F8000001			1751	DC XL16'00000001F800000100000001F8000001'
0000C100	C1C5C2D9 61C1C5C2			1752	DC CL48'AEBR/AEB NF -0/-Dnice FPCR'
0000C130	00000001 F8001001			1753	DC XL16'00000001F800100100000001F8001001'
0000C140	C1C5C2D9 61C1C5C2			1754	DC CL48'AEBR/AEB NF -0/-0 FPCR'
0000C170	00000000 F8000000			1755	DC XL16'00000000F800000000000000F8000000'
0000C180	C1C5C2D9 61C1C5C2			1756	DC CL48'AEBR/AEB NF -0/+0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000C1B0	00000000 F8000000			1757	DC XL16'00000000F800000000000000F8000000'
0000C1C0	C1C5C2D9 61C1C5C2			1758	DC CL48'AEBR/AEB NF -0/+Dnice FPCR'
0000C1F0	00000002 F8001002			1759	DC XL16'00000002F800100200000002F8001002'
0000C200	C1C5C2D9 61C1C5C2			1760	DC CL48'AEBR/AEB NF -0/+2.0 FPCR'
0000C230	00000002 F8000002			1761	DC XL16'00000002F800000200000002F8000002'
0000C240	C1C5C2D9 61C1C5C2			1762	DC CL48'AEBR/AEB NF -0/+inf FPCR'
0000C270	00000002 F8000002			1763	DC XL16'00000002F800000200000002F8000002'
0000C280	C1C5C2D9 61C1C5C2			1764	DC CL48'AEBR/AEB NF -0/-QNaN FPCR'
0000C2B0	00000003 F8000003			1765	DC XL16'00000003F800000300000003F8000003'
0000C2C0	C1C5C2D9 61C1C5C2			1766	DC CL48'AEBR/AEB NF -0/+SNaN FPCR'
0000C2F0	00800003 F8008003			1767	DC XL16'00800003F800800300800003F8008003'
0000C300	C1C5C2D9 61C1C5C2			1768	DC CL48'AEBR/AEB NF +0/-inf FPCR'
0000C330	00000001 F8000001			1769	DC XL16'00000001F800000100000001F8000001'
0000C340	C1C5C2D9 61C1C5C2			1770	DC CL48'AEBR/AEB NF +0/-2.0 FPCR'
0000C370	00000001 F8000001			1771	DC XL16'00000001F800000100000001F8000001'
0000C380	C1C5C2D9 61C1C5C2			1772	DC CL48'AEBR/AEB NF +0/-Dnice FPCR'
0000C3B0	00000001 F8001001			1773	DC XL16'00000001F800100100000001F8001001'
0000C3C0	C1C5C2D9 61C1C5C2			1774	DC CL48'AEBR/AEB NF +0/-0 FPCR'
0000C3F0	00000000 F8000000			1775	DC XL16'00000000F800000000000000F8000000'
0000C400	C1C5C2D9 61C1C5C2			1776	DC CL48'AEBR/AEB NF +0/+0 FPCR'
0000C430	00000000 F8000000			1777	DC XL16'00000000F800000000000000F8000000'
0000C440	C1C5C2D9 61C1C5C2			1778	DC CL48'AEBR/AEB NF +0/+Dnice FPCR'
0000C470	00000002 F8001002			1779	DC XL16'00000002F800100200000002F8001002'
0000C480	C1C5C2D9 61C1C5C2			1780	DC CL48'AEBR/AEB NF +0/+2.0 FPCR'
0000C4B0	00000002 F8000002			1781	DC XL16'00000002F800000200000002F8000002'
0000C4C0	C1C5C2D9 61C1C5C2			1782	DC CL48'AEBR/AEB NF +0/+inf FPCR'
0000C4F0	00000002 F8000002			1783	DC XL16'00000002F800000200000002F8000002'
0000C500	C1C5C2D9 61C1C5C2			1784	DC CL48'AEBR/AEB NF +0/-QNaN FPCR'
0000C530	00000003 F8000003			1785	DC XL16'00000003F800000300000003F8000003'
0000C540	C1C5C2D9 61C1C5C2			1786	DC CL48'AEBR/AEB NF +0/+SNaN FPCR'
0000C570	00800003 F8008003			1787	DC XL16'00800003F800800300800003F8008003'
0000C580	C1C5C2D9 61C1C5C2			1788	DC CL48'AEBR/AEB NF +Dnice/-inf FPCR'
0000C5B0	00000001 F8000001			1789	DC XL16'00000001F800000100000001F8000001'
0000C5C0	C1C5C2D9 61C1C5C2			1790	DC CL48'AEBR/AEB NF +Dnice/-2.0 FPCR'
0000C5F0	00080001 F8000C01			1791	DC XL16'00080001F8000C0100080001F8000C01'
0000C600	C1C5C2D9 61C1C5C2			1792	DC CL48'AEBR/AEB NF +Dnice/-Dnice FPCR'
0000C630	00000000 F8000000			1793	DC XL16'00000000F800000000000000F8000000'
0000C640	C1C5C2D9 61C1C5C2			1794	DC CL48'AEBR/AEB NF +Dnice/-0 FPCR'
0000C670	00000002 F8001002			1795	DC XL16'00000002F800100200000002F8001002'
0000C680	C1C5C2D9 61C1C5C2			1796	DC CL48'AEBR/AEB NF +Dnice/+0 FPCR'
0000C6B0	00000002 F8001002			1797	DC XL16'00000002F800100200000002F8001002'
0000C6C0	C1C5C2D9 61C1C5C2			1798	DC CL48'AEBR/AEB NF +Dnice/+Dnice FPCR'
0000C6F0	00000002 F8001002			1799	DC XL16'00000002F800100200000002F8001002'
0000C700	C1C5C2D9 61C1C5C2			1800	DC CL48'AEBR/AEB NF +Dnice/+2.0 FPCR'
0000C730	00080002 F8000802			1801	DC XL16'00080002F800080200080002F8000802'
0000C740	C1C5C2D9 61C1C5C2			1802	DC CL48'AEBR/AEB NF +Dnice/+inf FPCR'
0000C770	00000002 F8000002			1803	DC XL16'00000002F800000200000002F8000002'
0000C780	C1C5C2D9 61C1C5C2			1804	DC CL48'AEBR/AEB NF +Dnice/-QNaN FPCR'
0000C7B0	00000003 F8000003			1805	DC XL16'00000003F800000300000003F8000003'
0000C7C0	C1C5C2D9 61C1C5C2			1806	DC CL48'AEBR/AEB NF +Dnice/+SNaN FPCR'
0000C7F0	00800003 F8008003			1807	DC XL16'00800003F800800300800003F8008003'
0000C800	C1C5C2D9 61C1C5C2			1808	DC CL48'AEBR/AEB NF +2.0/-inf FPCR'
0000C830	00000001 F8000001			1809	DC XL16'00000001F800000100000001F8000001'
0000C840	C1C5C2D9 61C1C5C2			1810	DC CL48'AEBR/AEB NF +2.0/-2.0 FPCR'
0000C870	00000000 F8000000			1811	DC XL16'00000000F800000000000000F8000000'
0000C880	C1C5C2D9 61C1C5C2			1812	DC CL48'AEBR/AEB NF +2.0/-Dnice FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000C8B0	00080002 F8000C02			1813	DC XL16'00080002F8000C0200080002F8000C02'
0000C8C0	C1C5C2D9 61C1C5C2			1814	DC CL48'AEBR/AEB NF +2.0/-0 FPCR'
0000C8F0	00000002 F8000002			1815	DC XL16'00000002F800000200000002F8000002'
0000C900	C1C5C2D9 61C1C5C2			1816	DC CL48'AEBR/AEB NF +2.0/+0 FPCR'
0000C930	00000002 F8000002			1817	DC XL16'00000002F800000200000002F8000002'
0000C940	C1C5C2D9 61C1C5C2			1818	DC CL48'AEBR/AEB NF +2.0/+Dnice FPCR'
0000C970	00080002 F8000802			1819	DC XL16'00080002F800080200080002F8000802'
0000C980	C1C5C2D9 61C1C5C2			1820	DC CL48'AEBR/AEB NF +2.0/+2.0 FPCR'
0000C9B0	00000002 F8000002			1821	DC XL16'00000002F800000200000002F8000002'
0000C9C0	C1C5C2D9 61C1C5C2			1822	DC CL48'AEBR/AEB NF +2.0/+inf FPCR'
0000C9F0	00000002 F8000002			1823	DC XL16'00000002F800000200000002F8000002'
0000CA00	C1C5C2D9 61C1C5C2			1824	DC CL48'AEBR/AEB NF +2.0/-QNaN FPCR'
0000CA30	00000003 F8000003			1825	DC XL16'00000003F800000300000003F8000003'
0000CA40	C1C5C2D9 61C1C5C2			1826	DC CL48'AEBR/AEB NF +2.0/+SNaN FPCR'
0000CA70	00800003 F8008003			1827	DC XL16'00800003F800800300800003F8008003'
0000CA80	C1C5C2D9 61C1C5C2			1828	DC CL48'AEBR/AEB NF +inf/-inf FPCR'
0000CAB0	00800003 F8008003			1829	DC XL16'00800003F800800300800003F8008003'
0000CAC0	C1C5C2D9 61C1C5C2			1830	DC CL48'AEBR/AEB NF +inf/-2.0 FPCR'
0000CAF0	00000002 F8000002			1831	DC XL16'00000002F800000200000002F8000002'
0000CB00	C1C5C2D9 61C1C5C2			1832	DC CL48'AEBR/AEB NF +inf/-Dnice FPCR'
0000CB30	00000002 F8000002			1833	DC XL16'00000002F800000200000002F8000002'
0000CB40	C1C5C2D9 61C1C5C2			1834	DC CL48'AEBR/AEB NF +inf/-0 FPCR'
0000CB70	00000002 F8000002			1835	DC XL16'00000002F800000200000002F8000002'
0000CB80	C1C5C2D9 61C1C5C2			1836	DC CL48'AEBR/AEB NF +inf/+0 FPCR'
0000CBB0	00000002 F8000002			1837	DC XL16'00000002F800000200000002F8000002'
0000CBC0	C1C5C2D9 61C1C5C2			1838	DC CL48'AEBR/AEB NF +inf/+Dnice FPCR'
0000CBF0	00000002 F8000002			1839	DC XL16'00000002F800000200000002F8000002'
0000CC00	C1C5C2D9 61C1C5C2			1840	DC CL48'AEBR/AEB NF +inf/+2.0 FPCR'
0000CC30	00000002 F8000002			1841	DC XL16'00000002F800000200000002F8000002'
0000CC40	C1C5C2D9 61C1C5C2			1842	DC CL48'AEBR/AEB NF +inf/+inf FPCR'
0000CC70	00000002 F8000002			1843	DC XL16'00000002F800000200000002F8000002'
0000CC80	C1C5C2D9 61C1C5C2			1844	DC CL48'AEBR/AEB NF +inf/-QNaN FPCR'
0000CCB0	00000003 F8000003			1845	DC XL16'00000003F800000300000003F8000003'
0000CCC0	C1C5C2D9 61C1C5C2			1846	DC CL48'AEBR/AEB NF +inf/+SNaN FPCR'
0000CCF0	00800003 F8008003			1847	DC XL16'00800003F800800300800003F8008003'
0000CD00	C1C5C2D9 61C1C5C2			1848	DC CL48'AEBR/AEB NF -QNaN/-inf FPCR'
0000CD30	00000003 F8000003			1849	DC XL16'00000003F800000300000003F8000003'
0000CD40	C1C5C2D9 61C1C5C2			1850	DC CL48'AEBR/AEB NF -QNaN/-2.0 FPCR'
0000CD70	00000003 F8000003			1851	DC XL16'00000003F800000300000003F8000003'
0000CD80	C1C5C2D9 61C1C5C2			1852	DC CL48'AEBR/AEB NF -QNaN/-Dnice FPCR'
0000CDB0	00000003 F8000003			1853	DC XL16'00000003F800000300000003F8000003'
0000CDC0	C1C5C2D9 61C1C5C2			1854	DC CL48'AEBR/AEB NF -QNaN/-0 FPCR'
0000CDF0	00000003 F8000003			1855	DC XL16'00000003F800000300000003F8000003'
0000CE00	C1C5C2D9 61C1C5C2			1856	DC CL48'AEBR/AEB NF -QNaN/+0 FPCR'
0000CE30	00000003 F8000003			1857	DC XL16'00000003F800000300000003F8000003'
0000CE40	C1C5C2D9 61C1C5C2			1858	DC CL48'AEBR/AEB NF -QNaN/+Dnice FPCR'
0000CE70	00000003 F8000003			1859	DC XL16'00000003F800000300000003F8000003'
0000CE80	C1C5C2D9 61C1C5C2			1860	DC CL48'AEBR/AEB NF -QNaN/+2.0 FPCR'
0000CEB0	00000003 F8000003			1861	DC XL16'00000003F800000300000003F8000003'
0000CEC0	C1C5C2D9 61C1C5C2			1862	DC CL48'AEBR/AEB NF -QNaN/+inf FPCR'
0000CEF0	00000003 F8000003			1863	DC XL16'00000003F800000300000003F8000003'
0000CF00	C1C5C2D9 61C1C5C2			1864	DC CL48'AEBR/AEB NF -QNaN/-QNaN FPCR'
0000CF30	00000003 F8000003			1865	DC XL16'00000003F800000300000003F8000003'
0000CF40	C1C5C2D9 61C1C5C2			1866	DC CL48'AEBR/AEB NF -QNaN/+SNaN FPCR'
0000CF70	00800003 F8008003			1867	DC XL16'00800003F800800300800003F8008003'
0000CF80	C1C5C2D9 61C1C5C2			1868	DC CL48'AEBR/AEB NF +SNaN/-inf FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000CFB0	00800003 F8008003			1869 DC XL16'00800003F800800300800003F8008003'
0000CFC0	C1C5C2D9 61C1C5C2			1870 DC CL48'AEBR/AEB NF +SNaN/-2.0 FPCR'
0000CFF0	00800003 F8008003			1871 DC XL16'00800003F800800300800003F8008003'
0000D000	C1C5C2D9 61C1C5C2			1872 DC CL48'AEBR/AEB NF +SNaN/-Dnice FPCR'
0000D030	00800003 F8008003			1873 DC XL16'00800003F800800300800003F8008003'
0000D040	C1C5C2D9 61C1C5C2			1874 DC CL48'AEBR/AEB NF +SNaN/-0 FPCR'
0000D070	00800003 F8008003			1875 DC XL16'00800003F800800300800003F8008003'
0000D080	C1C5C2D9 61C1C5C2			1876 DC CL48'AEBR/AEB NF +SNaN/+0 FPCR'
0000D0B0	00800003 F8008003			1877 DC XL16'00800003F800800300800003F8008003'
0000D0C0	C1C5C2D9 61C1C5C2			1878 DC CL48'AEBR/AEB NF +SNaN/+Dnice FPCR'
0000D0F0	00800003 F8008003			1879 DC XL16'00800003F800800300800003F8008003'
0000D100	C1C5C2D9 61C1C5C2			1880 DC CL48'AEBR/AEB NF +SNaN/+2.0 FPCR'
0000D130	00800003 F8008003			1881 DC XL16'00800003F800800300800003F8008003'
0000D140	C1C5C2D9 61C1C5C2			1882 DC CL48'AEBR/AEB NF +SNaN/+inf FPCR'
0000D170	00800003 F8008003			1883 DC XL16'00800003F800800300800003F8008003'
0000D180	C1C5C2D9 61C1C5C2			1884 DC CL48'AEBR/AEB NF +SNaN/-QNaN FPCR'
0000D1B0	00800003 F8008003			1885 DC XL16'00800003F800800300800003F8008003'
0000D1C0	C1C5C2D9 61C1C5C2			1886 DC CL48'AEBR/AEB NF +SNaN/+SNaN FPCR'
0000D1F0	00800003 F8008003			1887 DC XL16'00800003F800800300800003F8008003'
		00000064	00000001	1888 SBFPNFFL_NUM EQU (*-SBFPNFFL_GOOD)/64
				1889 *
				1890 *
		0000D200	00000001	1891 SBFPOUT_GOOD EQU *
0000D200	C1C5C2D9 61C1C5C2			1892 DC CL48'AEBR/AEB F Ovfl'
0000D230	7F800000 1FFFFFFF			1893 DC XL16'7F8000001FFFFFFF7F8000001FFFFFFF'
0000D240	C1C5C2D9 61C1C5C2			1894 DC CL48'AEBR/AEB F Ufl 1'
0000D270	007FFFFFF 607FFFFE			1895 DC XL16'007FFFFFF607FFFFE007FFFFFF607FFFFE'
0000D280	C1C5C2D9 61C1C5C2			1896 DC CL48'AEBR/AEB F Ufl 2'
0000D2B0	00040F0F 5E01E1E0			1897 DC XL16'00040F0F5E01E1E000040F0F5E01E1E0'
0000D2C0	C1C5C2D9 61C1C5C2			1898 DC CL48'AEBR/AEB F Nmin'
0000D2F0	00800000 00800000			1899 DC XL16'00800000008000000080000000800000'
0000D300	C1C5C2D9 61C1C5C2			1900 DC CL48'AEBR/AEB F Incr'
0000D330	3F800001 3F800001			1901 DC XL16'3F8000013F8000013F8000013F800001'
0000D340	C1C5C2D9 61C1C5C2			1902 DC CL48'AEBR/AEB F Trun'
0000D370	3F800000 3F800000			1903 DC XL16'3F8000003F8000003F8000003F800000'
		00000006	00000001	1904 SBFPOUT_NUM EQU (*-SBFPOUT_GOOD)/64
				1905 *
				1906 *
		0000D380	00000001	1907 SBFPFLGS_GOOD EQU *
0000D380	C1C5C2D9 61C1C5C2			1908 DC CL48'AEBR/AEB F Ovfl FPCR'
0000D3B0	00280002 F8002002			1909 DC XL16'00280002F800200200280002F8002002'
0000D3C0	C1C5C2D9 61C1C5C2			1910 DC CL48'AEBR/AEB F Ufl 1 FPCR'
0000D3F0	00000002 F8001002			1911 DC XL16'00000002F800100200000002F8001002'
0000D400	C1C5C2D9 61C1C5C2			1912 DC CL48'AEBR/AEB F Ufl 2 FPCR'
0000D430	00000002 F8001002			1913 DC XL16'00000002F800100200000002F8001002'
0000D440	C1C5C2D9 61C1C5C2			1914 DC CL48'AEBR/AEB F Nmin FPCR'
0000D470	00000002 F8000002			1915 DC XL16'00000002F800000200000002F8000002'
0000D480	C1C5C2D9 61C1C5C2			1916 DC CL48'AEBR/AEB F Incr FPCR'
0000D4B0	00080002 F8000C02			1917 DC XL16'00080002F8000C0200080002F8000C02'
0000D4C0	C1C5C2D9 61C1C5C2			1918 DC CL48'AEBR/AEB F Trun FPCR'
0000D4F0	00080002 F8000802			1919 DC XL16'00080002F800080200080002F8000802'
		00000006	00000001	1920 SBFPFLGS_NUM EQU (*-SBFPFLGS_GOOD)/64
				1921 *
				1922 *
		0000D500	00000001	1923 SBFPRMO_GOOD EQU *
0000D500	C1C5C2D9 61C1C5C2			1924 DC CL48'AEBR/AEB RM +NZ RNTE, RZ'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000D530	3F800000 3F800000			1925 DC XL16'3F8000003F8000003F8000003F800000'
0000D540	C1C5C2D9 61C1C5C2			1926 DC CL48'AEBR/AEB RM +NZ RP, RM'
0000D570	3F800001 3F800001			1927 DC XL16'3F8000013F8000013F8000003F800000'
0000D580	C1C5C2D9 61C1C5C2			1928 DC CL48'AEBR/AEB RM +NZ RFS'
0000D5B0	3F800001 3F800001			1929 DC XL16'3F8000013F8000010000000000000000'
0000D5C0	C1C5C2D9 61C1C5C2			1930 DC CL48'AEBR/AEB RM -NZ RNTE, RZ'
0000D5F0	BF800000 BF800000			1931 DC XL16'BF800000BF800000BF800000BF800000'
0000D600	C1C5C2D9 61C1C5C2			1932 DC CL48'AEBR/AEB RM -NZ RP, RM'
0000D630	BF800000 BF800000			1933 DC XL16'BF800000BF800000BF800001BF800001'
0000D640	C1C5C2D9 61C1C5C2			1934 DC CL48'AEBR/AEB RM -NZ RFS'
0000D670	BF800001 BF800001			1935 DC XL16'BF800001BF8000010000000000000000'
0000D680	C1C5C2D9 61C1C5C2			1936 DC CL48'AEBR/AEB RM +NA RNTE, RZ'
0000D6B0	3F800001 3F800001			1937 DC XL16'3F8000013F8000013F8000003F800000'
0000D6C0	C1C5C2D9 61C1C5C2			1938 DC CL48'AEBR/AEB RM +NA RP, RM'
0000D6F0	3F800001 3F800001			1939 DC XL16'3F8000013F8000013F8000003F800000'
0000D700	C1C5C2D9 61C1C5C2			1940 DC CL48'AEBR/AEB RM +NA RFS'
0000D730	3F800001 3F800001			1941 DC XL16'3F8000013F8000010000000000000000'
0000D740	C1C5C2D9 61C1C5C2			1942 DC CL48'AEBR/AEB RM -NA RNTE, RZ'
0000D770	BF800001 BF800001			1943 DC XL16'BF800001BF800001BF800000BF800000'
0000D780	C1C5C2D9 61C1C5C2			1944 DC CL48'AEBR/AEB RM -NA RP, RM'
0000D7B0	BF800000 BF800000			1945 DC XL16'BF800000BF800000BF800001BF800001'
0000D7C0	C1C5C2D9 61C1C5C2			1946 DC CL48'AEBR/AEB RM -NA RFS'
0000D7F0	BF800001 BF800001			1947 DC XL16'BF800001BF8000010000000000000000'
0000D800	C1C5C2D9 61C1C5C2			1948 DC CL48'AEBR/AEB RM +TZ RNTE, RZ'
0000D830	3F800000 3F800000			1949 DC XL16'3F8000003F8000003F8000003F800000'
0000D840	C1C5C2D9 61C1C5C2			1950 DC CL48'AEBR/AEB RM +TZ RP, RM'
0000D870	3F800001 3F800001			1951 DC XL16'3F8000013F8000013F8000003F800000'
0000D880	C1C5C2D9 61C1C5C2			1952 DC CL48'AEBR/AEB RM +TZ RFS'
0000D8B0	3F800001 3F800001			1953 DC XL16'3F8000013F8000010000000000000000'
0000D8C0	C1C5C2D9 61C1C5C2			1954 DC CL48'AEBR/AEB RM -TZ RNTE, RZ'
0000D8F0	BF800000 BF800000			1955 DC XL16'BF800000BF800000BF800000BF800000'
0000D900	C1C5C2D9 61C1C5C2			1956 DC CL48'AEBR/AEB RM -TZ RP, RM'
0000D930	BF800000 BF800000			1957 DC XL16'BF800000BF800000BF800001BF800001'
0000D940	C1C5C2D9 61C1C5C2			1958 DC CL48'AEBR/AEB RM -TZ RFS'
0000D970	BF800001 BF800001			1959 DC XL16'BF800001BF8000010000000000000000'
0000D980	C1C5C2D9 61C1C5C2			1960 DC CL48'AEBR/AEB RM +TA RNTE, RZ'
0000D9B0	3F800002 3F800002			1961 DC XL16'3F8000023F8000023F8000013F800001'
0000D9C0	C1C5C2D9 61C1C5C2			1962 DC CL48'AEBR/AEB RM +TA RP, RM'
0000D9F0	3F800002 3F800002			1963 DC XL16'3F8000023F8000023F8000013F800001'
0000DA00	C1C5C2D9 61C1C5C2			1964 DC CL48'AEBR/AEB RM +TA RFS'
0000DA30	3F800001 3F800001			1965 DC XL16'3F8000013F8000010000000000000000'
0000DA40	C1C5C2D9 61C1C5C2			1966 DC CL48'AEBR/AEB RM -TA RNTE, RZ'
0000DA70	BF800002 BF800002			1967 DC XL16'BF800002BF800002BF800001BF800001'
0000DA80	C1C5C2D9 61C1C5C2			1968 DC CL48'AEBR/AEB RM -TA RP, RM'
0000DAB0	BF800001 BF800001			1969 DC XL16'BF800001BF800001BF800002BF800002'
0000DAC0	C1C5C2D9 61C1C5C2			1970 DC CL48'AEBR/AEB RM -TA RFS'
0000DAF0	BF800001 BF800001			1971 DC XL16'BF800001BF8000010000000000000000'
		00000018	00000001	1972 SBFPRMO_NUM EQU (*-SBFPRMO_GOOD)/64
				1973 *
				1974 *
		0000DB00	00000001	1975 SBFPRMOF_GOOD EQU *
0000DB00	C1C5C2D9 61C1C5C2			1976 DC CL48'AEBR/AEB RM +NZ FPCR'
0000DB30	00080002 00080002			1977 DC XL16'00080002000800020008000200080002'
0000DB40	C1C5C2D9 61C1C5C2			1978 DC CL48'AEBR/AEB RM +NZ FPCR'
0000DB70	00080002 00080002			1979 DC XL16'00080002000800020008000200080002'
0000DB80	C1C5C2D9 61C1C5C2			1980 DC CL48'AEBR/AEB RM +NZ FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000DBB0	00080002 00080002			1981 DC XL16'00080002000800020000000000000000'
0000DBC0	C1C5C2D9 61C1C5C2			1982 DC CL48'AEBR/AEB RM -NZ FPCR'
0000DBF0	00080001 00080001			1983 DC XL16'00080001000800010008000100080001'
0000DC00	C1C5C2D9 61C1C5C2			1984 DC CL48'AEBR/AEB RM -NZ FPCR'
0000DC30	00080001 00080001			1985 DC XL16'00080001000800010008000100080001'
0000DC40	C1C5C2D9 61C1C5C2			1986 DC CL48'AEBR/AEB RM -NZ FPCR'
0000DC70	00080001 00080001			1987 DC XL16'00080001000800010000000000000000'
0000DC80	C1C5C2D9 61C1C5C2			1988 DC CL48'AEBR/AEB RM +NA FPCR'
0000DCB0	00080002 00080002			1989 DC XL16'00080002000800020008000200080002'
0000DCC0	C1C5C2D9 61C1C5C2			1990 DC CL48'AEBR/AEB RM +NA FPCR'
0000DCF0	00080002 00080002			1991 DC XL16'00080002000800020008000200080002'
0000DD00	C1C5C2D9 61C1C5C2			1992 DC CL48'AEBR/AEB RM +NA FPCR'
0000DD30	00080002 00080002			1993 DC XL16'00080002000800020000000000000000'
0000DD40	C1C5C2D9 61C1C5C2			1994 DC CL48'AEBR/AEB RM -NA FPCR'
0000DD70	00080001 00080001			1995 DC XL16'00080001000800010008000100080001'
0000DD80	C1C5C2D9 61C1C5C2			1996 DC CL48'AEBR/AEB RM -NA FPCR'
0000DDB0	00080001 00080001			1997 DC XL16'00080001000800010008000100080001'
0000DDC0	C1C5C2D9 61C1C5C2			1998 DC CL48'AEBR/AEB RM -NA FPCR'
0000DDF0	00080001 00080001			1999 DC XL16'00080001000800010000000000000000'
0000DE00	C1C5C2D9 61C1C5C2			2000 DC CL48'AEBR/AEB RM +TZ FPCR'
0000DE30	00080002 00080002			2001 DC XL16'00080002000800020008000200080002'
0000DE40	C1C5C2D9 61C1C5C2			2002 DC CL48'AEBR/AEB RM +TZ FPCR'
0000DE70	00080002 00080002			2003 DC XL16'00080002000800020008000200080002'
0000DE80	C1C5C2D9 61C1C5C2			2004 DC CL48'AEBR/AEB RM +TZ FPCR'
0000DEB0	00080002 00080002			2005 DC XL16'00080002000800020000000000000000'
0000DEC0	C1C5C2D9 61C1C5C2			2006 DC CL48'AEBR/AEB RM -TZ FPCR'
0000DEF0	00080001 00080001			2007 DC XL16'00080001000800010008000100080001'
0000DF00	C1C5C2D9 61C1C5C2			2008 DC CL48'AEBR/AEB RM -TZ FPCR'
0000DF30	00080001 00080001			2009 DC XL16'00080001000800010008000100080001'
0000DF40	C1C5C2D9 61C1C5C2			2010 DC CL48'AEBR/AEB RM -TZ FPCR'
0000DF70	00080001 00080001			2011 DC XL16'00080001000800010000000000000000'
0000DF80	C1C5C2D9 61C1C5C2			2012 DC CL48'AEBR/AEB RM +TA FPCR'
0000DFB0	00080002 00080002			2013 DC XL16'00080002000800020008000200080002'
0000DFC0	C1C5C2D9 61C1C5C2			2014 DC CL48'AEBR/AEB RM +TA FPCR'
0000DFF0	00080002 00080002			2015 DC XL16'00080002000800020008000200080002'
0000E000	C1C5C2D9 61C1C5C2			2016 DC CL48'AEBR/AEB RM +TA FPCR'
0000E030	00080002 00080002			2017 DC XL16'00080002000800020000000000000000'
0000E040	C1C5C2D9 61C1C5C2			2018 DC CL48'AEBR/AEB RM -TA FPCR'
0000E070	00080001 00080001			2019 DC XL16'00080001000800010008000100080001'
0000E080	C1C5C2D9 61C1C5C2			2020 DC CL48'AEBR/AEB RM -TA FPCR'
0000E0B0	00080001 00080001			2021 DC XL16'00080001000800010008000100080001'
0000E0C0	C1C5C2D9 61C1C5C2			2022 DC CL48'AEBR/AEB RM -TA FPCR'
0000E0F0	00080001 00080001			2023 DC XL16'00080001000800010000000000000000'
		00000018	00000001	2024 SBFPRMOF_NUM EQU (*-SBFPRMOF_GOOD)/64
				2025 *
				2026 *
		0000E100	00000001	2027 LBFPNFOT_GOOD EQU *
0000E100	C1C4C2D9 40D5C640			2028 DC CL48'ADBR NF -inf/-inf'
0000E130	FFF00000 00000000			2029 DC XL16'FFF0000000000000FFF0000000000000'
0000E140	C1C4C240 D5C64060			2030 DC CL48'ADB NF -inf/-inf'
0000E170	FFF00000 00000000			2031 DC XL16'FFF0000000000000FFF0000000000000'
0000E180	C1C4C2D9 40D5C640			2032 DC CL48'ADBR NF -inf/-2.0'
0000E1B0	FFF00000 00000000			2033 DC XL16'FFF0000000000000FFF0000000000000'
0000E1C0	C1C4C240 D5C64060			2034 DC CL48'ADB NF -inf/-2.0'
0000E1F0	FFF00000 00000000			2035 DC XL16'FFF0000000000000FFF0000000000000'
0000E200	C1C4C2D9 40D5C640			2036 DC CL48'ADBR NF -inf/-Dnice'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000E230	FFF00000 00000000			2037 DC XL16'FFF0000000000000FFF0000000000000'
0000E240	C1C4C240 D5C64060			2038 DC CL48'ADB NF -inf/-Dnice'
0000E270	FFF00000 00000000			2039 DC XL16'FFF0000000000000FFF0000000000000'
0000E280	C1C4C2D9 40D5C640			2040 DC CL48'ADBR NF -inf/-0'
0000E2B0	FFF00000 00000000			2041 DC XL16'FFF0000000000000FFF0000000000000'
0000E2C0	C1C4C240 D5C64060			2042 DC CL48'ADB NF -inf/-0'
0000E2F0	FFF00000 00000000			2043 DC XL16'FFF0000000000000FFF0000000000000'
0000E300	C1C4C2D9 40D5C640			2044 DC CL48'ADBR NF -inf/+0'
0000E330	FFF00000 00000000			2045 DC XL16'FFF0000000000000FFF0000000000000'
0000E340	C1C4C240 D5C64060			2046 DC CL48'ADB NF -inf/+0'
0000E370	FFF00000 00000000			2047 DC XL16'FFF0000000000000FFF0000000000000'
0000E380	C1C4C2D9 40D5C640			2048 DC CL48'ADBR NF -inf/+Dnice'
0000E3B0	FFF00000 00000000			2049 DC XL16'FFF0000000000000FFF0000000000000'
0000E3C0	C1C4C240 D5C64060			2050 DC CL48'ADB NF -inf/+Dnice'
0000E3F0	FFF00000 00000000			2051 DC XL16'FFF0000000000000FFF0000000000000'
0000E400	C1C4C2D9 40D5C640			2052 DC CL48'ADBR NF -inf/+2.0'
0000E430	FFF00000 00000000			2053 DC XL16'FFF0000000000000FFF0000000000000'
0000E440	C1C4C240 D5C64060			2054 DC CL48'ADB NF -inf/+2.0'
0000E470	FFF00000 00000000			2055 DC XL16'FFF0000000000000FFF0000000000000'
0000E480	C1C4C2D9 40D5C640			2056 DC CL48'ADBR NF -inf/+inf'
0000E4B0	7FF80000 00000000			2057 DC XL16'7FF8000000000000FFF0000000000000'
0000E4C0	C1C4C240 D5C64060			2058 DC CL48'ADB NF -inf/+inf'
0000E4F0	7FF80000 00000000			2059 DC XL16'7FF8000000000000FFF0000000000000'
0000E500	C1C4C2D9 40D5C640			2060 DC CL48'ADBR NF -inf/-QNaN'
0000E530	FFF8B000 00000000			2061 DC XL16'FFF8B00000000000FFF8B00000000000'
0000E540	C1C4C240 D5C64060			2062 DC CL48'ADB NF -inf/-QNaN'
0000E570	FFF8B000 00000000			2063 DC XL16'FFF8B00000000000FFF8B00000000000'
0000E580	C1C4C2D9 40D5C640			2064 DC CL48'ADBR NF -inf/+SNaN'
0000E5B0	7FF8A000 00000000			2065 DC XL16'7FF8A00000000000FFF0000000000000'
0000E5C0	C1C4C240 D5C64060			2066 DC CL48'ADB NF -inf/+SNaN'
0000E5F0	7FF8A000 00000000			2067 DC XL16'7FF8A00000000000FFF0000000000000'
0000E600	C1C4C2D9 40D5C640			2068 DC CL48'ADBR NF -2.0/-inf'
0000E630	FFF00000 00000000			2069 DC XL16'FFF0000000000000FFF0000000000000'
0000E640	C1C4C240 D5C64060			2070 DC CL48'ADB NF -2.0/-inf'
0000E670	FFF00000 00000000			2071 DC XL16'FFF0000000000000FFF0000000000000'
0000E680	C1C4C2D9 40D5C640			2072 DC CL48'ADBR NF -2.0/-2.0'
0000E6B0	C0100000 00000000			2073 DC XL16'C010000000000000C010000000000000'
0000E6C0	C1C4C240 D5C64060			2074 DC CL48'ADB NF -2.0/-2.0'
0000E6F0	C0100000 00000000			2075 DC XL16'C010000000000000C010000000000000'
0000E700	C1C4C2D9 40D5C640			2076 DC CL48'ADBR NF -2.0/-Dnice'
0000E730	C0000000 00000000			2077 DC XL16'C000000000000000C000000000000000'
0000E740	C1C4C240 D5C64060			2078 DC CL48'ADB NF -2.0/-Dnice'
0000E770	C0000000 00000000			2079 DC XL16'C000000000000000C000000000000000'
0000E780	C1C4C2D9 40D5C640			2080 DC CL48'ADBR NF -2.0/-0'
0000E7B0	C0000000 00000000			2081 DC XL16'C000000000000000C000000000000000'
0000E7C0	C1C4C240 D5C64060			2082 DC CL48'ADB NF -2.0/-0'
0000E7F0	C0000000 00000000			2083 DC XL16'C000000000000000C000000000000000'
0000E800	C1C4C2D9 40D5C640			2084 DC CL48'ADBR NF -2.0/+0'
0000E830	C0000000 00000000			2085 DC XL16'C000000000000000C000000000000000'
0000E840	C1C4C240 D5C64060			2086 DC CL48'ADB NF -2.0/+0'
0000E870	C0000000 00000000			2087 DC XL16'C000000000000000C000000000000000'
0000E880	C1C4C2D9 40D5C640			2088 DC CL48'ADBR NF -2.0/+Dnice'
0000E8B0	C0000000 00000000			2089 DC XL16'C000000000000000C000000000000000'
0000E8C0	C1C4C240 D5C64060			2090 DC CL48'ADB NF -2.0/+Dnice'
0000E8F0	C0000000 00000000			2091 DC XL16'C000000000000000C000000000000000'
0000E900	C1C4C2D9 40D5C640			2092 DC CL48'ADBR NF -2.0/+2.0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000E930	00000000 00000000			2093 DC XL16'00000000000000000000000000000000'
0000E940	C1C4C240 D5C64060			2094 DC CL48'ADB NF -2.0/+2.0'
0000E970	00000000 00000000			2095 DC XL16'00000000000000000000000000000000'
0000E980	C1C4C2D9 40D5C640			2096 DC CL48'ADBR NF -2.0/+inf'
0000E9B0	7FF00000 00000000			2097 DC XL16'7FF00000000000007FF0000000000000'
0000E9C0	C1C4C240 D5C64060			2098 DC CL48'ADB NF -2.0/+inf'
0000E9F0	7FF00000 00000000			2099 DC XL16'7FF00000000000007FF0000000000000'
0000EA00	C1C4C2D9 40D5C640			2100 DC CL48'ADBR NF -2.0/-QNaN'
0000EA30	FFF8B000 00000000			2101 DC XL16'FFF8B00000000000FFF8B00000000000'
0000EA40	C1C4C240 D5C64060			2102 DC CL48'ADB NF -2.0/-QNaN'
0000EA70	FFF8B000 00000000			2103 DC XL16'FFF8B00000000000FFF8B00000000000'
0000EA80	C1C4C2D9 40D5C640			2104 DC CL48'ADBR NF -2.0/+SNaN'
0000EAB0	7FF8A000 00000000			2105 DC XL16'7FF8A00000000000C0000000000000'
0000EAC0	C1C4C240 D5C64060			2106 DC CL48'ADB NF -2.0/+SNaN'
0000EAF0	7FF8A000 00000000			2107 DC XL16'7FF8A00000000000C0000000000000'
0000EB00	C1C4C2D9 40D5C640			2108 DC CL48'ADBR NF -Dnice/-inf'
0000EB30	FFF00000 00000000			2109 DC XL16'FFF0000000000000FFF0000000000000'
0000EB40	C1C4C240 D5C64060			2110 DC CL48'ADB NF -Dnice/-inf'
0000EB70	FFF00000 00000000			2111 DC XL16'FFF0000000000000FFF0000000000000'
0000EB80	C1C4C2D9 40D5C640			2112 DC CL48'ADBR NF -Dnice/-2.0'
0000EBB0	C0000000 00000000			2113 DC XL16'C000000000000000C0000000000000'
0000EBC0	C1C4C240 D5C64060			2114 DC CL48'ADB NF -Dnice/-2.0'
0000EBF0	C0000000 00000000			2115 DC XL16'C000000000000000C0000000000000'
0000EC00	C1C4C2D9 40D5C640			2116 DC CL48'ADBR NF -Dnice/-Dnice'
0000EC30	80020000 00000000			2117 DC XL16'8002000000000000DFE000000000000'
0000EC40	C1C4C240 D5C64060			2118 DC CL48'ADB NF -Dnice/-Dnice'
0000EC70	80020000 00000000			2119 DC XL16'8002000000000000DFE000000000000'
0000EC80	C1C4C2D9 40D5C640			2120 DC CL48'ADBR NF -Dnice/-0'
0000ECB0	80010000 00000000			2121 DC XL16'8001000000000000DFD000000000000'
0000ECC0	C1C4C240 D5C64060			2122 DC CL48'ADB NF -Dnice/-0'
0000ECF0	80010000 00000000			2123 DC XL16'8001000000000000DFD000000000000'
0000ED00	C1C4C2D9 40D5C640			2124 DC CL48'ADBR NF -Dnice/+0'
0000ED30	80010000 00000000			2125 DC XL16'8001000000000000DFD000000000000'
0000ED40	C1C4C240 D5C64060			2126 DC CL48'ADB NF -Dnice/+0'
0000ED70	80010000 00000000			2127 DC XL16'8001000000000000DFD000000000000'
0000ED80	C1C4C2D9 40D5C640			2128 DC CL48'ADBR NF -Dnice/+Dnice'
0000EDB0	00000000 00000000			2129 DC XL16'000000000000000000000000000000'
0000EDC0	C1C4C240 D5C64060			2130 DC CL48'ADB NF -Dnice/+Dnice'
0000EDF0	00000000 00000000			2131 DC XL16'000000000000000000000000000000'
0000EE00	C1C4C2D9 40D5C640			2132 DC CL48'ADBR NF -Dnice/+2.0'
0000EE30	40000000 00000000			2133 DC XL16'400000000000000040000000000000'
0000EE40	C1C4C240 D5C64060			2134 DC CL48'ADB NF -Dnice/+2.0'
0000EE70	40000000 00000000			2135 DC XL16'400000000000000040000000000000'
0000EE80	C1C4C2D9 40D5C640			2136 DC CL48'ADBR NF -Dnice/+inf'
0000EEB0	7FF00000 00000000			2137 DC XL16'7FF00000000000007FF000000000000'
0000EEC0	C1C4C240 D5C64060			2138 DC CL48'ADB NF -Dnice/+inf'
0000EEF0	7FF00000 00000000			2139 DC XL16'7FF00000000000007FF000000000000'
0000EF00	C1C4C2D9 40D5C640			2140 DC CL48'ADBR NF -Dnice/-QNaN'
0000EF30	FFF8B000 00000000			2141 DC XL16'FFF8B00000000000FFF8B00000000000'
0000EF40	C1C4C240 D5C64060			2142 DC CL48'ADB NF -Dnice/-QNaN'
0000EF70	FFF8B000 00000000			2143 DC XL16'FFF8B00000000000FFF8B00000000000'
0000EF80	C1C4C2D9 40D5C640			2144 DC CL48'ADBR NF -Dnice/+SNaN'
0000EFB0	7FF8A000 00000000			2145 DC XL16'7FF8A000000000008001000000000000'
0000EFC0	C1C4C240 D5C64060			2146 DC CL48'ADB NF -Dnice/+SNaN'
0000EFF0	7FF8A000 00000000			2147 DC XL16'7FF8A000000000008001000000000000'
0000F000	C1C4C2D9 40D5C640			2148 DC CL48'ADBR NF -0/-inf'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000F030	FFF00000	00000000		2149 DC XL16'FFF0000000000000FFF0000000000000'
0000F040	C1C4C240	D5C64060		2150 DC CL48'ADB NF -0/-inf'
0000F070	FFF00000	00000000		2151 DC XL16'FFF0000000000000FFF0000000000000'
0000F080	C1C4C2D9	40D5C640		2152 DC CL48'ADBR NF -0/-2.0'
0000F0B0	C0000000	00000000		2153 DC XL16'C000000000000000C000000000000000'
0000F0C0	C1C4C240	D5C64060		2154 DC CL48'ADB NF -0/-2.0'
0000F0F0	C0000000	00000000		2155 DC XL16'C000000000000000C000000000000000'
0000F100	C1C4C2D9	40D5C640		2156 DC CL48'ADBR NF -0/-Dnice'
0000F130	80010000	00000000		2157 DC XL16'8001000000000000DFD0000000000000'
0000F140	C1C4C240	D5C64060		2158 DC CL48'ADB NF -0/-Dnice'
0000F170	80010000	00000000		2159 DC XL16'8001000000000000DFD0000000000000'
0000F180	C1C4C2D9	40D5C640		2160 DC CL48'ADBR NF -0/-0'
0000F1B0	80000000	00000000		2161 DC XL16'80000000000000008000000000000000'
0000F1C0	C1C4C240	D5C64060		2162 DC CL48'ADB NF -0/-0'
0000F1F0	80000000	00000000		2163 DC XL16'80000000000000008000000000000000'
0000F200	C1C4C2D9	40D5C640		2164 DC CL48'ADBR NF -0/+0'
0000F230	00000000	00000000		2165 DC XL16'00000000000000000000000000000000'
0000F240	C1C4C240	D5C64060		2166 DC CL48'ADB NF -0/+0'
0000F270	00000000	00000000		2167 DC XL16'00000000000000000000000000000000'
0000F280	C1C4C2D9	40D5C640		2168 DC CL48'ADBR NF -0/+Dnice'
0000F2B0	00010000	00000000		2169 DC XL16'000100000000000005FD0000000000000'
0000F2C0	C1C4C240	D5C64060		2170 DC CL48'ADB NF -0/+Dnice'
0000F2F0	00010000	00000000		2171 DC XL16'000100000000000005FD0000000000000'
0000F300	C1C4C2D9	40D5C640		2172 DC CL48'ADBR NF -0/+2.0'
0000F330	40000000	00000000		2173 DC XL16'40000000000000004000000000000000'
0000F340	C1C4C240	D5C64060		2174 DC CL48'ADB NF -0/+2.0'
0000F370	40000000	00000000		2175 DC XL16'40000000000000004000000000000000'
0000F380	C1C4C2D9	40D5C640		2176 DC CL48'ADBR NF -0/+inf'
0000F3B0	7FF00000	00000000		2177 DC XL16'7FF00000000000007FF0000000000000'
0000F3C0	C1C4C240	D5C64060		2178 DC CL48'ADB NF -0/+inf'
0000F3F0	7FF00000	00000000		2179 DC XL16'7FF00000000000007FF0000000000000'
0000F400	C1C4C2D9	40D5C640		2180 DC CL48'ADBR NF -0/-QNaN'
0000F430	FFF8B000	00000000		2181 DC XL16'FFF8B00000000000FFF8B0000000000000'
0000F440	C1C4C240	D5C64060		2182 DC CL48'ADB NF -0/-QNaN'
0000F470	FFF8B000	00000000		2183 DC XL16'FFF8B00000000000FFF8B0000000000000'
0000F480	C1C4C2D9	40D5C640		2184 DC CL48'ADBR NF -0/+SNaN'
0000F4B0	7FF8A000	00000000		2185 DC XL16'7FF8A000000000008000000000000000'
0000F4C0	C1C4C240	D5C64060		2186 DC CL48'ADB NF -0/+SNaN'
0000F4F0	7FF8A000	00000000		2187 DC XL16'7FF8A000000000008000000000000000'
0000F500	C1C4C2D9	40D5C640		2188 DC CL48'ADBR NF +0/-inf'
0000F530	FFF00000	00000000		2189 DC XL16'FFF0000000000000FFF0000000000000'
0000F540	C1C4C240	D5C6404E		2190 DC CL48'ADB NF +0/-inf'
0000F570	FFF00000	00000000		2191 DC XL16'FFF0000000000000FFF0000000000000'
0000F580	C1C4C2D9	40D5C640		2192 DC CL48'ADBR NF +0/-2.0'
0000F5B0	C0000000	00000000		2193 DC XL16'C000000000000000C000000000000000'
0000F5C0	C1C4C240	D5C6404E		2194 DC CL48'ADB NF +0/-2.0'
0000F5F0	C0000000	00000000		2195 DC XL16'C000000000000000C000000000000000'
0000F600	C1C4C2D9	40D5C640		2196 DC CL48'ADBR NF +0/-Dnice'
0000F630	80010000	00000000		2197 DC XL16'8001000000000000DFD0000000000000'
0000F640	C1C4C240	D5C6404E		2198 DC CL48'ADB NF +0/-Dnice'
0000F670	80010000	00000000		2199 DC XL16'8001000000000000DFD0000000000000'
0000F680	C1C4C2D9	40D5C640		2200 DC CL48'ADBR NF +0/-0'
0000F6B0	00000000	00000000		2201 DC XL16'00000000000000000000000000000000'
0000F6C0	C1C4C240	D5C6404E		2202 DC CL48'ADB NF +0/-0'
0000F6F0	00000000	00000000		2203 DC XL16'00000000000000000000000000000000'
0000F700	C1C4C2D9	40D5C640		2204 DC CL48'ADBR NF +0/+0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
0000F730	00000000 00000000			2205 DC XL16'000000000000000000000000000000'
0000F740	C1C4C240 D5C6404E			2206 DC CL48'ADB NF +0/+0'
0000F770	00000000 00000000			2207 DC XL16'000000000000000000000000000000'
0000F780	C1C4C2D9 40D5C640			2208 DC CL48'ADBR NF +0/+Dnice'
0000F7B0	00010000 00000000			2209 DC XL16'000100000000000005FD0000000000000'
0000F7C0	C1C4C240 D5C6404E			2210 DC CL48'ADB NF +0/+Dnice'
0000F7F0	00010000 00000000			2211 DC XL16'000100000000000005FD0000000000000'
0000F800	C1C4C2D9 40D5C640			2212 DC CL48'ADBR NF +0/+2.0'
0000F830	40000000 00000000			2213 DC XL16'40000000000000000400000000000000'
0000F840	C1C4C240 D5C6404E			2214 DC CL48'ADB NF +0/+2.0'
0000F870	40000000 00000000			2215 DC XL16'40000000000000000400000000000000'
0000F880	C1C4C2D9 40D5C640			2216 DC CL48'ADBR NF +0/+inf'
0000F8B0	7FF00000 00000000			2217 DC XL16'7FF000000000000007FF0000000000000'
0000F8C0	C1C4C240 D5C6404E			2218 DC CL48'ADB NF +0/+inf'
0000F8F0	7FF00000 00000000			2219 DC XL16'7FF000000000000007FF0000000000000'
0000F900	C1C4C2D9 40D5C640			2220 DC CL48'ADBR NF +0/-QNaN'
0000F930	FFF8B000 00000000			2221 DC XL16'FFF8B000000000000FFF8B0000000000000'
0000F940	C1C4C240 D5C6404E			2222 DC CL48'ADB NF +0/-QNaN'
0000F970	FFF8B000 00000000			2223 DC XL16'FFF8B000000000000FFF8B0000000000000'
0000F980	C1C4C2D9 40D5C640			2224 DC CL48'ADBR NF +0/+SNaN'
0000F9B0	7FF8A000 00000000			2225 DC XL16'7FF8A000000000000000000000000000'
0000F9C0	C1C4C240 D5C6404E			2226 DC CL48'ADB NF +0/+SNaN'
0000F9F0	7FF8A000 00000000			2227 DC XL16'7FF8A000000000000000000000000000'
0000FA00	C1C4C2D9 40D5C640			2228 DC CL48'ADBR NF +Dnice/-inf'
0000FA30	FFF00000 00000000			2229 DC XL16'FFF00000000000000FFF00000000000000'
0000FA40	C1C4C240 D5C6404E			2230 DC CL48'ADB NF +Dnice/-inf'
0000FA70	FFF00000 00000000			2231 DC XL16'FFF00000000000000FFF00000000000000'
0000FA80	C1C4C2D9 40D5C640			2232 DC CL48'ADBR NF +Dnice/-2.0'
0000FAB0	C0000000 00000000			2233 DC XL16'C0000000000000000C0000000000000000'
0000FAC0	C1C4C240 D5C6404E			2234 DC CL48'ADB NF +Dnice/-2.0'
0000FAF0	C0000000 00000000			2235 DC XL16'C0000000000000000C0000000000000000'
0000FB00	C1C4C2D9 40D5C640			2236 DC CL48'ADBR NF +Dnice/-Dnice'
0000FB30	00000000 00000000			2237 DC XL16'0000000000000000000000000000000000'
0000FB40	C1C4C240 D5C6404E			2238 DC CL48'ADB NF +Dnice/-Dnice'
0000FB70	00000000 00000000			2239 DC XL16'0000000000000000000000000000000000'
0000FB80	C1C4C2D9 40D5C640			2240 DC CL48'ADBR NF +Dnice/-0'
0000FBB0	00010000 00000000			2241 DC XL16'000100000000000005FD000000000000000'
0000FBC0	C1C4C240 D5C6404E			2242 DC CL48'ADB NF +Dnice/-0'
0000FBF0	00010000 00000000			2243 DC XL16'000100000000000005FD000000000000000'
0000FC00	C1C4C2D9 40D5C640			2244 DC CL48'ADBR NF +Dnice/+0'
0000FC30	00010000 00000000			2245 DC XL16'000100000000000005FD000000000000000'
0000FC40	C1C4C240 D5C6404E			2246 DC CL48'ADB NF +Dnice/+0'
0000FC70	00010000 00000000			2247 DC XL16'000100000000000005FD000000000000000'
0000FC80	C1C4C2D9 40D5C640			2248 DC CL48'ADBR NF +Dnice/+Dnice'
0000FCB0	00020000 00000000			2249 DC XL16'000200000000000005FE000000000000000'
0000FCC0	C1C4C240 D5C6404E			2250 DC CL48'ADB NF +Dnice/+Dnice'
0000FCF0	00020000 00000000			2251 DC XL16'000200000000000005FE000000000000000'
0000FD00	C1C4C2D9 40D5C640			2252 DC CL48'ADBR NF +Dnice/+2.0'
0000FD30	40000000 00000000			2253 DC XL16'4000000000000000040000000000000000'
0000FD40	C1C4C240 D5C6404E			2254 DC CL48'ADB NF +Dnice/+2.0'
0000FD70	40000000 00000000			2255 DC XL16'4000000000000000040000000000000000'
0000FD80	C1C4C2D9 40D5C640			2256 DC CL48'ADBR NF +Dnice/+inf'
0000FDB0	7FF00000 00000000			2257 DC XL16'7FF000000000000007FF000000000000000'
0000FDC0	C1C4C240 D5C6404E			2258 DC CL48'ADB NF +Dnice/+inf'
0000FDF0	7FF00000 00000000			2259 DC XL16'7FF000000000000007FF000000000000000'
0000FE00	C1C4C2D9 40D5C640			2260 DC CL48'ADBR NF +Dnice/-QNaN'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
0000FE30	FFF8B000 00000000			2261	DC XL16'FFF8B00000000000FFF8B00000000000'
0000FE40	C1C4C240 D5C6404E			2262	DC CL48'ADB NF +Dnice/-QNaN'
0000FE70	FFF8B000 00000000			2263	DC XL16'FFF8B00000000000FFF8B00000000000'
0000FE80	C1C4C2D9 40D5C640			2264	DC CL48'ADBR NF +Dnice/+SNaN'
0000FEB0	7FF8A000 00000000			2265	DC XL16'7FF8A000000000000010000000000000'
0000FEC0	C1C4C240 D5C6404E			2266	DC CL48'ADB NF +Dnice/+SNaN'
0000FEF0	7FF8A000 00000000			2267	DC XL16'7FF8A0000000000000100000000000'
0000FF00	C1C4C2D9 40D5C640			2268	DC CL48'ADBR NF +2.0/-inf'
0000FF30	FFF00000 00000000			2269	DC XL16'FFF0000000000000FFF0000000000000'
0000FF40	C1C4C240 D5C6404E			2270	DC CL48'ADB NF +2.0/-inf'
0000FF70	FFF00000 00000000			2271	DC XL16'FFF0000000000000FFF0000000000000'
0000FF80	C1C4C2D9 40D5C640			2272	DC CL48'ADBR NF +2.0/-2.0'
0000FFB0	00000000 00000000			2273	DC XL16'000000000000000000000000000000'
0000FFC0	C1C4C240 D5C6404E			2274	DC CL48'ADB NF +2.0/-2.0'
0000FFF0	00000000 00000000			2275	DC XL16'000000000000000000000000000000'
00010000	C1C4C2D9 40D5C640			2276	DC CL48'ADBR NF +2.0/-Dnice'
00010030	40000000 00000000			2277	DC XL16'400000000000000040000000000000'
00010040	C1C4C240 D5C6404E			2278	DC CL48'ADB NF +2.0/-Dnice'
00010070	40000000 00000000			2279	DC XL16'400000000000000040000000000000'
00010080	C1C4C2D9 40D5C640			2280	DC CL48'ADBR NF +2.0/-0'
000100B0	40000000 00000000			2281	DC XL16'400000000000000040000000000000'
000100C0	C1C4C240 D5C6404E			2282	DC CL48'ADB NF +2.0/-0'
000100F0	40000000 00000000			2283	DC XL16'400000000000000040000000000000'
00010100	C1C4C2D9 40D5C640			2284	DC CL48'ADBR NF +2.0/+0'
00010130	40000000 00000000			2285	DC XL16'400000000000000040000000000000'
00010140	C1C4C240 D5C6404E			2286	DC CL48'ADB NF +2.0/+0'
00010170	40000000 00000000			2287	DC XL16'400000000000000040000000000000'
00010180	C1C4C2D9 40D5C640			2288	DC CL48'ADBR NF +2.0/+Dnice'
000101B0	40000000 00000000			2289	DC XL16'400000000000000040000000000000'
000101C0	C1C4C240 D5C6404E			2290	DC CL48'ADB NF +2.0/+Dnice'
000101F0	40000000 00000000			2291	DC XL16'400000000000000040000000000000'
00010200	C1C4C2D9 40D5C640			2292	DC CL48'ADBR NF +2.0/+2.0'
00010230	40100000 00000000			2293	DC XL16'40100000000000004010000000000000'
00010240	C1C4C240 D5C6404E			2294	DC CL48'ADB NF +2.0/+2.0'
00010270	40100000 00000000			2295	DC XL16'40100000000000004010000000000000'
00010280	C1C4C2D9 40D5C640			2296	DC CL48'ADBR NF +2.0/+inf'
000102B0	7FF00000 00000000			2297	DC XL16'7FF00000000000007FF0000000000000'
000102C0	C1C4C240 D5C6404E			2298	DC CL48'ADB NF +2.0/+inf'
000102F0	7FF00000 00000000			2299	DC XL16'7FF00000000000007FF0000000000000'
00010300	C1C4C2D9 40D5C640			2300	DC CL48'ADBR NF +2.0/-QNaN'
00010330	FFF8B000 00000000			2301	DC XL16'FFF8B00000000000FFF8B00000000000'
00010340	C1C4C240 D5C6404E			2302	DC CL48'ADB NF +2.0/-QNaN'
00010370	FFF8B000 00000000			2303	DC XL16'FFF8B00000000000FFF8B00000000000'
00010380	C1C4C2D9 40D5C640			2304	DC CL48'ADBR NF +2.0/+SNaN'
000103B0	7FF8A000 00000000			2305	DC XL16'7FF8A0000000000040000000000000'
000103C0	C1C4C240 D5C6404E			2306	DC CL48'ADB NF +2.0/+SNaN'
000103F0	7FF8A000 00000000			2307	DC XL16'7FF8A0000000000040000000000000'
00010400	C1C4C2D9 40D5C640			2308	DC CL48'ADBR NF +inf/-inf'
00010430	7FF80000 00000000			2309	DC XL16'7FF80000000000007FF0000000000000'
00010440	C1C4C240 D5C6404E			2310	DC CL48'ADB NF +inf/-inf'
00010470	7FF80000 00000000			2311	DC XL16'7FF80000000000007FF0000000000000'
00010480	C1C4C2D9 40D5C640			2312	DC CL48'ADBR NF +inf/-2.0'
000104B0	7FF00000 00000000			2313	DC XL16'7FF00000000000007FF0000000000000'
000104C0	C1C4C240 D5C6404E			2314	DC CL48'ADB NF +inf/-2.0'
000104F0	7FF00000 00000000			2315	DC XL16'7FF00000000000007FF0000000000000'
00010500	C1C4C2D9 40D5C640			2316	DC CL48'ADBR NF +inf/-Dnice'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00010530	7FF00000 00000000			2317 DC XL16'7FF00000000000007FF0000000000000'
00010540	C1C4C240 D5C6404E			2318 DC CL48'ADB NF +inf/-Dnice'
00010570	7FF00000 00000000			2319 DC XL16'7FF00000000000007FF0000000000000'
00010580	C1C4C2D9 40D5C640			2320 DC CL48'ADBR NF +inf/-0'
000105B0	7FF00000 00000000			2321 DC XL16'7FF00000000000007FF0000000000000'
000105C0	C1C4C240 D5C6404E			2322 DC CL48'ADB NF +inf/-0'
000105F0	7FF00000 00000000			2323 DC XL16'7FF00000000000007FF0000000000000'
00010600	C1C4C2D9 40D5C640			2324 DC CL48'ADBR NF +inf/+0'
00010630	7FF00000 00000000			2325 DC XL16'7FF00000000000007FF0000000000000'
00010640	C1C4C240 D5C6404E			2326 DC CL48'ADB NF +inf/+0'
00010670	7FF00000 00000000			2327 DC XL16'7FF00000000000007FF0000000000000'
00010680	C1C4C2D9 40D5C640			2328 DC CL48'ADBR NF +inf/+Dnice'
000106B0	7FF00000 00000000			2329 DC XL16'7FF00000000000007FF0000000000000'
000106C0	C1C4C240 D5C6404E			2330 DC CL48'ADB NF +inf/+Dnice'
000106F0	7FF00000 00000000			2331 DC XL16'7FF00000000000007FF0000000000000'
00010700	C1C4C2D9 40D5C640			2332 DC CL48'ADBR NF +inf/+2.0'
00010730	7FF00000 00000000			2333 DC XL16'7FF00000000000007FF0000000000000'
00010740	C1C4C240 D5C6404E			2334 DC CL48'ADB NF +inf/+2.0'
00010770	7FF00000 00000000			2335 DC XL16'7FF00000000000007FF0000000000000'
00010780	C1C4C2D9 40D5C640			2336 DC CL48'ADBR NF +inf/+inf'
000107B0	7FF00000 00000000			2337 DC XL16'7FF00000000000007FF0000000000000'
000107C0	C1C4C240 D5C6404E			2338 DC CL48'ADB NF +inf/+inf'
000107F0	7FF00000 00000000			2339 DC XL16'7FF00000000000007FF0000000000000'
00010800	C1C4C2D9 40D5C640			2340 DC CL48'ADBR NF +inf/-QNaN'
00010830	FFF8B000 00000000			2341 DC XL16'FFF8B00000000000FFF8B00000000000'
00010840	C1C4C240 D5C6404E			2342 DC CL48'ADB NF +inf/-QNaN'
00010870	FFF8B000 00000000			2343 DC XL16'FFF8B00000000000FFF8B00000000000'
00010880	C1C4C2D9 40D5C640			2344 DC CL48'ADBR NF +inf/+SNaN'
000108B0	7FF8A000 00000000			2345 DC XL16'7FF8A000000000007FF000000000000'
000108C0	C1C4C240 D5C6404E			2346 DC CL48'ADB NF +inf/+SNaN'
000108F0	7FF8A000 00000000			2347 DC XL16'7FF8A000000000007FF000000000000'
00010900	C1C4C2D9 40D5C640			2348 DC CL48'ADBR NF -QNaN/-inf'
00010930	FFF8B000 00000000			2349 DC XL16'FFF8B00000000000FFF8B00000000000'
00010940	C1C4C240 D5C64060			2350 DC CL48'ADB NF -QNaN/-inf'
00010970	FFF8B000 00000000			2351 DC XL16'FFF8B00000000000FFF8B00000000000'
00010980	C1C4C2D9 40D5C640			2352 DC CL48'ADBR NF -QNaN/-2.0'
000109B0	FFF8B000 00000000			2353 DC XL16'FFF8B00000000000FFF8B00000000000'
000109C0	C1C4C240 D5C64060			2354 DC CL48'ADB NF -QNaN/-2.0'
000109F0	FFF8B000 00000000			2355 DC XL16'FFF8B00000000000FFF8B00000000000'
00010A00	C1C4C2D9 40D5C640			2356 DC CL48'ADBR NF -QNaN/-Dnice'
00010A30	FFF8B000 00000000			2357 DC XL16'FFF8B00000000000FFF8B00000000000'
00010A40	C1C4C240 D5C64060			2358 DC CL48'ADB NF -QNaN/-Dnice'
00010A70	FFF8B000 00000000			2359 DC XL16'FFF8B00000000000FFF8B00000000000'
00010A80	C1C4C2D9 40D5C640			2360 DC CL48'ADBR NF -QNaN/-0'
00010AB0	FFF8B000 00000000			2361 DC XL16'FFF8B00000000000FFF8B00000000000'
00010AC0	C1C4C240 D5C64060			2362 DC CL48'ADB NF -QNaN/-0'
00010AF0	FFF8B000 00000000			2363 DC XL16'FFF8B00000000000FFF8B00000000000'
00010B00	C1C4C2D9 40D5C640			2364 DC CL48'ADBR NF -QNaN/+0'
00010B30	FFF8B000 00000000			2365 DC XL16'FFF8B00000000000FFF8B00000000000'
00010B40	C1C4C240 D5C64060			2366 DC CL48'ADB NF -QNaN/+0'
00010B70	FFF8B000 00000000			2367 DC XL16'FFF8B00000000000FFF8B00000000000'
00010B80	C1C4C2D9 40D5C640			2368 DC CL48'ADBR NF -QNaN/+Dnice'
00010BB0	FFF8B000 00000000			2369 DC XL16'FFF8B00000000000FFF8B00000000000'
00010BC0	C1C4C240 D5C64060			2370 DC CL48'ADB NF -QNaN/+Dnice'
00010BF0	FFF8B000 00000000			2371 DC XL16'FFF8B00000000000FFF8B00000000000'
00010C00	C1C4C2D9 40D5C640			2372 DC CL48'ADBR NF -QNaN/+2.0'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00010C30	FFF8B000 00000000			2373	DC XL16'FFF8B00000000000FFF8B0000000000'
00010C40	C1C4C240 D5C64060			2374	DC CL48'ADB NF -QNaN/+2.0'
00010C70	FFF8B000 00000000			2375	DC XL16'FFF8B00000000000FFF8B0000000000'
00010C80	C1C4C2D9 40D5C640			2376	DC CL48'ADBR NF -QNaN/+inf'
00010CB0	FFF8B000 00000000			2377	DC XL16'FFF8B00000000000FFF8B0000000000'
00010CC0	C1C4C240 D5C64060			2378	DC CL48'ADB NF -QNaN/+inf'
00010CF0	FFF8B000 00000000			2379	DC XL16'FFF8B00000000000FFF8B0000000000'
00010D00	C1C4C2D9 40D5C640			2380	DC CL48'ADBR NF -QNaN/-QNaN'
00010D30	FFF8B000 00000000			2381	DC XL16'FFF8B00000000000FFF8B0000000000'
00010D40	C1C4C240 D5C64060			2382	DC CL48'ADB NF -QNaN/-QNaN'
00010D70	FFF8B000 00000000			2383	DC XL16'FFF8B00000000000FFF8B0000000000'
00010D80	C1C4C2D9 40D5C640			2384	DC CL48'ADBR NF -QNaN/+SNaN'
00010DB0	7FF8A000 00000000			2385	DC XL16'7FF8A00000000000FFF8B0000000000'
00010DC0	C1C4C240 D5C64060			2386	DC CL48'ADB NF -QNaN/+SNaN'
00010DF0	7FF8A000 00000000			2387	DC XL16'7FF8A00000000000FFF8B0000000000'
00010E00	C1C4C2D9 40D5C640			2388	DC CL48'ADBR NF +SNaN/-inf'
00010E30	7FF8A000 00000000			2389	DC XL16'7FF8A000000000007FF0A0000000000'
00010E40	C1C4C240 D5C6404E			2390	DC CL48'ADB NF +SNaN/-inf'
00010E70	7FF8A000 00000000			2391	DC XL16'7FF8A000000000007FF0A0000000000'
00010E80	C1C4C2D9 40D5C640			2392	DC CL48'ADBR NF +SNaN/-2.0'
00010EB0	7FF8A000 00000000			2393	DC XL16'7FF8A000000000007FF0A0000000000'
00010EC0	C1C4C240 D5C6404E			2394	DC CL48'ADB NF +SNaN/-2.0'
00010EF0	7FF8A000 00000000			2395	DC XL16'7FF8A000000000007FF0A0000000000'
00010F00	C1C4C2D9 40D5C640			2396	DC CL48'ADBR NF +SNaN/-Dnice'
00010F30	7FF8A000 00000000			2397	DC XL16'7FF8A000000000007FF0A0000000000'
00010F40	C1C4C240 D5C6404E			2398	DC CL48'ADB NF +SNaN/-Dnice'
00010F70	7FF8A000 00000000			2399	DC XL16'7FF8A000000000007FF0A0000000000'
00010F80	C1C4C2D9 40D5C640			2400	DC CL48'ADBR NF +SNaN/-0'
00010FB0	7FF8A000 00000000			2401	DC XL16'7FF8A000000000007FF0A0000000000'
00010FC0	C1C4C240 D5C6404E			2402	DC CL48'ADB NF +SNaN/-0'
00010FF0	7FF8A000 00000000			2403	DC XL16'7FF8A000000000007FF0A0000000000'
00011000	C1C4C2D9 40D5C640			2404	DC CL48'ADBR NF +SNaN/+0'
00011030	7FF8A000 00000000			2405	DC XL16'7FF8A000000000007FF0A0000000000'
00011040	C1C4C240 D5C6404E			2406	DC CL48'ADB NF +SNaN/+0'
00011070	7FF8A000 00000000			2407	DC XL16'7FF8A000000000007FF0A0000000000'
00011080	C1C4C2D9 40D5C640			2408	DC CL48'ADBR NF +SNaN/+Dnice'
000110B0	7FF8A000 00000000			2409	DC XL16'7FF8A000000000007FF0A0000000000'
000110C0	C1C4C240 D5C6404E			2410	DC CL48'ADB NF +SNaN/+Dnice'
000110F0	7FF8A000 00000000			2411	DC XL16'7FF8A000000000007FF0A0000000000'
00011100	C1C4C2D9 40D5C640			2412	DC CL48'ADBR NF +SNaN/+2.0'
00011130	7FF8A000 00000000			2413	DC XL16'7FF8A000000000007FF0A0000000000'
00011140	C1C4C240 D5C6404E			2414	DC CL48'ADB NF +SNaN/+2.0'
00011170	7FF8A000 00000000			2415	DC XL16'7FF8A000000000007FF0A0000000000'
00011180	C1C4C2D9 40D5C640			2416	DC CL48'ADBR NF +SNaN/+inf'
000111B0	7FF8A000 00000000			2417	DC XL16'7FF8A000000000007FF0A0000000000'
000111C0	C1C4C240 D5C6404E			2418	DC CL48'ADB NF +SNaN/+inf'
000111F0	7FF8A000 00000000			2419	DC XL16'7FF8A000000000007FF0A0000000000'
00011200	C1C4C2D9 40D5C640			2420	DC CL48'ADBR NF +SNaN/-QNaN'
00011230	7FF8A000 00000000			2421	DC XL16'7FF8A000000000007FF0A0000000000'
00011240	C1C4C240 D5C6404E			2422	DC CL48'ADB NF +SNaN/-QNaN'
00011270	7FF8A000 00000000			2423	DC XL16'7FF8A000000000007FF0A0000000000'
00011280	C1C4C2D9 40D5C640			2424	DC CL48'ADBR NF +SNaN/+SNaN'
000112B0	7FF8A000 00000000			2425	DC XL16'7FF8A000000000007FF0A0000000000'
000112C0	C1C4C240 D5C6404E			2426	DC CL48'ADB NF +SNaN/+SNaN'
000112F0	7FF8A000 00000000			2427	DC XL16'7FF8A000000000007FF0A0000000000'
		000000C8	00000001	2428	LBFPNFOT_NUM EQU (*-LBFPNFOT_GOOD)/64

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				2429 *
				2430 *
		00011300	00000001	2431 LBFPNFFL_GOOD EQU *
00011300	C1C4C2D9	61C1C4C2		2432 DC CL48'ADBR/ADB NF -inf/-inf FPCR'
00011330	00000001	F8000001		2433 DC XL16'00000001F800000100000001F8000001'
00011340	C1C4C2D9	61C1C4C2		2434 DC CL48'ADBR/ADB NF -inf/-2.0 FPCR'
00011370	00000001	F8000001		2435 DC XL16'00000001F800000100000001F8000001'
00011380	C1C4C2D9	61C1C4C2		2436 DC CL48'ADBR/ADB NF -inf/-Dnice FPCR'
000113B0	00000001	F8000001		2437 DC XL16'00000001F800000100000001F8000001'
000113C0	C1C4C2D9	61C1C4C2		2438 DC CL48'ADBR/ADB NF -inf/-0 FPCR'
000113F0	00000001	F8000001		2439 DC XL16'00000001F800000100000001F8000001'
00011400	C1C4C2D9	61C1C4C2		2440 DC CL48'ADBR/ADB NF -inf/+0 FPCR'
00011430	00000001	F8000001		2441 DC XL16'00000001F800000100000001F8000001'
00011440	C1C4C2D9	61C1C4C2		2442 DC CL48'ADBR/ADB NF -inf/+Dnice FPCR'
00011470	00000001	F8000001		2443 DC XL16'00000001F800000100000001F8000001'
00011480	C1C4C2D9	61C1C4C2		2444 DC CL48'ADBR/ADB NF -inf/+2.0 FPCR'
000114B0	00000001	F8000001		2445 DC XL16'00000001F800000100000001F8000001'
000114C0	C1C4C2D9	61C1C4C2		2446 DC CL48'ADBR/ADB NF -inf/+inf FPCR'
000114F0	00800003	F8008003		2447 DC XL16'00800003F800800300800003F8008003'
00011500	C1C4C2D9	61C1C4C2		2448 DC CL48'ADBR/ADB NF -inf/-QNaN FPCR'
00011530	00000003	F8000003		2449 DC XL16'00000003F800000300000003F8000003'
00011540	C1C4C2D9	61C1C4C2		2450 DC CL48'ADBR/ADB NF -inf/+SNaN FPCR'
00011570	00800003	F8008003		2451 DC XL16'00800003F800800300800003F8008003'
00011580	C1C4C2D9	61C1C4C2		2452 DC CL48'ADBR/ADB NF -2.0/-inf FPCR'
000115B0	00000001	F8000001		2453 DC XL16'00000001F800000100000001F8000001'
000115C0	C1C4C2D9	61C1C4C2		2454 DC CL48'ADBR/ADB NF -2.0/-2.0 FPCR'
000115F0	00000001	F8000001		2455 DC XL16'00000001F800000100000001F8000001'
00011600	C1C4C2D9	61C1C4C2		2456 DC CL48'ADBR/ADB NF -2.0/-Dnice FPCR'
00011630	00080001	F8000801		2457 DC XL16'00080001F800080100080001F8000801'
00011640	C1C4C2D9	61C1C4C2		2458 DC CL48'ADBR/ADB NF -2.0/-0 FPCR'
00011670	00000001	F8000001		2459 DC XL16'00000001F800000100000001F8000001'
00011680	C1C4C2D9	61C1C4C2		2460 DC CL48'ADBR/ADB NF -2.0/+0 FPCR'
000116B0	00000001	F8000001		2461 DC XL16'00000001F800000100000001F8000001'
000116C0	C1C4C2D9	61C1C4C2		2462 DC CL48'ADBR/ADB NF -2.0/+Dnice FPCR'
000116F0	00080001	F8000C01		2463 DC XL16'00080001F8000C0100080001F8000C01'
00011700	C1C4C2D9	61C1C4C2		2464 DC CL48'ADBR/ADB NF -2.0/+2.0 FPCR'
00011730	00000000	F8000000		2465 DC XL16'00000000F800000000000000F8000000'
00011740	C1C4C2D9	61C1C4C2		2466 DC CL48'ADBR/ADB NF -2.0/+inf FPCR'
00011770	00000002	F8000002		2467 DC XL16'00000002F800000200000002F8000002'
00011780	C1C4C2D9	61C1C4C2		2468 DC CL48'ADBR/ADB NF -2.0/-QNaN FPCR'
000117B0	00000003	F8000003		2469 DC XL16'00000003F800000300000003F8000003'
000117C0	C1C4C2D9	61C1C4C2		2470 DC CL48'ADBR/ADB NF -2.0/+SNaN FPCR'
000117F0	00800003	F8008003		2471 DC XL16'00800003F800800300800003F8008003'
00011800	C1C4C2D9	61C1C4C2		2472 DC CL48'ADBR/ADB NF -Dnice/-inf FPCR'
00011830	00000001	F8000001		2473 DC XL16'00000001F800000100000001F8000001'
00011840	C1C4C2D9	61C1C4C2		2474 DC CL48'ADBR/ADB NF -Dnice/-2.0 FPCR'
00011870	00080001	F8000801		2475 DC XL16'00080001F800080100080001F8000801'
00011880	C1C4C2D9	61C1C4C2		2476 DC CL48'ADBR/ADB NF -Dnice/-Dnice FPCR'
000118B0	00000001	F8001001		2477 DC XL16'00000001F800100100000001F8001001'
000118C0	C1C4C2D9	61C1C4C2		2478 DC CL48'ADBR/ADB NF -Dnice/-0 FPCR'
000118F0	00000001	F8001001		2479 DC XL16'00000001F800100100000001F8001001'
00011900	C1C4C2D9	61C1C4C2		2480 DC CL48'ADBR/ADB NF -Dnice/+0 FPCR'
00011930	00000001	F8001001		2481 DC XL16'00000001F800100100000001F8001001'
00011940	C1C4C2D9	61C1C4C2		2482 DC CL48'ADBR/ADB NF -Dnice/+Dnice FPCR'
00011970	00000000	F8000000		2483 DC XL16'00000000F800000000000000F8000000'
00011980	C1C4C2D9	61C1C4C2		2484 DC CL48'ADBR/ADB NF -Dnice/+2.0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000119B0	00080002 F8000C02			2485 DC XL16'00080002F8000C0200080002F8000C02'
000119C0	C1C4C2D9 61C1C4C2			2486 DC CL48'ADBR/ADB NF -Dnice/+inf FPCR'
000119F0	00000002 F8000002			2487 DC XL16'00000002F800000200000002F8000002'
00011A00	C1C4C2D9 61C1C4C2			2488 DC CL48'ADBR/ADB NF -Dnice/-QNaN FPCR'
00011A30	00000003 F8000003			2489 DC XL16'00000003F800000300000003F8000003'
00011A40	C1C4C2D9 61C1C4C2			2490 DC CL48'ADBR/ADB NF -Dnice/+SNaN FPCR'
00011A70	00800003 F8008003			2491 DC XL16'00800003F800800300800003F8008003'
00011A80	C1C4C2D9 61C1C4C2			2492 DC CL48'ADBR/ADB NF -0/-inf FPCR'
00011AB0	00000001 F8000001			2493 DC XL16'00000001F800000100000001F8000001'
00011AC0	C1C4C2D9 61C1C4C2			2494 DC CL48'ADBR/ADB NF -0/-2.0 FPCR'
00011AF0	00000001 F8000001			2495 DC XL16'00000001F800000100000001F8000001'
00011B00	C1C4C2D9 61C1C4C2			2496 DC CL48'ADBR/ADB NF -0/-Dnice FPCR'
00011B30	00000001 F8001001			2497 DC XL16'00000001F800100100000001F8001001'
00011B40	C1C4C2D9 61C1C4C2			2498 DC CL48'ADBR/ADB NF -0/-0 FPCR'
00011B70	00000000 F8000000			2499 DC XL16'00000000F800000000000000F8000000'
00011B80	C1C4C2D9 61C1C4C2			2500 DC CL48'ADBR/ADB NF -0/+0 FPCR'
00011BB0	00000000 F8000000			2501 DC XL16'00000000F800000000000000F8000000'
00011BC0	C1C4C2D9 61C1C4C2			2502 DC CL48'ADBR/ADB NF -0/+Dnice FPCR'
00011BF0	00000002 F8001002			2503 DC XL16'00000002F800100200000002F8001002'
00011C00	C1C4C2D9 61C1C4C2			2504 DC CL48'ADBR/ADB NF -0/+2.0 FPCR'
00011C30	00000002 F8000002			2505 DC XL16'00000002F800000200000002F8000002'
00011C40	C1C4C2D9 61C1C4C2			2506 DC CL48'ADBR/ADB NF -0/+inf FPCR'
00011C70	00000002 F8000002			2507 DC XL16'00000002F800000200000002F8000002'
00011C80	C1C4C2D9 61C1C4C2			2508 DC CL48'ADBR/ADB NF -0/-QNaN FPCR'
00011CB0	00000003 F8000003			2509 DC XL16'00000003F800000300000003F8000003'
00011CC0	C1C4C2D9 61C1C4C2			2510 DC CL48'ADBR/ADB NF -0/+SNaN FPCR'
00011CF0	00800003 F8008003			2511 DC XL16'00800003F800800300800003F8008003'
00011D00	C1C4C2D9 61C1C4C2			2512 DC CL48'ADBR/ADB NF +0/-inf FPCR'
00011D30	00000001 F8000001			2513 DC XL16'00000001F800000100000001F8000001'
00011D40	C1C4C2D9 61C1C4C2			2514 DC CL48'ADBR/ADB NF +0/-2.0 FPCR'
00011D70	00000001 F8000001			2515 DC XL16'00000001F800000100000001F8000001'
00011D80	C1C4C2D9 61C1C4C2			2516 DC CL48'ADBR/ADB NF +0/-Dnice FPCR'
00011DB0	00000001 F8001001			2517 DC XL16'00000001F800100100000001F8001001'
00011DC0	C1C4C2D9 61C1C4C2			2518 DC CL48'ADBR/ADB NF +0/-0 FPCR'
00011DF0	00000000 F8000000			2519 DC XL16'00000000F800000000000000F8000000'
00011E00	C1C4C2D9 61C1C4C2			2520 DC CL48'ADBR/ADB NF +0/+0 FPCR'
00011E30	00000000 F8000000			2521 DC XL16'00000000F800000000000000F8000000'
00011E40	C1C4C2D9 61C1C4C2			2522 DC CL48'ADBR/ADB NF +0/+Dnice FPCR'
00011E70	00000002 F8001002			2523 DC XL16'00000002F800100200000002F8001002'
00011E80	C1C4C2D9 61C1C4C2			2524 DC CL48'ADBR/ADB NF +0/+2.0 FPCR'
00011EB0	00000002 F8000002			2525 DC XL16'00000002F800000200000002F8000002'
00011EC0	C1C4C2D9 61C1C4C2			2526 DC CL48'ADBR/ADB NF +0/+inf FPCR'
00011EF0	00000002 F8000002			2527 DC XL16'00000002F800000200000002F8000002'
00011F00	C1C4C2D9 61C1C4C2			2528 DC CL48'ADBR/ADB NF +0/-QNaN FPCR'
00011F30	00000003 F8000003			2529 DC XL16'00000003F800000300000003F8000003'
00011F40	C1C4C2D9 61C1C4C2			2530 DC CL48'ADBR/ADB NF +0/+SNaN FPCR'
00011F70	00800003 F8008003			2531 DC XL16'00800003F800800300800003F8008003'
00011F80	C1C4C2D9 61C1C4C2			2532 DC CL48'ADBR/ADB NF +Dnice/-inf FPCR'
00011FB0	00000001 F8000001			2533 DC XL16'00000001F800000100000001F8000001'
00011FC0	C1C4C2D9 61C1C4C2			2534 DC CL48'ADBR/ADB NF +Dnice/-2.0 FPCR'
00011FF0	00080001 F8000C01			2535 DC XL16'00080001F8000C0100080001F8000C01'
00012000	C1C4C2D9 61C1C4C2			2536 DC CL48'ADBR/ADB NF +Dnice/-Dnice FPCR'
00012030	00000000 F8000000			2537 DC XL16'00000000F800000000000000F8000000'
00012040	C1C4C2D9 61C1C4C2			2538 DC CL48'ADBR/ADB NF +Dnice/-0 FPCR'
00012070	00000002 F8001002			2539 DC XL16'00000002F800100200000002F8001002'
00012080	C1C4C2D9 61C1C4C2			2540 DC CL48'ADBR/ADB NF +Dnice/+0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000120B0	00000002 F8001002			2541 DC XL16'00000002F800100200000002F8001002'
000120C0	C1C4C2D9 61C1C4C2			2542 DC CL48'ADBR/ADB NF +Dnice/+Dnice FPCR'
000120F0	00000002 F8001002			2543 DC XL16'00000002F800100200000002F8001002'
00012100	C1C4C2D9 61C1C4C2			2544 DC CL48'ADBR/ADB NF +Dnice/+2.0 FPCR'
00012130	00080002 F8000802			2545 DC XL16'00080002F800080200080002F8000802'
00012140	C1C4C2D9 61C1C4C2			2546 DC CL48'ADBR/ADB NF +Dnice/+inf FPCR'
00012170	00000002 F8000002			2547 DC XL16'00000002F800000200000002F8000002'
00012180	C1C4C2D9 61C1C4C2			2548 DC CL48'ADBR/ADB NF +Dnice/-QNaN FPCR'
000121B0	00000003 F8000003			2549 DC XL16'00000003F800000300000003F8000003'
000121C0	C1C4C2D9 61C1C4C2			2550 DC CL48'ADBR/ADB NF +Dnice/+SNaN FPCR'
000121F0	00800003 F8008003			2551 DC XL16'00800003F800800300800003F8008003'
00012200	C1C4C2D9 61C1C4C2			2552 DC CL48'ADBR/ADB NF +2.0/-inf FPCR'
00012230	00000001 F8000001			2553 DC XL16'00000001F800000100000001F8000001'
00012240	C1C4C2D9 61C1C4C2			2554 DC CL48'ADBR/ADB NF +2.0/-2.0 FPCR'
00012270	00000000 F8000000			2555 DC XL16'00000000F800000000000000F8000000'
00012280	C1C4C2D9 61C1C4C2			2556 DC CL48'ADBR/ADB NF +2.0/-Dnice FPCR'
000122B0	00080002 F8000C02			2557 DC XL16'00080002F8000C0200080002F8000C02'
000122C0	C1C4C2D9 61C1C4C2			2558 DC CL48'ADBR/ADB NF +2.0/-0 FPCR'
000122F0	00000002 F8000002			2559 DC XL16'00000002F800000200000002F8000002'
00012300	C1C4C2D9 61C1C4C2			2560 DC CL48'ADBR/ADB NF +2.0/+0 FPCR'
00012330	00000002 F8000002			2561 DC XL16'00000002F800000200000002F8000002'
00012340	C1C4C2D9 61C1C4C2			2562 DC CL48'ADBR/ADB NF +2.0/+Dnice FPCR'
00012370	00080002 F8000802			2563 DC XL16'00080002F800080200080002F8000802'
00012380	C1C4C2D9 61C1C4C2			2564 DC CL48'ADBR/ADB NF +2.0/+2.0 FPCR'
000123B0	00000002 F8000002			2565 DC XL16'00000002F800000200000002F8000002'
000123C0	C1C4C2D9 61C1C4C2			2566 DC CL48'ADBR/ADB NF +2.0/+inf FPCR'
000123F0	00000002 F8000002			2567 DC XL16'00000002F800000200000002F8000002'
00012400	C1C4C2D9 61C1C4C2			2568 DC CL48'ADBR/ADB NF +2.0/-QNaN FPCR'
00012430	00000003 F8000003			2569 DC XL16'00000003F800000300000003F8000003'
00012440	C1C4C2D9 61C1C4C2			2570 DC CL48'ADBR/ADB NF +2.0/+SNaN FPCR'
00012470	00800003 F8008003			2571 DC XL16'00800003F800800300800003F8008003'
00012480	C1C4C2D9 61C1C4C2			2572 DC CL48'ADBR/ADB NF +inf/-inf FPCR'
000124B0	00800003 F8008003			2573 DC XL16'00800003F800800300800003F8008003'
000124C0	C1C4C2D9 61C1C4C2			2574 DC CL48'ADBR/ADB NF +inf/-2.0 FPCR'
000124F0	00000002 F8000002			2575 DC XL16'00000002F800000200000002F8000002'
00012500	C1C4C2D9 61C1C4C2			2576 DC CL48'ADBR/ADB NF +inf/-Dnice FPCR'
00012530	00000002 F8000002			2577 DC XL16'00000002F800000200000002F8000002'
00012540	C1C4C2D9 61C1C4C2			2578 DC CL48'ADBR/ADB NF +inf/-0 FPCR'
00012570	00000002 F8000002			2579 DC XL16'00000002F800000200000002F8000002'
00012580	C1C4C2D9 61C1C4C2			2580 DC CL48'ADBR/ADB NF +inf/+0 FPCR'
000125B0	00000002 F8000002			2581 DC XL16'00000002F800000200000002F8000002'
000125C0	C1C4C2D9 61C1C4C2			2582 DC CL48'ADBR/ADB NF +inf/+Dnice FPCR'
000125F0	00000002 F8000002			2583 DC XL16'00000002F800000200000002F8000002'
00012600	C1C4C2D9 61C1C4C2			2584 DC CL48'ADBR/ADB NF +inf/+2.0 FPCR'
00012630	00000002 F8000002			2585 DC XL16'00000002F800000200000002F8000002'
00012640	C1C4C2D9 61C1C4C2			2586 DC CL48'ADBR/ADB NF +inf/+inf FPCR'
00012670	00000002 F8000002			2587 DC XL16'00000002F800000200000002F8000002'
00012680	C1C4C2D9 61C1C4C2			2588 DC CL48'ADBR/ADB NF +inf/-QNaN FPCR'
000126B0	00000003 F8000003			2589 DC XL16'00000003F800000300000003F8000003'
000126C0	C1C4C2D9 61C1C4C2			2590 DC CL48'ADBR/ADB NF +inf/+SNaN FPCR'
000126F0	00800003 F8008003			2591 DC XL16'00800003F800800300800003F8008003'
00012700	C1C4C2D9 61C1C4C2			2592 DC CL48'ADBR/ADB NF -QNaN/-inf FPCR'
00012730	00000003 F8000003			2593 DC XL16'00000003F800000300000003F8000003'
00012740	C1C4C2D9 61C1C4C2			2594 DC CL48'ADBR/ADB NF -QNaN/-2.0 FPCR'
00012770	00000003 F8000003			2595 DC XL16'00000003F800000300000003F8000003'
00012780	C1C4C2D9 61C1C4C2			2596 DC CL48'ADBR/ADB NF -QNaN/-Dnice FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000127B0	00000003 F8000003			2597 DC XL16'00000003F800000300000003F8000003'
000127C0	C1C4C2D9 61C1C4C2			2598 DC CL48'ADBR/ADB NF -QNaN/-0 FPCR'
000127F0	00000003 F8000003			2599 DC XL16'00000003F800000300000003F8000003'
00012800	C1C4C2D9 61C1C4C2			2600 DC CL48'ADBR/ADB NF -QNaN/+0 FPCR'
00012830	00000003 F8000003			2601 DC XL16'00000003F800000300000003F8000003'
00012840	C1C4C2D9 61C1C4C2			2602 DC CL48'ADBR/ADB NF -QNaN/+Dnice FPCR'
00012870	00000003 F8000003			2603 DC XL16'00000003F800000300000003F8000003'
00012880	C1C4C2D9 61C1C4C2			2604 DC CL48'ADBR/ADB NF -QNaN/+2.0 FPCR'
000128B0	00000003 F8000003			2605 DC XL16'00000003F800000300000003F8000003'
000128C0	C1C4C2D9 61C1C4C2			2606 DC CL48'ADBR/ADB NF -QNaN/+inf FPCR'
000128F0	00000003 F8000003			2607 DC XL16'00000003F800000300000003F8000003'
00012900	C1C4C2D9 61C1C4C2			2608 DC CL48'ADBR/ADB NF -QNaN/-QNaN FPCR'
00012930	00000003 F8000003			2609 DC XL16'00000003F800000300000003F8000003'
00012940	C1C4C2D9 61C1C4C2			2610 DC CL48'ADBR/ADB NF -QNaN/+SNaN FPCR'
00012970	00800003 F8008003			2611 DC XL16'00800003F800800300800003F8008003'
00012980	C1C4C2D9 61C1C4C2			2612 DC CL48'ADBR/ADB NF +SNaN/-inf FPCR'
000129B0	00800003 F8008003			2613 DC XL16'00800003F800800300800003F8008003'
000129C0	C1C4C2D9 61C1C4C2			2614 DC CL48'ADBR/ADB NF +SNaN/-2.0 FPCR'
000129F0	00800003 F8008003			2615 DC XL16'00800003F800800300800003F8008003'
00012A00	C1C4C2D9 61C1C4C2			2616 DC CL48'ADBR/ADB NF +SNaN/-Dnice FPCR'
00012A30	00800003 F8008003			2617 DC XL16'00800003F800800300800003F8008003'
00012A40	C1C4C2D9 61C1C4C2			2618 DC CL48'ADBR/ADB NF +SNaN/-0 FPCR'
00012A70	00800003 F8008003			2619 DC XL16'00800003F800800300800003F8008003'
00012A80	C1C4C2D9 61C1C4C2			2620 DC CL48'ADBR/ADB NF +SNaN/+0 FPCR'
00012AB0	00800003 F8008003			2621 DC XL16'00800003F800800300800003F8008003'
00012AC0	C1C4C2D9 61C1C4C2			2622 DC CL48'ADBR/ADB NF +SNaN/+Dnice FPCR'
00012AF0	00800003 F8008003			2623 DC XL16'00800003F800800300800003F8008003'
00012B00	C1C4C2D9 61C1C4C2			2624 DC CL48'ADBR/ADB NF +SNaN/+2.0 FPCR'
00012B30	00800003 F8008003			2625 DC XL16'00800003F800800300800003F8008003'
00012B40	C1C4C2D9 61C1C4C2			2626 DC CL48'ADBR/ADB NF +SNaN/+inf FPCR'
00012B70	00800003 F8008003			2627 DC XL16'00800003F800800300800003F8008003'
00012B80	C1C4C2D9 61C1C4C2			2628 DC CL48'ADBR/ADB NF +SNaN/-QNaN FPCR'
00012BB0	00800003 F8008003			2629 DC XL16'00800003F800800300800003F8008003'
00012BC0	C1C4C2D9 61C1C4C2			2630 DC CL48'ADBR/ADB NF +SNaN/+SNaN FPCR'
00012BF0	00800003 F8008003			2631 DC XL16'00800003F800800300800003F8008003'
		00000064	00000001	2632 LBFPNFFL_NUM EQU (*-LBFPNFFL_GOOD)/64
				2633 *
				2634 *
		00012C00	00000001	2635 LBFPNFFL_GOOD EQU *
00012C00	C1C4C2D9 40C640D6			2636 DC CL48'ADBR F Ovfl'
00012C30	7FFFFFFF FFFFFFFF			2637 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00012C40	C1C4C240 C640D6A5			2638 DC CL48'ADB F Ovfl'
00012C70	7FFFFFFF FFFFFFFF			2639 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00012C80	C1C4C2D9 40C640E4			2640 DC CL48'ADBR F Ufl 1'
00012CB0	000FFFFFF FFFFFFFF			2641 DC XL16'000FFFFFFF600FFFFFFF600FFFFFFF'
00012CC0	C1C4C240 C640E486			2642 DC CL48'ADB F Ufl 1'
00012CF0	000FFFFFF FFFFFFFF			2643 DC XL16'000FFFFFFF600FFFFFFF600FFFFFFF'
00012D00	C1C4C2D9 40C640E4			2644 DC CL48'ADBR F Ufl 2'
00012D30	0008F0F0 00000000			2645 DC XL16'0008F0F0000000006001E1E00000000'
00012D40	C1C4C240 C640E486			2646 DC CL48'ADB F Ufl 2'
00012D70	0008F0F0 00000000			2647 DC XL16'0008F0F0000000006001E1E00000000'
00012D80	C1C4C2D9 40C640D5			2648 DC CL48'ADBR F Nmin'
00012DB0	00100000 00000000			2649 DC XL16'00100000000000000010000000000000'
00012DC0	C1C4C240 C640D594			2650 DC CL48'ADB F Nmin'
00012DF0	00100000 00000000			2651 DC XL16'00100000000000000010000000000000'
00012E00	C1C4C2D9 40C640C9			2652 DC CL48'ADBR F Incr'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00012E30	3FF00000 00000001			2653 DC XL16'3FF00000000000013FF0000000000001'
00012E40	C1C4C240 C640C995			2654 DC CL48'ADB F Incr'
00012E70	3FF00000 00000001			2655 DC XL16'3FF00000000000013FF0000000000001'
00012E80	C1C4C2D9 40C640E3			2656 DC CL48'ADBR F Trun'
00012EB0	3FF00000 00000000			2657 DC XL16'3FF00000000000003FF0000000000000'
00012EC0	C1C4C240 C640E399			2658 DC CL48'ADB F Trun'
00012EF0	3FF00000 00000000			2659 DC XL16'3FF00000000000003FF0000000000000'
		0000000C	00000001	2660 LBFPOUT_NUM EQU (*-LBFPOUT_GOOD)/64
				2661 *
				2662 *
		00012F00	00000001	2663 LBFPFLGS_GOOD EQU *
00012F00	C1C4C2D9 61C1C4C2			2664 DC CL48'ADBR/ADB F Ovfl FPCR'
00012F30	00000003 F8000003			2665 DC XL16'00000003F800000300000003F8000003'
00012F40	C1C4C2D9 61C1C4C2			2666 DC CL48'ADBR/ADB F Ufl 1 FPCR'
00012F70	00000002 F8001002			2667 DC XL16'00000002F800100200000002F8001002'
00012F80	C1C4C2D9 61C1C4C2			2668 DC CL48'ADBR/ADB F Ufl 2 FPCR'
00012FB0	00000002 F8001002			2669 DC XL16'00000002F800100200000002F8001002'
00012FC0	C1C4C2D9 61C1C4C2			2670 DC CL48'ADBR/ADB F Nmin FPCR'
00012FF0	00000002 F8000002			2671 DC XL16'00000002F800000200000002F8000002'
00013000	C1C4C2D9 61C1C4C2			2672 DC CL48'ADBR/ADB F Incr FPCR'
00013030	00080002 F8000C02			2673 DC XL16'00080002F8000C0200080002F8000C02'
00013040	C1C4C2D9 61C1C4C2			2674 DC CL48'ADBR/ADB F Trun FPCR'
00013070	00080002 F8000802			2675 DC XL16'00080002F800080200080002F8000802'
		00000006	00000001	2676 LBFPFLGS_NUM EQU (*-LBFPFLGS_GOOD)/64
				2677 *
				2678 *
		00013080	00000001	2679 LBFPOMO_GOOD EQU *
00013080	C1C4C2D9 61C1C4C2			2680 DC CL48'ADBR/ADB RM +NZ RNTE'
000130B0	3FF00000 00000000			2681 DC XL16'3FF00000000000003FF0000000000000'
000130C0	C1C4C2D9 61C1C4C2			2682 DC CL48'ADBR/ADB RM +NZ RZ'
000130F0	3FF00000 00000000			2683 DC XL16'3FF00000000000003FF0000000000000'
00013100	C1C4C2D9 61C1C4C2			2684 DC CL48'ADBR/ADB RM +NZ RP'
00013130	3FF00000 00000001			2685 DC XL16'3FF00000000000013FF0000000000001'
00013140	C1C4C2D9 61C1C4C2			2686 DC CL48'ADBR/ADB RM +NZ RM'
00013170	3FF00000 00000000			2687 DC XL16'3FF00000000000003FF0000000000000'
00013180	C1C4C2D9 61C1C4C2			2688 DC CL48'ADBR/ADB RM +NZ RFS'
000131B0	3FF00000 00000001			2689 DC XL16'3FF00000000000013FF0000000000001'
000131C0	C1C4C2D9 61C1C4C2			2690 DC CL48'ADBR/ADB RM -NZ RNTE'
000131F0	BFF00000 00000000			2691 DC XL16'BFF0000000000000BFF0000000000000'
00013200	C1C4C2D9 61C1C4C2			2692 DC CL48'ADBR/ADB RM -NZ RZ'
00013230	BFF00000 00000000			2693 DC XL16'BFF0000000000000BFF0000000000000'
00013240	C1C4C2D9 61C1C4C2			2694 DC CL48'ADBR/ADB RM -NZ RP'
00013270	BFF00000 00000000			2695 DC XL16'BFF0000000000000BFF0000000000000'
00013280	C1C4C2D9 61C1C4C2			2696 DC CL48'ADBR/ADB RM -NZ RM'
000132B0	BFF00000 00000001			2697 DC XL16'BFF0000000000001BFF0000000000001'
000132C0	C1C4C2D9 61C1C4C2			2698 DC CL48'ADBR/ADB RM -NZ RFS'
000132F0	BFF00000 00000001			2699 DC XL16'BFF0000000000001BFF0000000000001'
00013300	C1C4C2D9 61C1C4C2			2700 DC CL48'ADBR/ADB RM +NA RNTE'
00013330	3FF00000 00000001			2701 DC XL16'3FF00000000000013FF0000000000001'
00013340	C1C4C2D9 61C1C4C2			2702 DC CL48'ADBR/ADB RM +NA RZ'
00013370	3FF00000 00000000			2703 DC XL16'3FF00000000000003FF0000000000000'
00013380	C1C4C2D9 61C1C4C2			2704 DC CL48'ADBR/ADB RM +NA RP'
000133B0	3FF00000 00000001			2705 DC XL16'3FF00000000000013FF0000000000001'
000133C0	C1C4C2D9 61C1C4C2			2706 DC CL48'ADBR/ADB RM +NA RM'
000133F0	3FF00000 00000000			2707 DC XL16'3FF00000000000003FF0000000000000'
00013400	C1C4C2D9 61C1C4C2			2708 DC CL48'ADBR/ADB RM +NA RFS'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00013430	3FF00000 00000001			2709 DC XL16'3FF000000000000013FF0000000000001'
00013440	C1C4C2D9 61C1C4C2			2710 DC CL48'ADBR/ADB RM -NA RNTE'
00013470	BFF00000 00000001			2711 DC XL16'BFF00000000000001BFF0000000000001'
00013480	C1C4C2D9 61C1C4C2			2712 DC CL48'ADBR/ADB RM -NA RZ'
000134B0	BFF00000 00000000			2713 DC XL16'BFF0000000000000BFF0000000000000'
000134C0	C1C4C2D9 61C1C4C2			2714 DC CL48'ADBR/ADB RM -NA RP'
000134F0	BFF00000 00000000			2715 DC XL16'BFF0000000000000BFF0000000000000'
00013500	C1C4C2D9 61C1C4C2			2716 DC CL48'ADBR/ADB RM -NA RM'
00013530	BFF00000 00000001			2717 DC XL16'BFF0000000000001BFF0000000000001'
00013540	C1C4C2D9 61C1C4C2			2718 DC CL48'ADBR/ADB RM -NA RFS'
00013570	BFF00000 00000001			2719 DC XL16'BFF0000000000001BFF0000000000001'
00013580	C1C4C2D9 61C1C4C2			2720 DC CL48'ADBR/ADB RM +TZ RNTE'
000135B0	3FF00000 00000000			2721 DC XL16'3FF00000000000003FF0000000000000'
000135C0	C1C4C2D9 61C1C4C2			2722 DC CL48'ADBR/ADB RM +TZ RZ'
000135F0	3FF00000 00000000			2723 DC XL16'3FF00000000000003FF0000000000000'
00013600	C1C4C2D9 61C1C4C2			2724 DC CL48'ADBR/ADB RM +TZ RP'
00013630	3FF00000 00000001			2725 DC XL16'3FF00000000000013FF0000000000001'
00013640	C1C4C2D9 61C1C4C2			2726 DC CL48'ADBR/ADB RM +TZ RM'
00013670	3FF00000 00000000			2727 DC XL16'3FF00000000000003FF0000000000000'
00013680	C1C4C2D9 61C1C4C2			2728 DC CL48'ADBR/ADB RM +TZ RFS'
000136B0	3FF00000 00000001			2729 DC XL16'3FF00000000000013FF0000000000001'
000136C0	C1C4C2D9 61C1C4C2			2730 DC CL48'ADBR/ADB RM -TZ RNTE'
000136F0	BFF00000 00000000			2731 DC XL16'BFF0000000000000BFF0000000000000'
00013700	C1C4C2D9 61C1C4C2			2732 DC CL48'ADBR/ADB RM -TZ RZ'
00013730	BFF00000 00000000			2733 DC XL16'BFF0000000000000BFF0000000000000'
00013740	C1C4C2D9 61C1C4C2			2734 DC CL48'ADBR/ADB RM -TZ RP'
00013770	BFF00000 00000000			2735 DC XL16'BFF0000000000000BFF0000000000000'
00013780	C1C4C2D9 61C1C4C2			2736 DC CL48'ADBR/ADB RM -TZ RM'
000137B0	BFF00000 00000001			2737 DC XL16'BFF0000000000001BFF0000000000001'
000137C0	C1C4C2D9 61C1C4C2			2738 DC CL48'ADBR/ADB RM -TZ RFS'
000137F0	BFF00000 00000001			2739 DC XL16'BFF0000000000001BFF0000000000001'
00013800	C1C4C2D9 61C1C4C2			2740 DC CL48'ADBR/ADB RM +TA RNTE'
00013830	3FF00000 00000002			2741 DC XL16'3FF00000000000023FF0000000000002'
00013840	C1C4C2D9 61C1C4C2			2742 DC CL48'ADBR/ADB RM +TA RZ'
00013870	3FF00000 00000001			2743 DC XL16'3FF00000000000013FF0000000000001'
00013880	C1C4C2D9 61C1C4C2			2744 DC CL48'ADBR/ADB RM +TA RP'
000138B0	3FF00000 00000002			2745 DC XL16'3FF00000000000023FF0000000000002'
000138C0	C1C4C2D9 61C1C4C2			2746 DC CL48'ADBR/ADB RM +TA RM'
000138F0	3FF00000 00000001			2747 DC XL16'3FF00000000000013FF0000000000001'
00013900	C1C4C2D9 61C1C4C2			2748 DC CL48'ADBR/ADB RM +TA RFS'
00013930	3FF00000 00000001			2749 DC XL16'3FF00000000000013FF0000000000001'
00013940	C1C4C2D9 61C1C4C2			2750 DC CL48'ADBR/ADB RM -TA RNTE'
00013970	BFF00000 00000002			2751 DC XL16'BFF0000000000002BFF0000000000002'
00013980	C1C4C2D9 61C1C4C2			2752 DC CL48'ADBR/ADB RM -TA RZ'
000139B0	BFF00000 00000001			2753 DC XL16'BFF0000000000001BFF0000000000001'
000139C0	C1C4C2D9 61C1C4C2			2754 DC CL48'ADBR/ADB RM -TA RP'
000139F0	BFF00000 00000001			2755 DC XL16'BFF0000000000001BFF0000000000001'
00013A00	C1C4C2D9 61C1C4C2			2756 DC CL48'ADBR/ADB RM -TA RM'
00013A30	BFF00000 00000002			2757 DC XL16'BFF0000000000002BFF0000000000002'
00013A40	C1C4C2D9 61C1C4C2			2758 DC CL48'ADBR/ADB RM -TA RFS'
00013A70	BFF00000 00000001			2759 DC XL16'BFF0000000000001BFF0000000000001'
		00000028	00000001	2760 LBFPRMO_NUM EQU (*-LBFPRMO_GOOD)/64
				2761 *
				2762 *
		00013A80	00000001	2763 LBFPRMOF_GOOD EQU *
00013A80	C1C4C2D9 61C1C4C2			2764 DC CL48'ADBR/ADB RM +NZ RNTE, RZ FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00013AB0	00080002 00080002			2765 DC XL16'00080002000800020008000200080002'
00013AC0	C1C4C2D9 61C1C4C2			2766 DC CL48'ADBR/ADB RM +NZ RP, RM FPCR'
00013AF0	00080002 00080002			2767 DC XL16'00080002000800020008000200080002'
00013B00	C1C4C2D9 61C1C4C2			2768 DC CL48'ADBR/ADB RM +NZ RFS FPCR'
00013B30	00080002 00080002			2769 DC XL16'00080002000800020000000000000000'
00013B40	C1C4C2D9 61C1C4C2			2770 DC CL48'ADBR/ADB RM +NZ RNTE, RZ FPCR'
00013B70	00080001 00080001			2771 DC XL16'00080001000800010008000100080001'
00013B80	C1C4C2D9 61C1C4C2			2772 DC CL48'ADBR/ADB RM +NZ RP, RM FPCR'
00013BB0	00080001 00080001			2773 DC XL16'00080001000800010008000100080001'
00013BC0	C1C4C2D9 61C1C4C2			2774 DC CL48'ADBR/ADB RM -NZ RFS FPCR'
00013BF0	00080001 00080001			2775 DC XL16'00080001000800010000000000000000'
00013C00	C1C4C2D9 61C1C4C2			2776 DC CL48'ADBR/ADB RM -NZ RNTE, RZ FPCR'
00013C30	00080002 00080002			2777 DC XL16'00080002000800020008000200080002'
00013C40	C1C4C2D9 61C1C4C2			2778 DC CL48'ADBR/ADB RM -NZ RP, RM FPCR'
00013C70	00080002 00080002			2779 DC XL16'00080002000800020008000200080002'
00013C80	C1C4C2D9 61C1C4C2			2780 DC CL48'ADBR/ADB RM -NZ RFS FPCR'
00013CB0	00080002 00080002			2781 DC XL16'00080002000800020000000000000000'
00013CC0	C1C4C2D9 61C1C4C2			2782 DC CL48'ADBR/ADB RM -NZ RNTE, RZ FPCR'
00013CF0	00080001 00080001			2783 DC XL16'00080001000800010008000100080001'
00013D00	C1C4C2D9 61C1C4C2			2784 DC CL48'ADBR/ADB RM -NA RP, RM FPCR'
00013D30	00080001 00080001			2785 DC XL16'00080001000800010008000100080001'
00013D40	C1C4C2D9 61C1C4C2			2786 DC CL48'ADBR/ADB RM -NA RFS FPCR'
00013D70	00080001 00080001			2787 DC XL16'00080001000800010000000000000000'
00013D80	C1C4C2D9 61C1C4C2			2788 DC CL48'ADBR/ADB RM +TZ RNTE, RZ FPCR'
00013DB0	00080002 00080002			2789 DC XL16'00080002000800020008000200080002'
00013DC0	C1C4C2D9 61C1C4C2			2790 DC CL48'ADBR/ADB RM +TZ RP, RM FPCR'
00013DF0	00080002 00080002			2791 DC XL16'00080002000800020008000200080002'
00013E00	C1C4C2D9 61C1C4C2			2792 DC CL48'ADBR/ADB RM +TZ RFS FPCR'
00013E30	00080002 00080002			2793 DC XL16'00080002000800020000000000000000'
00013E40	C1C4C2D9 61C1C4C2			2794 DC CL48'ADBR/ADB RM -TZ RNTE, RZ FPCR'
00013E70	00080001 00080001			2795 DC XL16'00080001000800010008000100080001'
00013E80	C1C4C2D9 61C1C4C2			2796 DC CL48'ADBR/ADB RM -TZ RP, RM FPCR'
00013EB0	00080001 00080001			2797 DC XL16'00080001000800010008000100080001'
00013EC0	C1C4C2D9 61C1C4C2			2798 DC CL48'ADBR/ADB RM -TZ RFS FPCR'
00013EF0	00080001 00080001			2799 DC XL16'00080001000800010000000000000000'
00013F00	C1C4C2D9 61C1C4C2			2800 DC CL48'ADBR/ADB RM +TA RNTE, RZ FPCR'
00013F30	00080002 00080002			2801 DC XL16'00080002000800020008000200080002'
00013F40	C1C4C2D9 61C1C4C2			2802 DC CL48'ADBR/ADB RM +TA RP, RM FPCR'
00013F70	00080002 00080002			2803 DC XL16'00080002000800020008000200080002'
00013F80	C1C4C2D9 61C1C4C2			2804 DC CL48'ADBR/ADB RM +TA RFS FPCR'
00013FB0	00080002 00080002			2805 DC XL16'00080002000800020000000000000000'
00013FC0	C1C4C2D9 61C1C4C2			2806 DC CL48'ADBR/ADB RM -TA RNTE, RZ FPCR'
00013FF0	00080001 00080001			2807 DC XL16'00080001000800010008000100080001'
00014000	C1C4C2D9 61C1C4C2			2808 DC CL48'ADBR/ADB RM -TA RP, RM FPCR'
00014030	00080001 00080001			2809 DC XL16'00080001000800010008000100080001'
00014040	C1C4C2D9 61C1C4C2			2810 DC CL48'ADBR/ADB RM -TA RFS FPCR'
00014070	00080001 00080001			2811 DC XL16'00080001000800010000000000000000'
		00000018	00000001	2812 LBFPRMOF_NUM EQU (*-LBFPRMOF_GOOD)/64
				2813 *
				2814 *
		00014080	00000001	2815 XBFPNFOT_GOOD EQU *
00014080	C1E7C2D9 40D5C640			2816 DC CL48'AXBR NF -inf/-inf NT'
000140B0	FFFF0000 00000000			2817 DC XL16'FFFF0000000000000000000000000000'
000140C0	C1E7C2D9 40D5C640			2818 DC CL48'AXBR NF -inf/-inf Tr'
000140F0	FFFF0000 00000000			2819 DC XL16'FFFF0000000000000000000000000000'
00014100	C1E7C2D9 40D5C640			2820 DC CL48'AXBR NF -inf/-2.0 NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00014130	FFFF0000 00000000			2821 DC XL16'FFFF0000000000000000000000000000'
00014140	C1E7C2D9 40D5C640			2822 DC CL48'AXBR NF -inf/-2.0 Tr'
00014170	FFFF0000 00000000			2823 DC XL16'FFFF0000000000000000000000000000'
00014180	C1E7C2D9 40D5C640			2824 DC CL48'AXBR NF -inf/-Dnice NT'
000141B0	FFFF0000 00000000			2825 DC XL16'FFFF0000000000000000000000000000'
000141C0	C1E7C2D9 40D5C640			2826 DC CL48'AXBR NF -inf/-Dnice Tr'
000141F0	FFFF0000 00000000			2827 DC XL16'FFFF0000000000000000000000000000'
00014200	C1E7C2D9 40D5C640			2828 DC CL48'AXBR NF -inf/-0 NT'
00014230	FFFF0000 00000000			2829 DC XL16'FFFF0000000000000000000000000000'
00014240	C1E7C2D9 40D5C640			2830 DC CL48'AXBR NF -inf/-0 Tr'
00014270	FFFF0000 00000000			2831 DC XL16'FFFF0000000000000000000000000000'
00014280	C1E7C2D9 40D5C640			2832 DC CL48'AXBR NF -inf/+0 NT'
000142B0	FFFF0000 00000000			2833 DC XL16'FFFF0000000000000000000000000000'
000142C0	C1E7C2D9 40D5C640			2834 DC CL48'AXBR NF -inf/+0 Tr'
000142F0	FFFF0000 00000000			2835 DC XL16'FFFF0000000000000000000000000000'
00014300	C1E7C2D9 40D5C640			2836 DC CL48'AXBR NF -inf/+Dnice NT'
00014330	FFFF0000 00000000			2837 DC XL16'FFFF0000000000000000000000000000'
00014340	C1E7C2D9 40D5C640			2838 DC CL48'AXBR NF -inf/+Dnice Tr'
00014370	FFFF0000 00000000			2839 DC XL16'FFFF0000000000000000000000000000'
00014380	C1E7C2D9 40D5C640			2840 DC CL48'AXBR NF -inf/+2.0 NT'
000143B0	FFFF0000 00000000			2841 DC XL16'FFFF0000000000000000000000000000'
000143C0	C1E7C2D9 40D5C640			2842 DC CL48'AXBR NF -inf/+2.0 Tr'
000143F0	FFFF0000 00000000			2843 DC XL16'FFFF0000000000000000000000000000'
00014400	C1E7C2D9 40D5C640			2844 DC CL48'AXBR NF -inf/+inf NT'
00014430	7FFF8000 00000000			2845 DC XL16'7FFF8000000000000000000000000000'
00014440	C1E7C2D9 40D5C640			2846 DC CL48'AXBR NF -inf/+inf Tr'
00014470	FFFF0000 00000000			2847 DC XL16'FFFF0000000000000000000000000000'
00014480	C1E7C2D9 40D5C640			2848 DC CL48'AXBR NF -inf/-QNaN NT'
000144B0	FFFF8B00 00000000			2849 DC XL16'FFFF8B00000000000000000000000000'
000144C0	C1E7C2D9 40D5C640			2850 DC CL48'AXBR NF -inf/-QNaN Tr'
000144F0	FFFF8B00 00000000			2851 DC XL16'FFFF8B00000000000000000000000000'
00014500	C1E7C2D9 40D5C640			2852 DC CL48'AXBR NF -inf/+SNaN NT'
00014530	7FFF8A00 00000000			2853 DC XL16'7FFF8A00000000000000000000000000'
00014540	C1E7C2D9 40D5C640			2854 DC CL48'AXBR NF -inf/+SNaN Tr'
00014570	FFFF0000 00000000			2855 DC XL16'FFFF0000000000000000000000000000'
00014580	C1E7C2D9 40D5C640			2856 DC CL48'AXBR NF -2.0/-inf NT'
000145B0	FFFF0000 00000000			2857 DC XL16'FFFF0000000000000000000000000000'
000145C0	C1E7C2D9 40D5C640			2858 DC CL48'AXBR NF -2.0/-inf Tr'
000145F0	FFFF0000 00000000			2859 DC XL16'FFFF0000000000000000000000000000'
00014600	C1E7C2D9 40D5C640			2860 DC CL48'AXBR NF -2.0/-2.0 NT'
00014630	C0010000 00000000			2861 DC XL16'C0010000000000000000000000000000'
00014640	C1E7C2D9 40D5C640			2862 DC CL48'AXBR NF -2.0/-2.0 Tr'
00014670	C0010000 00000000			2863 DC XL16'C0010000000000000000000000000000'
00014680	C1E7C2D9 40D5C640			2864 DC CL48'AXBR NF -2.0/-Dnice NT'
000146B0	C0000000 00000000			2865 DC XL16'C0000000000000000000000000000000'
000146C0	C1E7C2D9 40D5C640			2866 DC CL48'AXBR NF -2.0/-Dnice Tr'
000146F0	C0000000 00000000			2867 DC XL16'C0000000000000000000000000000000'
00014700	C1E7C2D9 40D5C640			2868 DC CL48'AXBR NF -2.0/-0 NT'
00014730	C0000000 00000000			2869 DC XL16'C0000000000000000000000000000000'
00014740	C1E7C2D9 40D5C640			2870 DC CL48'AXBR NF -2.0/-0 Tr'
00014770	C0000000 00000000			2871 DC XL16'C0000000000000000000000000000000'
00014780	C1E7C2D9 40D5C640			2872 DC CL48'AXBR NF -2.0/+0 NT'
000147B0	C0000000 00000000			2873 DC XL16'C0000000000000000000000000000000'
000147C0	C1E7C2D9 40D5C640			2874 DC CL48'AXBR NF -2.0/+0 Tr'
000147F0	C0000000 00000000			2875 DC XL16'C0000000000000000000000000000000'
00014800	C1E7C2D9 40D5C640			2876 DC CL48'AXBR NF -2.0/+Dnice NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00014830	C0000000 00000000			2877	DC XL16'C0000000000000000000000000000000'
00014840	C1E7C2D9 40D5C640			2878	DC CL48'AXBR NF -2.0/+Dnice Tr'
00014870	C0000000 00000000			2879	DC XL16'C0000000000000000000000000000000'
00014880	C1E7C2D9 40D5C640			2880	DC CL48'AXBR NF -2.0/+2.0 NT'
000148B0	00000000 00000000			2881	DC XL16'00000000000000000000000000000000'
000148C0	C1E7C2D9 40D5C640			2882	DC CL48'AXBR NF -2.0/+2.0 Tr'
000148F0	00000000 00000000			2883	DC XL16'00000000000000000000000000000000'
00014900	C1E7C2D9 40D5C640			2884	DC CL48'AXBR NF -2.0/+inf NT'
00014930	7FFF0000 00000000			2885	DC XL16'7FFF0000000000000000000000000000'
00014940	C1E7C2D9 40D5C640			2886	DC CL48'AXBR NF -2.0/+inf Tr'
00014970	7FFF0000 00000000			2887	DC XL16'7FFF0000000000000000000000000000'
00014980	C1E7C2D9 40D5C640			2888	DC CL48'AXBR NF -2.0/-QNaN NT'
000149B0	FFFF8B00 00000000			2889	DC XL16'FFFF8B00000000000000000000000000'
000149C0	C1E7C2D9 40D5C640			2890	DC CL48'AXBR NF -2.0/-QNaN Tr'
000149F0	FFFF8B00 00000000			2891	DC XL16'FFFF8B00000000000000000000000000'
00014A00	C1E7C2D9 40D5C640			2892	DC CL48'AXBR NF -2.0/+SNaN NT'
00014A30	7FFF8A00 00000000			2893	DC XL16'7FFF8A00000000000000000000000000'
00014A40	C1E7C2D9 40D5C640			2894	DC CL48'AXBR NF -2.0/+SNaN Tr'
00014A70	C0000000 00000000			2895	DC XL16'C0000000000000000000000000000000'
00014A80	C1E7C2D9 40D5C640			2896	DC CL48'AXBR NF -Dnice/-inf NT'
00014AB0	FFFF0000 00000000			2897	DC XL16'FFFF0000000000000000000000000000'
00014AC0	C1E7C2D9 40D5C640			2898	DC CL48'AXBR NF -Dnice/-inf Tr'
00014AF0	FFFF0000 00000000			2899	DC XL16'FFFF0000000000000000000000000000'
00014B00	C1E7C2D9 40D5C640			2900	DC CL48'AXBR NF -Dnice/-2.0 NT'
00014B30	C0000000 00000000			2901	DC XL16'C0000000000000000000000000000000'
00014B40	C1E7C2D9 40D5C640			2902	DC CL48'AXBR NF -Dnice/-2.0 Tr'
00014B70	C0000000 00000000			2903	DC XL16'C0000000000000000000000000000000'
00014B80	C1E7C2D9 40D5C640			2904	DC CL48'AXBR NF -Dnice/-Dnice NT'
00014BB0	80002000 00000000			2905	DC XL16'80002000000000000000000000000000'
00014BC0	C1E7C2D9 40D5C640			2906	DC CL48'AXBR NF -Dnice/-Dnice Tr'
00014BF0	DFFE0000 00000000			2907	DC XL16'DFFE0000000000000000000000000000'
00014C00	C1E7C2D9 40D5C640			2908	DC CL48'AXBR NF -Dnice/-0 NT'
00014C30	80001000 00000000			2909	DC XL16'80001000000000000000000000000000'
00014C40	C1E7C2D9 40D5C640			2910	DC CL48'AXBR NF -Dnice/-0 Tr'
00014C70	DFFD0000 00000000			2911	DC XL16'DFFD0000000000000000000000000000'
00014C80	C1E7C2D9 40D5C640			2912	DC CL48'AXBR NF -Dnice/+0 NT'
00014CB0	80001000 00000000			2913	DC XL16'80001000000000000000000000000000'
00014CC0	C1E7C2D9 40D5C640			2914	DC CL48'AXBR NF -Dnice/+0 Tr'
00014CF0	DFFD0000 00000000			2915	DC XL16'DFFD0000000000000000000000000000'
00014D00	C1E7C2D9 40D5C640			2916	DC CL48'AXBR NF -Dnice/+Dnice NT'
00014D30	00000000 00000000			2917	DC XL16'00000000000000000000000000000000'
00014D40	C1E7C2D9 40D5C640			2918	DC CL48'AXBR NF -Dnice/+Dnice Tr'
00014D70	00000000 00000000			2919	DC XL16'00000000000000000000000000000000'
00014D80	C1E7C2D9 40D5C640			2920	DC CL48'AXBR NF -Dnice/+2.0 NT'
00014DB0	40000000 00000000			2921	DC XL16'40000000000000000000000000000000'
00014DC0	C1E7C2D9 40D5C640			2922	DC CL48'AXBR NF -Dnice/+2.0 Tr'
00014DF0	40000000 00000000			2923	DC XL16'40000000000000000000000000000000'
00014E00	C1E7C2D9 40D5C640			2924	DC CL48'AXBR NF -Dnice/+inf NT'
00014E30	7FFF0000 00000000			2925	DC XL16'7FFF0000000000000000000000000000'
00014E40	C1E7C2D9 40D5C640			2926	DC CL48'AXBR NF -Dnice/+inf Tr'
00014E70	7FFF0000 00000000			2927	DC XL16'7FFF0000000000000000000000000000'
00014E80	C1E7C2D9 40D5C640			2928	DC CL48'AXBR NF -Dnice/-QNaN NT'
00014EB0	FFFF8B00 00000000			2929	DC XL16'FFFF8B00000000000000000000000000'
00014EC0	C1E7C2D9 40D5C640			2930	DC CL48'AXBR NF -Dnice/-QNaN Tr'
00014EF0	FFFF8B00 00000000			2931	DC XL16'FFFF8B00000000000000000000000000'
00014F00	C1E7C2D9 40D5C640			2932	DC CL48'AXBR NF -Dnice/+SNaN NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00015630	00000000 00000000			2989 DC XL16'00000000000000000000000000000000'
00015640	C1E7C2D9 40D5C640			2990 DC CL48'AXBR NF +0/-0 Tr'
00015670	00000000 00000000			2991 DC XL16'00000000000000000000000000000000'
00015680	C1E7C2D9 40D5C640			2992 DC CL48'AXBR NF +0/+0 NT'
000156B0	00000000 00000000			2993 DC XL16'00000000000000000000000000000000'
000156C0	C1E7C2D9 40D5C640			2994 DC CL48'AXBR NF +0/+0 Tr'
000156F0	00000000 00000000			2995 DC XL16'00000000000000000000000000000000'
00015700	C1E7C2D9 40D5C640			2996 DC CL48'AXBR NF +0/+Dnice NT'
00015730	00001000 00000000			2997 DC XL16'00001000000000000000000000000000'
00015740	C1E7C2D9 40D5C640			2998 DC CL48'AXBR NF +0/+Dnice Tr'
00015770	5FFD0000 00000000			2999 DC XL16'5FFD0000000000000000000000000000'
00015780	C1E7C2D9 40D5C640			3000 DC CL48'AXBR NF +0/+2.0 NT'
000157B0	40000000 00000000			3001 DC XL16'40000000000000000000000000000000'
000157C0	C1E7C2D9 40D5C640			3002 DC CL48'AXBR NF +0/+2.0 Tr'
000157F0	40000000 00000000			3003 DC XL16'40000000000000000000000000000000'
00015800	C1E7C2D9 40D5C640			3004 DC CL48'AXBR NF +0/+inf NT'
00015830	7FFF0000 00000000			3005 DC XL16'7FFF0000000000000000000000000000'
00015840	C1E7C2D9 40D5C640			3006 DC CL48'AXBR NF +0/+inf Tr'
00015870	7FFF0000 00000000			3007 DC XL16'7FFF0000000000000000000000000000'
00015880	C1E7C2D9 40D5C640			3008 DC CL48'AXBR NF +0/-QNaN NT'
000158B0	FFFF8B00 00000000			3009 DC XL16'FFFF8B00000000000000000000000000'
000158C0	C1E7C2D9 40D5C640			3010 DC CL48'AXBR NF +0/-QNaN Tr'
000158F0	FFFF8B00 00000000			3011 DC XL16'FFFF8B00000000000000000000000000'
00015900	C1E7C2D9 40D5C640			3012 DC CL48'AXBR NF +0/+SNaN NT'
00015930	7FFF8A00 00000000			3013 DC XL16'7FFF8A00000000000000000000000000'
00015940	C1E7C2D9 40D5C640			3014 DC CL48'AXBR NF +0/+SNaN Tr'
00015970	00000000 00000000			3015 DC XL16'00000000000000000000000000000000'
00015980	C1E7C2D9 40D5C640			3016 DC CL48'AXBR NF +Dnice/-inf NT'
000159B0	FFFF0000 00000000			3017 DC XL16'FFFF0000000000000000000000000000'
000159C0	C1E7C2D9 40D5C640			3018 DC CL48'AXBR NF +Dnice/-inf Tr'
000159F0	FFFF0000 00000000			3019 DC XL16'FFFF0000000000000000000000000000'
00015A00	C1E7C2D9 40D5C640			3020 DC CL48'AXBR NF +Dnice/-2.0 NT'
00015A30	C0000000 00000000			3021 DC XL16'C0000000000000000000000000000000'
00015A40	C1E7C2D9 40D5C640			3022 DC CL48'AXBR NF +Dnice/-2.0 Tr'
00015A70	C0000000 00000000			3023 DC XL16'C0000000000000000000000000000000'
00015A80	C1E7C2D9 40D5C640			3024 DC CL48'AXBR NF +Dnice/-Dnice NT'
00015AB0	00000000 00000000			3025 DC XL16'00000000000000000000000000000000'
00015AC0	C1E7C2D9 40D5C640			3026 DC CL48'AXBR NF +Dnice/-Dnice Tr'
00015AF0	00000000 00000000			3027 DC XL16'00000000000000000000000000000000'
00015B00	C1E7C2D9 40D5C640			3028 DC CL48'AXBR NF +Dnice/-0 NT'
00015B30	00001000 00000000			3029 DC XL16'00001000000000000000000000000000'
00015B40	C1E7C2D9 40D5C640			3030 DC CL48'AXBR NF +Dnice/-0 Tr'
00015B70	5FFD0000 00000000			3031 DC XL16'5FFD0000000000000000000000000000'
00015B80	C1E7C2D9 40D5C640			3032 DC CL48'AXBR NF +Dnice/+0 NT'
00015BB0	00001000 00000000			3033 DC XL16'00001000000000000000000000000000'
00015BC0	C1E7C2D9 40D5C640			3034 DC CL48'AXBR NF +Dnice/+0 Tr'
00015BF0	5FFD0000 00000000			3035 DC XL16'5FFD0000000000000000000000000000'
00015C00	C1E7C2D9 40D5C640			3036 DC CL48'AXBR NF +Dnice/+Dnice NT'
00015C30	00002000 00000000			3037 DC XL16'00002000000000000000000000000000'
00015C40	C1E7C2D9 40D5C640			3038 DC CL48'AXBR NF +Dnice/+Dnice Tr'
00015C70	5FFE0000 00000000			3039 DC XL16'5FFE0000000000000000000000000000'
00015C80	C1E7C2D9 40D5C640			3040 DC CL48'AXBR NF +Dnice/+2.0 NT'
00015CB0	40000000 00000000			3041 DC XL16'40000000000000000000000000000000'
00015CC0	C1E7C2D9 40D5C640			3042 DC CL48'AXBR NF +Dnice/+2.0 Tr'
00015CF0	40000000 00000000			3043 DC XL16'40000000000000000000000000000000'
00015D00	C1E7C2D9 40D5C640			3044 DC CL48'AXBR NF +Dnice/+inf NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	
00015D30	7FFF0000 00000000			3045	DC XL16'7FFF0000000000000000000000000000'
00015D40	C1E7C2D9 40D5C640			3046	DC CL48'AXBR NF +Dnice/+inf Tr'
00015D70	7FFF0000 00000000			3047	DC XL16'7FFF0000000000000000000000000000'
00015D80	C1E7C2D9 40D5C640			3048	DC CL48'AXBR NF +Dnice/-QNaN NT'
00015DB0	FFFF8B00 00000000			3049	DC XL16'FFFF8B00000000000000000000000000'
00015DC0	C1E7C2D9 40D5C640			3050	DC CL48'AXBR NF +Dnice/-QNaN Tr'
00015DF0	FFFF8B00 00000000			3051	DC XL16'FFFF8B00000000000000000000000000'
00015E00	C1E7C2D9 40D5C640			3052	DC CL48'AXBR NF +Dnice/+SNaN NT'
00015E30	7FFF8A00 00000000			3053	DC XL16'7FFF8A00000000000000000000000000'
00015E40	C1E7C2D9 40D5C640			3054	DC CL48'AXBR NF +Dnice/+SNaN Tr'
00015E70	00001000 00000000			3055	DC XL16'00001000000000000000000000000000'
00015E80	C1E7C2D9 40D5C640			3056	DC CL48'AXBR NF +2.0/-inf NT'
00015EB0	FFFF0000 00000000			3057	DC XL16'FFFF0000000000000000000000000000'
00015EC0	C1E7C2D9 40D5C640			3058	DC CL48'AXBR NF +2.0/-inf Tr'
00015EF0	FFFF0000 00000000			3059	DC XL16'FFFF0000000000000000000000000000'
00015F00	C1E7C2D9 40D5C640			3060	DC CL48'AXBR NF +2.0/-2.0 NT'
00015F30	00000000 00000000			3061	DC XL16'00000000000000000000000000000000'
00015F40	C1E7C2D9 40D5C640			3062	DC CL48'AXBR NF +2.0/-2.0 Tr'
00015F70	00000000 00000000			3063	DC XL16'00000000000000000000000000000000'
00015F80	C1E7C2D9 40D5C640			3064	DC CL48'AXBR NF +2.0/-Dnice NT'
00015FB0	40000000 00000000			3065	DC XL16'40000000000000000000000000000000'
00015FC0	C1E7C2D9 40D5C640			3066	DC CL48'AXBR NF +2.0/-Dnice Tr'
00015FF0	40000000 00000000			3067	DC XL16'40000000000000000000000000000000'
00016000	C1E7C2D9 40D5C640			3068	DC CL48'AXBR NF +2.0/-0 NT'
00016030	40000000 00000000			3069	DC XL16'40000000000000000000000000000000'
00016040	C1E7C2D9 40D5C640			3070	DC CL48'AXBR NF +2.0/-0 Tr'
00016070	40000000 00000000			3071	DC XL16'40000000000000000000000000000000'
00016080	C1E7C2D9 40D5C640			3072	DC CL48'AXBR NF +2.0/+0 NT'
000160B0	40000000 00000000			3073	DC XL16'40000000000000000000000000000000'
000160C0	C1E7C2D9 40D5C640			3074	DC CL48'AXBR NF +2.0/+0 Tr'
000160F0	40000000 00000000			3075	DC XL16'40000000000000000000000000000000'
00016100	C1E7C2D9 40D5C640			3076	DC CL48'AXBR NF +2.0/+Dnice NT'
00016130	40000000 00000000			3077	DC XL16'40000000000000000000000000000000'
00016140	C1E7C2D9 40D5C640			3078	DC CL48'AXBR NF +2.0/+Dnice Tr'
00016170	40000000 00000000			3079	DC XL16'40000000000000000000000000000000'
00016180	C1E7C2D9 40D5C640			3080	DC CL48'AXBR NF +2.0/+2.0 NT'
000161B0	40010000 00000000			3081	DC XL16'40010000000000000000000000000000'
000161C0	C1E7C2D9 40D5C640			3082	DC CL48'AXBR NF +2.0/+2.0 Tr'
000161F0	40010000 00000000			3083	DC XL16'40010000000000000000000000000000'
00016200	C1E7C2D9 40D5C640			3084	DC CL48'AXBR NF +2.0/+inf NT'
00016230	7FFF0000 00000000			3085	DC XL16'7FFF0000000000000000000000000000'
00016240	C1E7C2D9 40D5C640			3086	DC CL48'AXBR NF +2.0/+inf Tr'
00016270	7FFF0000 00000000			3087	DC XL16'7FFF0000000000000000000000000000'
00016280	C1E7C2D9 40D5C640			3088	DC CL48'AXBR NF +2.0/-QNaN NT'
000162B0	FFFF8B00 00000000			3089	DC XL16'FFFF8B00000000000000000000000000'
000162C0	C1E7C2D9 40D5C640			3090	DC CL48'AXBR NF +2.0/-QNaN Tr'
000162F0	FFFF8B00 00000000			3091	DC XL16'FFFF8B00000000000000000000000000'
00016300	C1E7C2D9 40D5C640			3092	DC CL48'AXBR NF +2.0/+SNaN NT'
00016330	7FFF8A00 00000000			3093	DC XL16'7FFF8A00000000000000000000000000'
00016340	C1E7C2D9 40D5C640			3094	DC CL48'AXBR NF +2.0/+SNaN Tr'
00016370	40000000 00000000			3095	DC XL16'40000000000000000000000000000000'
00016380	C1E7C2D9 40D5C640			3096	DC CL48'AXBR NF +inf/-inf NT'
000163B0	7FFF8000 00000000			3097	DC XL16'7FFF8000000000000000000000000000'
000163C0	C1E7C2D9 40D5C640			3098	DC CL48'AXBR NF +inf/-inf Tr'
000163F0	7FFF0000 00000000			3099	DC XL16'7FFF0000000000000000000000000000'
00016400	C1E7C2D9 40D5C640			3100	DC CL48'AXBR NF +inf/-2.0 NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00016430	7FFF0000 00000000			3101 DC XL16'7FFF0000000000000000000000000000'
00016440	C1E7C2D9 40D5C640			3102 DC CL48'AXBR NF +inf/-2.0 Tr'
00016470	7FFF0000 00000000			3103 DC XL16'7FFF0000000000000000000000000000'
00016480	C1E7C2D9 40D5C640			3104 DC CL48'AXBR NF +inf/-Dnice NT'
000164B0	7FFF0000 00000000			3105 DC XL16'7FFF0000000000000000000000000000'
000164C0	C1E7C2D9 40D5C640			3106 DC CL48'AXBR NF +inf/-Dnice Tr'
000164F0	7FFF0000 00000000			3107 DC XL16'7FFF0000000000000000000000000000'
00016500	C1E7C2D9 40D5C640			3108 DC CL48'AXBR NF +inf/-0 NT'
00016530	7FFF0000 00000000			3109 DC XL16'7FFF0000000000000000000000000000'
00016540	C1E7C2D9 40D5C640			3110 DC CL48'AXBR NF +inf/-0 Tr'
00016570	7FFF0000 00000000			3111 DC XL16'7FFF0000000000000000000000000000'
00016580	C1E7C2D9 40D5C640			3112 DC CL48'AXBR NF +inf/+0 NT'
000165B0	7FFF0000 00000000			3113 DC XL16'7FFF0000000000000000000000000000'
000165C0	C1E7C2D9 40D5C640			3114 DC CL48'AXBR NF +inf/+0 Tr'
000165F0	7FFF0000 00000000			3115 DC XL16'7FFF0000000000000000000000000000'
00016600	C1E7C2D9 40D5C640			3116 DC CL48'AXBR NF +inf/+Dnice NT'
00016630	7FFF0000 00000000			3117 DC XL16'7FFF0000000000000000000000000000'
00016640	C1E7C2D9 40D5C640			3118 DC CL48'AXBR NF +inf/+Dnice Tr'
00016670	7FFF0000 00000000			3119 DC XL16'7FFF0000000000000000000000000000'
00016680	C1E7C2D9 40D5C640			3120 DC CL48'AXBR NF +inf/+2.0 NT'
000166B0	7FFF0000 00000000			3121 DC XL16'7FFF0000000000000000000000000000'
000166C0	C1E7C2D9 40D5C640			3122 DC CL48'AXBR NF +inf/+2.0 Tr'
000166F0	7FFF0000 00000000			3123 DC XL16'7FFF0000000000000000000000000000'
00016700	C1E7C2D9 40D5C640			3124 DC CL48'AXBR NF +inf/+inf NT'
00016730	7FFF0000 00000000			3125 DC XL16'7FFF0000000000000000000000000000'
00016740	C1E7C2D9 40D5C640			3126 DC CL48'AXBR NF +inf/+inf Tr'
00016770	7FFF0000 00000000			3127 DC XL16'7FFF0000000000000000000000000000'
00016780	C1E7C2D9 40D5C640			3128 DC CL48'AXBR NF +inf/-QNaN NT'
000167B0	FFFF8B00 00000000			3129 DC XL16'FFFF8B00000000000000000000000000'
000167C0	C1E7C2D9 40D5C640			3130 DC CL48'AXBR NF +inf/-QNaN Tr'
000167F0	FFFF8B00 00000000			3131 DC XL16'FFFF8B00000000000000000000000000'
00016800	C1E7C2D9 40D5C640			3132 DC CL48'AXBR NF +inf/+SNaN NT'
00016830	7FFF8A00 00000000			3133 DC XL16'7FFF8A00000000000000000000000000'
00016840	C1E7C2D9 40D5C640			3134 DC CL48'AXBR NF +inf/+SNaN Tr'
00016870	7FFF0000 00000000			3135 DC XL16'7FFF0000000000000000000000000000'
00016880	C1E7C2D9 40D5C640			3136 DC CL48'AXBR NF -QNaN/-inf NT'
000168B0	FFFF8B00 00000000			3137 DC XL16'FFFF8B00000000000000000000000000'
000168C0	C1E7C2D9 40D5C640			3138 DC CL48'AXBR NF -QNaN/-inf Tr'
000168F0	FFFF8B00 00000000			3139 DC XL16'FFFF8B00000000000000000000000000'
00016900	C1E7C2D9 40D5C640			3140 DC CL48'AXBR NF -QNaN/-2.0 NT'
00016930	FFFF8B00 00000000			3141 DC XL16'FFFF8B00000000000000000000000000'
00016940	C1E7C2D9 40D5C640			3142 DC CL48'AXBR NF -QNaN/-2.0 Tr'
00016970	FFFF8B00 00000000			3143 DC XL16'FFFF8B00000000000000000000000000'
00016980	C1E7C2D9 40D5C640			3144 DC CL48'AXBR NF -QNaN/-Dnice NT'
000169B0	FFFF8B00 00000000			3145 DC XL16'FFFF8B00000000000000000000000000'
000169C0	C1E7C2D9 40D5C640			3146 DC CL48'AXBR NF -QNaN/-Dnice Tr'
000169F0	FFFF8B00 00000000			3147 DC XL16'FFFF8B00000000000000000000000000'
00016A00	C1E7C2D9 40D5C640			3148 DC CL48'AXBR NF -QNaN/-0 NT'
00016A30	FFFF8B00 00000000			3149 DC XL16'FFFF8B00000000000000000000000000'
00016A40	C1E7C2D9 40D5C640			3150 DC CL48'AXBR NF -QNaN/-0 Tr'
00016A70	FFFF8B00 00000000			3151 DC XL16'FFFF8B00000000000000000000000000'
00016A80	C1E7C2D9 40D5C640			3152 DC CL48'AXBR NF -QNaN/+0 NT'
00016AB0	FFFF8B00 00000000			3153 DC XL16'FFFF8B00000000000000000000000000'
00016AC0	C1E7C2D9 40D5C640			3154 DC CL48'AXBR NF -QNaN/+0 Tr'
00016AF0	FFFF8B00 00000000			3155 DC XL16'FFFF8B00000000000000000000000000'
00016B00	C1E7C2D9 40D5C640			3156 DC CL48'AXBR NF -QNaN/+Dnice NT'

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
00016B30	FFFF8B00	00000000			3157 DC XL16'FFFF8B000000000000000000000000'
00016B40	C1E7C2D9	40D5C640			3158 DC CL48'AXBR NF -QNaN/+Dnice Tr'
00016B70	FFFF8B00	00000000			3159 DC XL16'FFFF8B000000000000000000000000'
00016B80	C1E7C2D9	40D5C640			3160 DC CL48'AXBR NF -QNaN/+2.0 NT'
00016BB0	FFFF8B00	00000000			3161 DC XL16'FFFF8B000000000000000000000000'
00016BC0	C1E7C2D9	40D5C640			3162 DC CL48'AXBR NF -QNaN/+2.0 Tr'
00016BF0	FFFF8B00	00000000			3163 DC XL16'FFFF8B000000000000000000000000'
00016C00	C1E7C2D9	40D5C640			3164 DC CL48'AXBR NF -QNaN/+inf NT'
00016C30	FFFF8B00	00000000			3165 DC XL16'FFFF8B000000000000000000000000'
00016C40	C1E7C2D9	40D5C640			3166 DC CL48'AXBR NF -QNaN/+inf Tr'
00016C70	FFFF8B00	00000000			3167 DC XL16'FFFF8B000000000000000000000000'
00016C80	C1E7C2D9	40D5C640			3168 DC CL48'AXBR NF -QNaN/-QNaN NT'
00016CB0	FFFF8B00	00000000			3169 DC XL16'FFFF8B000000000000000000000000'
00016CC0	C1E7C2D9	40D5C640			3170 DC CL48'AXBR NF -QNaN/-QNaN Tr'
00016CF0	FFFF8B00	00000000			3171 DC XL16'FFFF8B000000000000000000000000'
00016D00	C1E7C2D9	40D5C640			3172 DC CL48'AXBR NF -QNaN/+SNaN NT'
00016D30	7FFF8A00	00000000			3173 DC XL16'7FFF8A000000000000000000000000'
00016D40	C1E7C2D9	40D5C640			3174 DC CL48'AXBR NF -QNaN/+SNaN Tr'
00016D70	FFFF8B00	00000000			3175 DC XL16'FFFF8B000000000000000000000000'
00016D80	C1E7C2D9	40D5C640			3176 DC CL48'AXBR NF +SNaN/-inf NT'
00016DB0	7FFF8A00	00000000			3177 DC XL16'7FFF8A000000000000000000000000'
00016DC0	C1E7C2D9	40D5C640			3178 DC CL48'AXBR NF +SNaN/-inf Tr'
00016DF0	7FFF0A00	00000000			3179 DC XL16'7FFF0A000000000000000000000000'
00016E00	C1E7C2D9	40D5C640			3180 DC CL48'AXBR NF +SNaN/-2.0 NT'
00016E30	7FFF8A00	00000000			3181 DC XL16'7FFF8A000000000000000000000000'
00016E40	C1E7C2D9	40D5C640			3182 DC CL48'AXBR NF +SNaN/-2.0 Tr'
00016E70	7FFF0A00	00000000			3183 DC XL16'7FFF0A000000000000000000000000'
00016E80	C1E7C2D9	40D5C640			3184 DC CL48'AXBR NF +SNaN/-Dnice NT'
00016EB0	7FFF8A00	00000000			3185 DC XL16'7FFF8A000000000000000000000000'
00016EC0	C1E7C2D9	40D5C640			3186 DC CL48'AXBR NF +SNaN/-Dnice Tr'
00016EF0	7FFF0A00	00000000			3187 DC XL16'7FFF0A000000000000000000000000'
00016F00	C1E7C2D9	40D5C640			3188 DC CL48'AXBR NF +SNaN/-0 NT'
00016F30	7FFF8A00	00000000			3189 DC XL16'7FFF8A000000000000000000000000'
00016F40	C1E7C2D9	40D5C640			3190 DC CL48'AXBR NF +SNaN/-0 Tr'
00016F70	7FFF0A00	00000000			3191 DC XL16'7FFF0A000000000000000000000000'
00016F80	C1E7C2D9	40D5C640			3192 DC CL48'AXBR NF +SNaN/+0 NT'
00016FB0	7FFF8A00	00000000			3193 DC XL16'7FFF8A000000000000000000000000'
00016FC0	C1E7C2D9	40D5C640			3194 DC CL48'AXBR NF +SNaN/+0 Tr'
00016FF0	7FFF0A00	00000000			3195 DC XL16'7FFF0A000000000000000000000000'
00017000	C1E7C2D9	40D5C640			3196 DC CL48'AXBR NF +SNaN/+Dnice NT'
00017030	7FFF8A00	00000000			3197 DC XL16'7FFF8A000000000000000000000000'
00017040	C1E7C2D9	40D5C640			3198 DC CL48'AXBR NF +SNaN/+Dnice Tr'
00017070	7FFF0A00	00000000			3199 DC XL16'7FFF0A000000000000000000000000'
00017080	C1E7C2D9	40D5C640			3200 DC CL48'AXBR NF +SNaN/+2.0 NT'
000170B0	7FFF8A00	00000000			3201 DC XL16'7FFF8A000000000000000000000000'
000170C0	C1E7C2D9	40D5C640			3202 DC CL48'AXBR NF +SNaN/+2.0 Tr'
000170F0	7FFF0A00	00000000			3203 DC XL16'7FFF0A000000000000000000000000'
00017100	C1E7C2D9	40D5C640			3204 DC CL48'AXBR NF +SNaN/+inf NT'
00017130	7FFF8A00	00000000			3205 DC XL16'7FFF8A000000000000000000000000'
00017140	C1E7C2D9	40D5C640			3206 DC CL48'AXBR NF +SNaN/+inf Tr'
00017170	7FFF0A00	00000000			3207 DC XL16'7FFF0A000000000000000000000000'
00017180	C1E7C2D9	40D5C640			3208 DC CL48'AXBR NF +SNaN/-QNaN NT'
000171B0	7FFF8A00	00000000			3209 DC XL16'7FFF8A000000000000000000000000'
000171C0	C1E7C2D9	40D5C640			3210 DC CL48'AXBR NF +SNaN/-QNaN Tr'
000171F0	7FFF0A00	00000000			3211 DC XL16'7FFF0A000000000000000000000000'
00017200	C1E7C2D9	40D5C640			3212 DC CL48'AXBR NF +SNaN/+SNaN NT'

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
00017230	7FFF8A00	00000000			3213 DC XL16'7FFF8A000000000000000000000000'
00017240	C1E7C2D9	40D5C640			3214 DC CL48'AXBR NF +SNaN/+SNaN Tr'
00017270	7FFF0A00	00000000			3215 DC XL16'7FFF0A000000000000000000000000'
			000000C8	00000001	3216 XBFPNFOT_NUM EQU (*-XBFPNFOT_GOOD)/64
					3217 *
					3218 *
			00017280	00000001	3219 XBFPNFFL_GOOD EQU *
00017280	C1E7C2D9	40D5C640			3220 DC CL48'AXBR NF -inf/-inf FPCR'
000172B0	00000001	F8000001			3221 DC XL16'00000001F80000010000000000000000'
000172C0	C1E7C2D9	40D5C640			3222 DC CL48'AXBR NF -inf/-2.0 FPCR'
000172F0	00000001	F8000001			3223 DC XL16'00000001F80000010000000000000000'
00017300	C1E7C2D9	40D5C640			3224 DC CL48'AXBR NF -inf/-Dnice FPCR'
00017330	00000001	F8000001			3225 DC XL16'00000001F80000010000000000000000'
00017340	C1E7C2D9	40D5C640			3226 DC CL48'AXBR NF -inf/-0 FPCR'
00017370	00000001	F8000001			3227 DC XL16'00000001F80000010000000000000000'
00017380	C1E7C2D9	40D5C640			3228 DC CL48'AXBR NF -inf/+0 FPCR'
000173B0	00000001	F8000001			3229 DC XL16'00000001F80000010000000000000000'
000173C0	C1E7C2D9	40D5C640			3230 DC CL48'AXBR NF -inf/+Dnice FPCR'
000173F0	00000001	F8000001			3231 DC XL16'00000001F80000010000000000000000'
00017400	C1E7C2D9	40D5C640			3232 DC CL48'AXBR NF -inf/+2.0 FPCR'
00017430	00000001	F8000001			3233 DC XL16'00000001F80000010000000000000000'
00017440	C1E7C2D9	40D5C640			3234 DC CL48'AXBR NF -inf/+inf FPCR'
00017470	00800003	F8008003			3235 DC XL16'00800003F80080030000000000000000'
00017480	C1E7C2D9	40D5C640			3236 DC CL48'AXBR NF -inf/-QNaN FPCR'
000174B0	00000003	F8000003			3237 DC XL16'00000003F80000030000000000000000'
000174C0	C1E7C2D9	40D5C640			3238 DC CL48'AXBR NF -inf/+SNaN FPCR'
000174F0	00800003	F8008003			3239 DC XL16'00800003F80080030000000000000000'
00017500	C1E7C2D9	40D5C640			3240 DC CL48'AXBR NF -2.0/-inf FPCR'
00017530	00000001	F8000001			3241 DC XL16'00000001F80000010000000000000000'
00017540	C1E7C2D9	40D5C640			3242 DC CL48'AXBR NF -2.0/-2.0 FPCR'
00017570	00000001	F8000001			3243 DC XL16'00000001F80000010000000000000000'
00017580	C1E7C2D9	40D5C640			3244 DC CL48'AXBR NF -2.0/-Dnice FPCR'
000175B0	00080001	F8000801			3245 DC XL16'00080001F80008010000000000000000'
000175C0	C1E7C2D9	40D5C640			3246 DC CL48'AXBR NF -2.0/-0 FPCR'
000175F0	00000001	F8000001			3247 DC XL16'00000001F80000010000000000000000'
00017600	C1E7C2D9	40D5C640			3248 DC CL48'AXBR NF -2.0/+0 FPCR'
00017630	00000001	F8000001			3249 DC XL16'00000001F80000010000000000000000'
00017640	C1E7C2D9	40D5C640			3250 DC CL48'AXBR NF -2.0/+Dnice FPCR'
00017670	00080001	F8000C01			3251 DC XL16'00080001F8000C010000000000000000'
00017680	C1E7C2D9	40D5C640			3252 DC CL48'AXBR NF -2.0/+2.0 FPCR'
000176B0	00000000	F8000000			3253 DC XL16'00000000F80000000000000000000000'
000176C0	C1E7C2D9	40D5C640			3254 DC CL48'AXBR NF -2.0/+inf FPCR'
000176F0	00000002	F8000002			3255 DC XL16'00000002F80000020000000000000000'
00017700	C1E7C2D9	40D5C640			3256 DC CL48'AXBR NF -2.0/-QNaN FPCR'
00017730	00000003	F8000003			3257 DC XL16'00000003F80000030000000000000000'
00017740	C1E7C2D9	40D5C640			3258 DC CL48'AXBR NF -2.0/+SNaN FPCR'
00017770	00800003	F8008003			3259 DC XL16'00800003F80080030000000000000000'
00017780	C1E7C2D9	40D5C640			3260 DC CL48'AXBR NF -Dnice/-inf FPCR'
000177B0	00000001	F8000001			3261 DC XL16'00000001F80000010000000000000000'
000177C0	C1E7C2D9	40D5C640			3262 DC CL48'AXBR NF -Dnice/-2.0 FPCR'
000177F0	00080001	F8000801			3263 DC XL16'00080001F80008010000000000000000'
00017800	C1E7C2D9	40D5C640			3264 DC CL48'AXBR NF -Dnice/-Dnice FPCR'
00017830	00000001	F8001001			3265 DC XL16'00000001F80010010000000000000000'
00017840	C1E7C2D9	40D5C640			3266 DC CL48'AXBR NF -Dnice/-0 FPCR'
00017870	00000001	F8001001			3267 DC XL16'00000001F80010010000000000000000'
00017880	C1E7C2D9	40D5C640			3268 DC CL48'AXBR NF -Dnice/+0 FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000178B0	00000001 F8001001			3269 DC XL16'00000001F80010010000000000000000'
000178C0	C1E7C2D9 40D5C640			3270 DC CL48'AXBR NF -Dnice/+Dnice FPCR'
000178F0	00000000 F8000000			3271 DC XL16'00000000F80000000000000000000000'
00017900	C1E7C2D9 40D5C640			3272 DC CL48'AXBR NF -Dnice/+2.0 FPCR'
00017930	00080002 F8000C02			3273 DC XL16'00080002F8000C020000000000000000'
00017940	C1E7C2D9 40D5C640			3274 DC CL48'AXBR NF -Dnice/+inf FPCR'
00017970	00000002 F8000002			3275 DC XL16'00000002F80000020000000000000000'
00017980	C1E7C2D9 40D5C640			3276 DC CL48'AXBR NF -Dnice/-QNaN FPCR'
000179B0	00000003 F8000003			3277 DC XL16'00000003F80000030000000000000000'
000179C0	C1E7C2D9 40D5C640			3278 DC CL48'AXBR NF -Dnice/+SNaN FPCR'
000179F0	00800003 F8008003			3279 DC XL16'00800003F80080030000000000000000'
00017A00	C1E7C2D9 40D5C640			3280 DC CL48'AXBR NF -0/-inf FPCR'
00017A30	00000001 F8000001			3281 DC XL16'00000001F80000010000000000000000'
00017A40	C1E7C2D9 40D5C640			3282 DC CL48'AXBR NF -0/-2.0 FPCR'
00017A70	00000001 F8000001			3283 DC XL16'00000001F80000010000000000000000'
00017A80	C1E7C2D9 40D5C640			3284 DC CL48'AXBR NF -0/-Dnice FPCR'
00017AB0	00000001 F8001001			3285 DC XL16'00000001F80010010000000000000000'
00017AC0	C1E7C2D9 40D5C640			3286 DC CL48'AXBR NF -0/-0 FPCR'
00017AF0	00000000 F8000000			3287 DC XL16'00000000F80000000000000000000000'
00017B00	C1E7C2D9 40D5C640			3288 DC CL48'AXBR NF -0/+0 FPCR'
00017B30	00000000 F8000000			3289 DC XL16'00000000F80000000000000000000000'
00017B40	C1E7C2D9 40D5C640			3290 DC CL48'AXBR NF -0/+Dnice FPCR'
00017B70	00000002 F8001002			3291 DC XL16'00000002F80010020000000000000000'
00017B80	C1E7C2D9 40D5C640			3292 DC CL48'AXBR NF -0/+2.0 FPCR'
00017BB0	00000002 F8000002			3293 DC XL16'00000002F80000020000000000000000'
00017BC0	C1E7C2D9 40D5C640			3294 DC CL48'AXBR NF -0/+inf FPCR'
00017BF0	00000002 F8000002			3295 DC XL16'00000002F80000020000000000000000'
00017C00	C1E7C2D9 40D5C640			3296 DC CL48'AXBR NF -0/-QNaN FPCR'
00017C30	00000003 F8000003			3297 DC XL16'00000003F80000030000000000000000'
00017C40	C1E7C2D9 40D5C640			3298 DC CL48'AXBR NF -0/+SNaN FPCR'
00017C70	00800003 F8008003			3299 DC XL16'00800003F80080030000000000000000'
00017C80	C1E7C2D9 40D5C640			3300 DC CL48'AXBR NF +0/-inf FPCR'
00017CB0	00000001 F8000001			3301 DC XL16'00000001F80000010000000000000000'
00017CC0	C1E7C2D9 40D5C640			3302 DC CL48'AXBR NF +0/-2.0 FPCR'
00017CF0	00000001 F8000001			3303 DC XL16'00000001F80000010000000000000000'
00017D00	C1E7C2D9 40D5C640			3304 DC CL48'AXBR NF +0/-Dnice FPCR'
00017D30	00000001 F8001001			3305 DC XL16'00000001F80010010000000000000000'
00017D40	C1E7C2D9 40D5C640			3306 DC CL48'AXBR NF +0/-0 FPCR'
00017D70	00000000 F8000000			3307 DC XL16'00000000F80000000000000000000000'
00017D80	C1E7C2D9 40D5C640			3308 DC CL48'AXBR NF +0/+0 FPCR'
00017DB0	00000000 F8000000			3309 DC XL16'00000000F80000000000000000000000'
00017DC0	C1E7C2D9 40D5C640			3310 DC CL48'AXBR NF +0/+Dnice FPCR'
00017DF0	00000002 F8001002			3311 DC XL16'00000002F80010020000000000000000'
00017E00	C1E7C2D9 40D5C640			3312 DC CL48'AXBR NF +0/+2.0 FPCR'
00017E30	00000002 F8000002			3313 DC XL16'00000002F80000020000000000000000'
00017E40	C1E7C2D9 40D5C640			3314 DC CL48'AXBR NF +0/+inf FPCR'
00017E70	00000002 F8000002			3315 DC XL16'00000002F80000020000000000000000'
00017E80	C1E7C2D9 40D5C640			3316 DC CL48'AXBR NF +0/-QNaN FPCR'
00017EB0	00000003 F8000003			3317 DC XL16'00000003F80000030000000000000000'
00017EC0	C1E7C2D9 40D5C640			3318 DC CL48'AXBR NF +0/+SNaN FPCR'
00017EF0	00800003 F8008003			3319 DC XL16'00800003F80080030000000000000000'
00017F00	C1E7C2D9 40D5C640			3320 DC CL48'AXBR NF +Dnice/-inf FPCR'
00017F30	00000001 F8000001			3321 DC XL16'00000001F80000010000000000000000'
00017F40	C1E7C2D9 40D5C640			3322 DC CL48'AXBR NF +Dnice/-2.0 FPCR'
00017F70	00080001 F8000C01			3323 DC XL16'00080001F8000C010000000000000000'
00017F80	C1E7C2D9 40D5C640			3324 DC CL48'AXBR NF +Dnice/-Dnice FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00017FB0	00000000 F8000000			3325 DC XL16'00000000F80000000000000000000000'
00017FC0	C1E7C2D9 40D5C640			3326 DC CL48'AXBR NF +Dnice/-0 FPCR'
00017FF0	00000002 F8001002			3327 DC XL16'00000002F80010020000000000000000'
00018000	C1E7C2D9 40D5C640			3328 DC CL48'AXBR NF +Dnice/+0 FPCR'
00018030	00000002 F8001002			3329 DC XL16'00000002F80010020000000000000000'
00018040	C1E7C2D9 40D5C640			3330 DC CL48'AXBR NF +Dnice/+Dnice FPCR'
00018070	00000002 F8001002			3331 DC XL16'00000002F80010020000000000000000'
00018080	C1E7C2D9 40D5C640			3332 DC CL48'AXBR NF +Dnice/+2.0 FPCR'
000180B0	00080002 F8000802			3333 DC XL16'00080002F80008020000000000000000'
000180C0	C1E7C2D9 40D5C640			3334 DC CL48'AXBR NF +Dnice/+inf FPCR'
000180F0	00000002 F8000002			3335 DC XL16'00000002F80000020000000000000000'
00018100	C1E7C2D9 40D5C640			3336 DC CL48'AXBR NF +Dnice/-QNaN FPCR'
00018130	00000003 F8000003			3337 DC XL16'00000003F80000030000000000000000'
00018140	C1E7C2D9 40D5C640			3338 DC CL48'AXBR NF +Dnice/+SNaN FPCR'
00018170	00800003 F8008003			3339 DC XL16'00800003F80080030000000000000000'
00018180	C1E7C2D9 40D5C640			3340 DC CL48'AXBR NF +2.0/-inf FPCR'
000181B0	00000001 F8000001			3341 DC XL16'00000001F80000010000000000000000'
000181C0	C1E7C2D9 40D5C640			3342 DC CL48'AXBR NF +2.0/-2.0 FPCR'
000181F0	00000000 F8000000			3343 DC XL16'00000000F80000000000000000000000'
00018200	C1E7C2D9 40D5C640			3344 DC CL48'AXBR NF +2.0/-Dnice FPCR'
00018230	00080002 F8000C02			3345 DC XL16'00080002F8000C020000000000000000'
00018240	C1E7C2D9 40D5C640			3346 DC CL48'AXBR NF +2.0/-0 FPCR'
00018270	00000002 F8000002			3347 DC XL16'00000002F80000020000000000000000'
00018280	C1E7C2D9 40D5C640			3348 DC CL48'AXBR NF +2.0/+0 FPCR'
000182B0	00000002 F8000002			3349 DC XL16'00000002F80000020000000000000000'
000182C0	C1E7C2D9 40D5C640			3350 DC CL48'AXBR NF +2.0/+Dnice FPCR'
000182F0	00080002 F8000802			3351 DC XL16'00080002F80008020000000000000000'
00018300	C1E7C2D9 40D5C640			3352 DC CL48'AXBR NF +2.0/+2.0 FPCR'
00018330	00000002 F8000002			3353 DC XL16'00000002F80000020000000000000000'
00018340	C1E7C2D9 40D5C640			3354 DC CL48'AXBR NF +2.0/+inf FPCR'
00018370	00000002 F8000002			3355 DC XL16'00000002F80000020000000000000000'
00018380	C1E7C2D9 40D5C640			3356 DC CL48'AXBR NF +2.0/-QNaN FPCR'
000183B0	00000003 F8000003			3357 DC XL16'00000003F80000030000000000000000'
000183C0	C1E7C2D9 40D5C640			3358 DC CL48'AXBR NF +2.0/+SNaN FPCR'
000183F0	00800003 F8008003			3359 DC XL16'00800003F80080030000000000000000'
00018400	C1E7C2D9 40D5C640			3360 DC CL48'AXBR NF +inf/-inf FPCR'
00018430	00800003 F8008003			3361 DC XL16'00800003F80080030000000000000000'
00018440	C1E7C2D9 40D5C640			3362 DC CL48'AXBR NF +inf/-2.0 FPCR'
00018470	00000002 F8000002			3363 DC XL16'00000002F80000020000000000000000'
00018480	C1E7C2D9 40D5C640			3364 DC CL48'AXBR NF +inf/-Dnice FPCR'
000184B0	00000002 F8000002			3365 DC XL16'00000002F80000020000000000000000'
000184C0	C1E7C2D9 40D5C640			3366 DC CL48'AXBR NF +inf/-0 FPCR'
000184F0	00000002 F8000002			3367 DC XL16'00000002F80000020000000000000000'
00018500	C1E7C2D9 40D5C640			3368 DC CL48'AXBR NF +inf/+0 FPCR'
00018530	00000002 F8000002			3369 DC XL16'00000002F80000020000000000000000'
00018540	C1E7C2D9 40D5C640			3370 DC CL48'AXBR NF +inf/+Dnice FPCR'
00018570	00000002 F8000002			3371 DC XL16'00000002F80000020000000000000000'
00018580	C1E7C2D9 40D5C640			3372 DC CL48'AXBR NF +inf/+2.0 FPCR'
000185B0	00000002 F8000002			3373 DC XL16'00000002F80000020000000000000000'
000185C0	C1E7C2D9 40D5C640			3374 DC CL48'AXBR NF +inf/+inf FPCR'
000185F0	00000002 F8000002			3375 DC XL16'00000002F80000020000000000000000'
00018600	C1E7C2D9 40D5C640			3376 DC CL48'AXBR NF +inf/-QNaN FPCR'
00018630	00000003 F8000003			3377 DC XL16'00000003F80000030000000000000000'
00018640	C1E7C2D9 40D5C640			3378 DC CL48'AXBR NF +inf/+SNaN FPCR'
00018670	00800003 F8008003			3379 DC XL16'00800003F80080030000000000000000'
00018680	C1E7C2D9 40D5C640			3380 DC CL48'AXBR NF -QNaN/-inf FPCR'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
000186B0	00000003 F8000003			3381 DC XL16'00000003F80000030000000000000000'
000186C0	C1E7C2D9 40D5C640			3382 DC CL48'AXBR NF -QNaN/-2.0 FPCR'
000186F0	00000003 F8000003			3383 DC XL16'00000003F80000030000000000000000'
00018700	C1E7C2D9 40D5C640			3384 DC CL48'AXBR NF -QNaN/-Dnice FPCR'
00018730	00000003 F8000003			3385 DC XL16'00000003F80000030000000000000000'
00018740	C1E7C2D9 40D5C640			3386 DC CL48'AXBR NF -QNaN/-0 FPCR'
00018770	00000003 F8000003			3387 DC XL16'00000003F80000030000000000000000'
00018780	C1E7C2D9 40D5C640			3388 DC CL48'AXBR NF -QNaN/+0 FPCR'
000187B0	00000003 F8000003			3389 DC XL16'00000003F80000030000000000000000'
000187C0	C1E7C2D9 40D5C640			3390 DC CL48'AXBR NF -QNaN/+Dnice FPCR'
000187F0	00000003 F8000003			3391 DC XL16'00000003F80000030000000000000000'
00018800	C1E7C2D9 40D5C640			3392 DC CL48'AXBR NF -QNaN/+2.0 FPCR'
00018830	00000003 F8000003			3393 DC XL16'00000003F80000030000000000000000'
00018840	C1E7C2D9 40D5C640			3394 DC CL48'AXBR NF -QNaN/+inf FPCR'
00018870	00000003 F8000003			3395 DC XL16'00000003F80000030000000000000000'
00018880	C1E7C2D9 40D5C640			3396 DC CL48'AXBR NF -QNaN/-QNaN FPCR'
000188B0	00000003 F8000003			3397 DC XL16'00000003F80000030000000000000000'
000188C0	C1E7C2D9 40D5C640			3398 DC CL48'AXBR NF -QNaN/+SNaN FPCR'
000188F0	00800003 F8008003			3399 DC XL16'00800003F80080030000000000000000'
00018900	C1E7C2D9 40D5C640			3400 DC CL48'AXBR NF +SNaN/-inf FPCR'
00018930	00800003 F8008003			3401 DC XL16'00800003F80080030000000000000000'
00018940	C1E7C2D9 40D5C640			3402 DC CL48'AXBR NF +SNaN/-2.0 FPCR'
00018970	00800003 F8008003			3403 DC XL16'00800003F80080030000000000000000'
00018980	C1E7C2D9 40D5C640			3404 DC CL48'AXBR NF +SNaN/-Dnice FPCR'
000189B0	00800003 F8008003			3405 DC XL16'00800003F80080030000000000000000'
000189C0	C1E7C2D9 40D5C640			3406 DC CL48'AXBR NF +SNaN/-0 FPCR'
000189F0	00800003 F8008003			3407 DC XL16'00800003F80080030000000000000000'
00018A00	C1E7C2D9 40D5C640			3408 DC CL48'AXBR NF +SNaN/+0 FPCR'
00018A30	00800003 F8008003			3409 DC XL16'00800003F80080030000000000000000'
00018A40	C1E7C2D9 40D5C640			3410 DC CL48'AXBR NF +SNaN/+Dnice FPCR'
00018A70	00800003 F8008003			3411 DC XL16'00800003F80080030000000000000000'
00018A80	C1E7C2D9 40D5C640			3412 DC CL48'AXBR NF +SNaN/+2.0 FPCR'
00018AB0	00800003 F8008003			3413 DC XL16'00800003F80080030000000000000000'
00018AC0	C1E7C2D9 40D5C640			3414 DC CL48'AXBR NF +SNaN/+inf FPCR'
00018AF0	00800003 F8008003			3415 DC XL16'00800003F80080030000000000000000'
00018B00	C1E7C2D9 40D5C640			3416 DC CL48'AXBR NF +SNaN/-QNaN FPCR'
00018B30	00800003 F8008003			3417 DC XL16'00800003F80080030000000000000000'
00018B40	C1E7C2D9 40D5C640			3418 DC CL48'AXBR NF +SNaN/+SNaN FPCR'
00018B70	00800003 F8008003			3419 DC XL16'00800003F80080030000000000000000'
		00000064	00000001	3420 XBFPNFFL_NUM EQU (*-XBFPNFFL_GOOD)/64
				3421 *
				3422 *
		00018B80	00000001	3423 XBFPNFFL_GOOD EQU *
00018B80	C1E7C2D9 40C640D6			3424 DC CL48'AXBR F Ovfl NT'
00018BB0	7FFFFFFF FFFFFFFF			3425 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00018BC0	C1E7C2D9 40C640D6			3426 DC CL48'AXBR F Ovfl Tr'
00018BF0	7FFFFFFF FFFFFFFF			3427 DC XL16'7FFFFFFF7FFFFFFF7FFFFFFF7FFFFFFF'
00018C00	C1E7C2D9 40C640E4			3428 DC CL48'AXBR F Ufl 1 NT'
00018C30	0000FFFF FFFFFFFF			3429 DC XL16'0000FFFF00000000000000000000000'
00018C40	C1E7C2D9 40C640E4			3430 DC CL48'AXBR F Ufl 1 Tr'
00018C70	6000FFFF FFFFFFFF			3431 DC XL16'6000FFFF60000000000000000000000'
00018C80	C1E7C2D9 40C640E4			3432 DC CL48'AXBR F Ufl 2 NT'
00018CB0	00008F0F 00000000			3433 DC XL16'00008F0F000000000000000000000000'
00018CC0	C1E7C2D9 40C640E4			3434 DC CL48'AXBR F Ufl 2 Tr'
00018CF0	60001E1E 00000000			3435 DC XL16'60001E1E000000000000000000000000'
00018D00	C1E7C2D9 40C640D5			3436 DC CL48'AXBR F Nmin NT'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00018D30	00010000 00000000			3437 DC XL16'00010000000000000000000000000000'
00018D40	C1E7C2D9 40C640D5			3438 DC CL48'AXBR F Nmin Tr'
00018D70	00010000 00000000			3439 DC XL16'00010000000000000000000000000000'
00018D80	C1E7C2D9 40C640C9			3440 DC CL48'AXBR F Incr NT'
00018DB0	3FFF0000 00000000			3441 DC XL16'3FFF0000000000000000000000000001'
00018DC0	C1E7C2D9 40C640C9			3442 DC CL48'AXBR F Incr Tr'
00018DF0	3FFF0000 00000000			3443 DC XL16'3FFF0000000000000000000000000001'
00018E00	C1E7C2D9 40C640E3			3444 DC CL48'AXBR F Trun NT'
00018E30	3FFF0000 00000000			3445 DC XL16'3FFF0000000000000000000000000000'
00018E40	C1E7C2D9 40C640E3			3446 DC CL48'AXBR F Trun Tr'
00018E70	3FFF0000 00000000			3447 DC XL16'3FFF0000000000000000000000000000'
		0000000C	00000001	3448 XBFPOUT_NUM EQU (*-XBFPOUT_GOOD)/64
				3449 *
				3450 *
		00018E80	00000001	3451 XBFPFLGS_GOOD EQU *
00018E80	C1E7C2D9 40C640D6			3452 DC CL48'AXBR F Ovfl FPCR'
00018EB0	00000003 F8000003			3453 DC XL16'00000003F80000030000000000000000'
00018EC0	C1E7C2D9 40C640E4			3454 DC CL48'AXBR F Ufl 1 FPCR'
00018EF0	00000002 F8001002			3455 DC XL16'00000002F80010020000000000000000'
00018F00	C1E7C2D9 40C640E4			3456 DC CL48'AXBR F Ufl 2 FPCR'
00018F30	00000002 F8001002			3457 DC XL16'00000002F80010020000000000000000'
00018F40	C1E7C2D9 40C640D5			3458 DC CL48'AXBR F Nmin FPCR'
00018F70	00000002 F8000002			3459 DC XL16'00000002F80000020000000000000000'
00018F80	C1E7C2D9 40C640C9			3460 DC CL48'AXBR F Incr FPCR'
00018FB0	00080002 F8000C02			3461 DC XL16'00080002F8000C020000000000000000'
00018FC0	C1E7C2D9 40C640E3			3462 DC CL48'AXBR F Trun FPCR'
00018FF0	00080002 F8000802			3463 DC XL16'00080002F80008020000000000000000'
		00000006	00000001	3464 XBFPFLGS_NUM EQU (*-XBFPFLGS_GOOD)/64
				3465 *
				3466 *
		00019000	00000001	3467 XBFPRMO_GOOD EQU *
00019000	C1E7C2D9 40D9D440			3468 DC CL48'AXBR RM +NZ RNTE'
00019030	3FFF0000 00000000			3469 DC XL16'3FFF0000000000000000000000000000'
00019040	C1E7C2D9 40D9D440			3470 DC CL48'AXBR RM +NZ RZ'
00019070	3FFF0000 00000000			3471 DC XL16'3FFF0000000000000000000000000000'
00019080	C1E7C2D9 40D9D440			3472 DC CL48'AXBR RM +NZ RP'
000190B0	3FFF0000 00000000			3473 DC XL16'3FFF0000000000000000000000000001'
000190C0	C1E7C2D9 40D9D440			3474 DC CL48'AXBR RM +NZ RM'
000190F0	3FFF0000 00000000			3475 DC XL16'3FFF0000000000000000000000000000'
00019100	C1E7C2D9 40D9D440			3476 DC CL48'AXBR RM +NZ RFS'
00019130	3FFF0000 00000000			3477 DC XL16'3FFF0000000000000000000000000001'
00019140	C1E7C2D9 40D9D440			3478 DC CL48'AXBR RM -NZ RNTE'
00019170	BFFF0000 00000000			3479 DC XL16'BFFF0000000000000000000000000000'
00019180	C1E7C2D9 40D9D440			3480 DC CL48'AXBR RM -NZ RZ'
000191B0	BFFF0000 00000000			3481 DC XL16'BFFF0000000000000000000000000000'
000191C0	C1E7C2D9 40D9D440			3482 DC CL48'AXBR RM -NZ RP'
000191F0	BFFF0000 00000000			3483 DC XL16'BFFF0000000000000000000000000000'
00019200	C1E7C2D9 40D9D440			3484 DC CL48'AXBR RM -NZ RM'
00019230	BFFF0000 00000000			3485 DC XL16'BFFF0000000000000000000000000001'
00019240	C1E7C2D9 40D9D440			3486 DC CL48'AXBR RM -NZ RFS'
00019270	BFFF0000 00000000			3487 DC XL16'BFFF0000000000000000000000000001'
00019280	C1E7C2D9 40D9D440			3488 DC CL48'AXBR RM +NA RNTE'
000192B0	3FFF0000 00000000			3489 DC XL16'3FFF0000000000000000000000000001'
000192C0	C1E7C2D9 40D9D440			3490 DC CL48'AXBR RM +NA RZ'
000192F0	3FFF0000 00000000			3491 DC XL16'3FFF0000000000000000000000000000'
00019300	C1E7C2D9 40D9D440			3492 DC CL48'AXBR RM +NA RP'

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
00019330	3FFF0000 00000000			3493 DC XL16'3FFF0000000000000000000000000001'
00019340	C1E7C2D9 40D9D440			3494 DC CL48'AXBR RM +NA RM'
00019370	3FFF0000 00000000			3495 DC XL16'3FFF000000000000000000000000000'
00019380	C1E7C2D9 40D9D440			3496 DC CL48'AXBR RM +NA RFS'
000193B0	3FFF0000 00000000			3497 DC XL16'3FFF000000000000000000000000001'
000193C0	C1E7C2D9 40D9D440			3498 DC CL48'AXBR RM -NA RNTE'
000193F0	BFFF0000 00000000			3499 DC XL16'BFFF000000000000000000000000001'
00019400	C1E7C2D9 40D9D440			3500 DC CL48'AXBR RM -NA RZ'
00019430	BFFF0000 00000000			3501 DC XL16'BFFF000000000000000000000000000'
00019440	C1E7C2D9 40D9D440			3502 DC CL48'AXBR RM -NA RP'
00019470	BFFF0000 00000000			3503 DC XL16'BFFF000000000000000000000000000'
00019480	C1E7C2D9 40D9D440			3504 DC CL48'AXBR RM -NA RM'
000194B0	BFFF0000 00000000			3505 DC XL16'BFFF000000000000000000000000001'
000194C0	C1E7C2D9 40D9D440			3506 DC CL48'AXBR RM -NA RFS'
000194F0	BFFF0000 00000000			3507 DC XL16'BFFF000000000000000000000000001'
00019500	C1E7C2D9 40D9D440			3508 DC CL48'AXBR RM +TZ RNTE'
00019530	3FFF0000 00000000			3509 DC XL16'3FFF000000000000000000000000000'
00019540	C1E7C2D9 40D9D440			3510 DC CL48'AXBR RM +TZ RZ'
00019570	3FFF0000 00000000			3511 DC XL16'3FFF000000000000000000000000000'
00019580	C1E7C2D9 40D9D440			3512 DC CL48'AXBR RM +TZ RP'
000195B0	3FFF0000 00000000			3513 DC XL16'3FFF000000000000000000000000001'
000195C0	C1E7C2D9 40D9D440			3514 DC CL48'AXBR RM +TZ RM'
000195F0	3FFF0000 00000000			3515 DC XL16'3FFF000000000000000000000000000'
00019600	C1E7C2D9 40D9D440			3516 DC CL48'AXBR RM +TZ RFS'
00019630	3FFF0000 00000000			3517 DC XL16'3FFF000000000000000000000000001'
00019640	C1E7C2D9 40D9D440			3518 DC CL48'AXBR RM -TZ RNTE'
00019670	BFFF0000 00000000			3519 DC XL16'BFFF000000000000000000000000000'
00019680	C1E7C2D9 40D9D440			3520 DC CL48'AXBR RM -TZ RZ'
000196B0	BFFF0000 00000000			3521 DC XL16'BFFF000000000000000000000000000'
000196C0	C1E7C2D9 40D9D440			3522 DC CL48'AXBR RM -TZ RP'
000196F0	BFFF0000 00000000			3523 DC XL16'BFFF000000000000000000000000000'
00019700	C1E7C2D9 40D9D440			3524 DC CL48'AXBR RM -TZ RM'
00019730	BFFF0000 00000000			3525 DC XL16'BFFF000000000000000000000000001'
00019740	C1E7C2D9 40D9D440			3526 DC CL48'AXBR RM -TZ RFS'
00019770	BFFF0000 00000000			3527 DC XL16'BFFF000000000000000000000000001'
00019780	C1E7C2D9 40D9D440			3528 DC CL48'AXBR RM +TA RNTE'
000197B0	3FFF0000 00000000			3529 DC XL16'3FFF000000000000000000000000002'
000197C0	C1E7C2D9 40D9D440			3530 DC CL48'AXBR RM +TA RZ'
000197F0	3FFF0000 00000000			3531 DC XL16'3FFF000000000000000000000000001'
00019800	C1E7C2D9 40D9D440			3532 DC CL48'AXBR RM +TA RP'
00019830	3FFF0000 00000000			3533 DC XL16'3FFF000000000000000000000000002'
00019840	C1E7C2D9 40D9D440			3534 DC CL48'AXBR RM +TA RM'
00019870	3FFF0000 00000000			3535 DC XL16'3FFF000000000000000000000000001'
00019880	C1E7C2D9 40D9D440			3536 DC CL48'AXBR RM +TA RFS'
000198B0	3FFF0000 00000000			3537 DC XL16'3FFF000000000000000000000000001'
000198C0	C1E7C2D9 40D9D440			3538 DC CL48'AXBR RM -TA RNTE'
000198F0	BFFF0000 00000000			3539 DC XL16'BFFF000000000000000000000000002'
00019900	C1E7C2D9 40D9D440			3540 DC CL48'AXBR RM -TA RZ'
00019930	BFFF0000 00000000			3541 DC XL16'BFFF000000000000000000000000001'
00019940	C1E7C2D9 40D9D440			3542 DC CL48'AXBR RM -TA RP'
00019970	BFFF0000 00000000			3543 DC XL16'BFFF000000000000000000000000001'
00019980	C1E7C2D9 40D9D440			3544 DC CL48'AXBR RM -TA RM'
000199B0	BFFF0000 00000000			3545 DC XL16'BFFF000000000000000000000000002'
000199C0	C1E7C2D9 40D9D440			3546 DC CL48'AXBR RM -TA RFS'
000199F0	BFFF0000 00000000			3547 DC XL16'BFFF000000000000000000000000001'

00000028 00000001

3548 XBFPRMO_NUM EQU (*-XBFPRMO_GOOD)/64

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3549 *
				3550 *
		00019A00	00000001	3551 XBFPRMOF_GOOD EQU *
00019A00	C1E7C2D9	40D9D440		3552 DC CL48'AXBR RM +NZ FPCR'
00019A30	00080002	00080002		3553 DC XL16'00080002000800020008000200080002'
00019A40	C1E7C2D9	40D9D440		3554 DC CL48'AXBR RM +NZ FPCR'
00019A70	00080002	00000000		3555 DC XL16'000800020000000000000000000000'
00019A80	C1E7C2D9	40D9D440		3556 DC CL48'AXBR RM -NZ FPCR'
00019AB0	00080001	00080001		3557 DC XL16'00080001000800010008000100080001'
00019AC0	C1E7C2D9	40D9D440		3558 DC CL48'AXBR RM -NZ FPCR'
00019AF0	00080001	00000000		3559 DC XL16'000800010000000000000000000000'
00019B00	C1E7C2D9	40D9D440		3560 DC CL48'AXBR RM +NA FPCR'
00019B30	00080002	00080002		3561 DC XL16'00080002000800020008000200080002'
00019B40	C1E7C2D9	40D9D440		3562 DC CL48'AXBR RM +NA FPCR'
00019B70	00080002	00000000		3563 DC XL16'000800020000000000000000000000'
00019B80	C1E7C2D9	40D9D440		3564 DC CL48'AXBR RM -NA FPCR'
00019BB0	00080001	00080001		3565 DC XL16'00080001000800010008000100080001'
00019BC0	C1E7C2D9	40D9D440		3566 DC CL48'AXBR RM -NA FPCR'
00019BF0	00080001	00000000		3567 DC XL16'000800010000000000000000000000'
00019C00	C1E7C2D9	40D9D440		3568 DC CL48'AXBR RM +TZ FPCR'
00019C30	00080002	00080002		3569 DC XL16'00080002000800020008000200080002'
00019C40	C1E7C2D9	40D9D440		3570 DC CL48'AXBR RM +TZ FPCR'
00019C70	00080002	00000000		3571 DC XL16'000800020000000000000000000000'
00019C80	C1E7C2D9	40D9D440		3572 DC CL48'AXBR RM -TZ FPCR'
00019CB0	00080001	00080001		3573 DC XL16'00080001000800010008000100080001'
00019CC0	C1E7C2D9	40D9D440		3574 DC CL48'AXBR RM -TZ FPCR'
00019CF0	00080001	00000000		3575 DC XL16'000800010000000000000000000000'
00019D00	C1E7C2D9	40D9D440		3576 DC CL48'AXBR RM +TA FPCR'
00019D30	00080002	00080002		3577 DC XL16'00080002000800020008000200080002'
00019D40	C1E7C2D9	40D9D440		3578 DC CL48'AXBR RM +TA FPCR'
00019D70	00080002	00000000		3579 DC XL16'000800020000000000000000000000'
00019D80	C1E7C2D9	40D9D440		3580 DC CL48'AXBR RM -TA FPCR'
00019DB0	00080001	00080001		3581 DC XL16'00080001000800010008000100080001'
00019DC0	C1E7C2D9	40D9D440		3582 DC CL48'AXBR RM -TA FPCR'
00019DF0	00080001	00000000		3583 DC XL16'000800010000000000000000000000'
		00000010	00000001	3584 XBFPRMOF_NUM EQU (*-XBFPRMOF_GOOD)/64

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT				
00019E00						3586	HELPERS	DS	0H (R12 base of helper subroutines)
						3588	*****		
						3589	* REPORT UNEXPECTED PROGRAM CHECK		
						3590	*****		
00019E00						3592	PGMCK	DS	0H
00019E00	F342	C072	F08E	00019E72	0000008E	3593		UNPK	PROGCODE(L'PROGCODE+1),PCINTCD(L'PCINTCD+1)
00019E06	926B	C076			00019E76	3594		MVI	PGMCOMMA,C','
00019E0A	DC03	C072	C178	00019E72	00019F78	3595		TR	PROGCODE,HEXTRTAB
00019E10						3597		UNPK	PGMPSW+(0*9)(9),PCOLDPSW+(0*4)(5)
00019E16	9240	C084			00019E84	3598		MVI	PGMPSW+(0*9)+8,C' '
00019E1A	DC07	C07C	C178	00019E7C	00019F78	3599		TR	PGMPSW+(0*9)(8),HEXTRTAB
00019E20						3601		UNPK	PGMPSW+(1*9)(9),PCOLDPSW+(1*4)(5)
00019E26	9240	C08D			00019E8D	3602		MVI	PGMPSW+(1*9)+8,C' '
00019E2A	DC07	C085	C178	00019E85	00019F78	3603		TR	PGMPSW+(1*9)(8),HEXTRTAB
00019E30						3605		UNPK	PGMPSW+(2*9)(9),PCOLDPSW+(2*4)(5)
00019E36	9240	C096			00019E96	3606		MVI	PGMPSW+(2*9)+8,C' '
00019E3A	DC07	C08E	C178	00019E8E	00019F78	3607		TR	PGMPSW+(2*9)(8),HEXTRTAB
00019E40						3609		UNPK	PGMPSW+(3*9)(9),PCOLDPSW+(3*4)(5)
00019E46	9240	C09F			00019E9F	3610		MVI	PGMPSW+(3*9)+8,C' '
00019E4A	DC07	C097	C178	00019E97	00019F78	3611		TR	PGMPSW+(3*9)(8),HEXTRTAB
00019E50						3613		LA	R0,L'PROGMSG R0 <= length of message
00019E54	4110	C05E			00019E5E	3614		LA	R1,PROGMSG R1 --> the message text itself
00019E58						3615		BAL	R2,MSG Go display this message
						3616			
00019E5C	07FD					3617		BR	R13 Return to caller
00019E5E						3619	PROGMSG	DS	0CL66
00019E5E	D7D9D6C7	D9C1D440				3620		DC	CL20'PROGRAM CHECK! CODE '
00019E72	88888888					3621	PROGCODE	DC	CL4'hhhh'
00019E76	6B					3622	PGMCOMMA	DC	CL1','
00019E77	40D7E2E6	40				3623		DC	CL5' PSW '
00019E7C	88888888	88888888				3624	PGMPSW	DC	CL36'hhhhhhhh hhhhhhhh hhhhhhhh hhhhhhhh '

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3626 *****
				3627 * VERIFICATION ROUTINE
				3628 *****
00019EA0				3630 VERISUB DS 0H
				3631 *
				3632 ** Loop through the VERIFY TABLE...
				3633 *
00019EA0	4110 C32C		0001A12C	3635 LA R1,VERIFTAB R1 --> Verify table
00019EA4	4120 0012		00000012	3636 LA R2,VERIFLEN R2 <= Number of entries
00019EA8	0D30			3637 BASR R3,0 Set top of loop
00019EAA	9846 1000		00000000	3639 LM R4,R6,0(R1) Load verify table values
00019EAE	4D70 C0C2		00019EC2	3640 BAS R7,VERIFY Verify results
00019EB2	4110 100C		0000000C	3641 LA R1,12(,R1) Next verify table entry
00019EB6	0623			3642 BCTR R2,R3 Loop through verify table
00019EB8	9500 C278		0001A078	3644 CLI FAILFLAG,X'00' Did all tests verify okay?
00019EBC	078D			3645 BER R13 Yes, return to caller
00019EBE	47F0 F238		00000238	3646 B FAIL No, load FAILURE disabled wait PSW
				3648 *
				3649 ** Loop through the ACTUAL / EXPECTED results...
				3650 *
00019EC2	0D80			3652 VERIFY BASR R8,0 Set top of loop
00019EC4	D50F 4000 5030	00000000	00000030	3654 CLC 0(16,R4),48(R5) Actual results == Expected results?
00019ECA	4770 C0DA		00019EDA	3655 BNE VERIFAIL No, show failure
00019ECE	4140 4010		00000010	3656 VERINEXT LA R4,16(,R4) Next actual result
00019ED2	4150 5040		00000040	3657 LA R5,64(,R5) Next expected result
00019ED6	0668			3658 BCTR R6,R8 Loop through results
00019ED8	07F7			3660 BR R7 Return to caller

LOC	OBJECT	CODE	ADDR1	ADDR2	STMT
					3662 *****
					3663 * Report the failure...
					3664 *****
00019EDA	9005	C250		0001A050	3666 VERIFAIL STM R0,R5,SAVER0R5 Save registers
00019EDE	92FF	C278		0001A078	3667 MVI FAILFLAG,X'FF' Remember verification failure
					3668 *
					3669 ** First, show them the description...
					3670 *
00019EE2	D22F	C1E0	5000	00019FE0	00000000 3671 MVC FAILDESC,0(R5) Save results/test description
00019EE8	4100	0044		00000044	3672 LA R0,L'FAILMSG1 R0 <= length of message
00019EEC	4110	C1CC		00019FCC	3673 LA R1,FAILMSG1 R1 --> the message text itself
00019EF0	4520	C27A		0001A07A	3674 BAL R2,MSG Go display this message
					3675 *
					3676 ** Save address of actual and expected results
					3677 *
00019EF4	5040	C24C		0001A04C	3678 ST R4,AACTUAL Save A(actual results)
00019EF8	4150	5030		00000030	3679 LA R5,48(,R5) R5 ==> expected results
00019EFC	5050	C248		0001A048	3680 ST R5,AEXPECT Save A(expected results)
					3681 *
					3682 ** Format and show them the EXPECTED ("Want") results...
					3683 *
00019F00	D205	C210	C408	0001A010	0001A208 3684 MVC WANTGOT,=CL6'Want: '
00019F06	F384	C216	C248	0001A016	0001A048 3685 UNPK FAILADR(L'FAILADR+1),AEXPECT(L'AEXPECT+1)
00019F0C	9240	C21E		0001A01E	3686 MVI BLANKEQ,C' '
00019F10	DC07	C216	C178	0001A016	00019F78 3687 TR FAILADR,HEXTRTAB
00019F16	F384	C221	5000	0001A021	00000000 3689 UNPK FAILVALS+(0*9)(9),(0*4)(5,R5)
00019F1C	9240	C229		0001A029	3690 MVI FAILVALS+(0*9)+8,C' '
00019F20	DC07	C221	C178	0001A021	00019F78 3691 TR FAILVALS+(0*9)(8),HEXTRTAB
00019F26	F384	C22A	5004	0001A02A	00000004 3693 UNPK FAILVALS+(1*9)(9),(1*4)(5,R5)
00019F2C	9240	C232		0001A032	3694 MVI FAILVALS+(1*9)+8,C' '
00019F30	DC07	C22A	C178	0001A02A	00019F78 3695 TR FAILVALS+(1*9)(8),HEXTRTAB
00019F36	F384	C233	5008	0001A033	00000008 3697 UNPK FAILVALS+(2*9)(9),(2*4)(5,R5)
00019F3C	9240	C23B		0001A03B	3698 MVI FAILVALS+(2*9)+8,C' '
00019F40	DC07	C233	C178	0001A033	00019F78 3699 TR FAILVALS+(2*9)(8),HEXTRTAB
00019F46	F384	C23C	500C	0001A03C	0000000C 3701 UNPK FAILVALS+(3*9)(9),(3*4)(5,R5)
00019F4C	9240	C244		0001A044	3702 MVI FAILVALS+(3*9)+8,C' '
00019F50	DC07	C23C	C178	0001A03C	00019F78 3703 TR FAILVALS+(3*9)(8),HEXTRTAB
00019F56	4100	0035		00000035	3705 LA R0,L'FAILMSG2 R0 <= length of message
00019F5A	4110	C210		0001A010	3706 LA R1,FAILMSG2 R1 --> the message text itself
00019F5E	4520	C27A		0001A07A	3707 BAL R2,MSG Go display this message

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3709 *
				3710 **
				3711 *
00019F62	D205 C210 C40E	0001A010	0001A20E	3712 MVC WANTGOT,=CL6'Got: '
00019F68	F384 C216 C24C	0001A016	0001A04C	3713 UNPK FAILADR(L'FAILADR+1),AACTUAL(L'AACTUAL+1)
00019F6E	9240 C21E		0001A01E	3714 MVI BLANKEQ,C' '
00019F72	DC07 C216 C178	0001A016	00019F78	3715 TR FAILADR,HEXTRTAB
00019F78	F384 C221 4000	0001A021	00000000	3717 UNPK FAILVALS+(0*9)(9),(0*4)(5,R4)
00019F7E	9240 C229		0001A029	3718 MVI FAILVALS+(0*9)+8,C' '
00019F82	DC07 C221 C178	0001A021	00019F78	3719 TR FAILVALS+(0*9)(8),HEXTRTAB
00019F88	F384 C22A 4004	0001A02A	00000004	3721 UNPK FAILVALS+(1*9)(9),(1*4)(5,R4)
00019F8E	9240 C232		0001A032	3722 MVI FAILVALS+(1*9)+8,C' '
00019F92	DC07 C22A C178	0001A02A	00019F78	3723 TR FAILVALS+(1*9)(8),HEXTRTAB
00019F98	F384 C233 4008	0001A033	00000008	3725 UNPK FAILVALS+(2*9)(9),(2*4)(5,R4)
00019F9E	9240 C23B		0001A03B	3726 MVI FAILVALS+(2*9)+8,C' '
00019FA2	DC07 C233 C178	0001A033	00019F78	3727 TR FAILVALS+(2*9)(8),HEXTRTAB
00019FA8	F384 C23C 400C	0001A03C	0000000C	3729 UNPK FAILVALS+(3*9)(9),(3*4)(5,R4)
00019FAE	9240 C244		0001A044	3730 MVI FAILVALS+(3*9)+8,C' '
00019FB2	DC07 C23C C178	0001A03C	00019F78	3731 TR FAILVALS+(3*9)(8),HEXTRTAB
00019FB8	4100 0035		00000035	3733 LA R0,L'FAILMSG2 R0 <= length of message
00019FBC	4110 C210		0001A010	3734 LA R1,FAILMSG2 R1 --> the message text itself
00019FC0	4520 C27A		0001A07A	3735 BAL R2,MSG Go display this message
00019FC4	9805 C250		0001A050	3737 LM R0,R5,SAVER0R5 Restore registers
00019FC8	47F0 C0CE		00019ECE	3738 B VERINEXT Continue with verification...
00019FCC				3740 FAILMSG1 DS 0CL68
00019FCC	C3D6D4D7 C1D9C9E2			3741 DC CL20'COMPARISON FAILURE! '
00019FE0	4D8485A2 83998997			3742 FAILDESC DC CL48'(description)'
0001A010				3744 FAILMSG2 DS 0CL53
0001A010	40404040 4040			3745 WANTGOT DC CL6' ' 'Want: ' -or- 'Got: '
0001A016	C1C1C1C1 C1C1C1C1			3746 FAILADR DC CL8'AAAAAAA'
0001A01E	407E40			3747 BLANKEQ DC CL3' = '
0001A021	88888888 88888888			3748 FAILVALS DC CL36'hhhhhhhh hhhhhhhh hhhhhhhh hhhhhhhh '
0001A048	00000000			3750 AEXPECT DC F'0' ==> Expected ("Want") results
0001A04C	00000000			3751 AACTUAL DC F'0' ==> Actual ("Got") results
0001A050	00000000 00000000			3752 SAVER0R5 DC 6F'0' Registers R0 - R5 save area
0001A068	F0F1F2F3 F4F5F6F7			3753 CHARHEX DC CL16'0123456789ABCDEF'
		00019F78	00000010	3754 HEXTRTAB EQU CHARHEX-X'F0' Hexadecimal translation table
0001A078	00			3755 FAILFLAG DC X'00' FF = Fail, 00 = Success

LOC	OBJECT CODE	ADDR1	ADDR2	STMT
				3790 *****
				3791 * VERIFY TABLE
				3792 *****
				3793 *
				3794 * A(actual results), A(expected results), A(#of results)
				3795 *
				3796 *****
0001A12C				3798 VERIFTAB DC 0F'0'
0001A12C	00001000			3799 DC A(SBFPNFOT)
0001A130	0000A000			3800 DC A(SBFPNFOT_GOOD)
0001A134	00000064			3801 DC A(SBFPNFOT_NUM)
				3802 *
0001A138	00001700			3803 DC A(SBFPNFFL)
0001A13C	0000B900			3804 DC A(SBFPNFFL_GOOD)
0001A140	00000064			3805 DC A(SBFPNFFL_NUM)
				3806 *
0001A144	00001E00			3807 DC A(SBFPOUT)
0001A148	0000D200			3808 DC A(SBFPOUT_GOOD)
0001A14C	00000006			3809 DC A(SBFPOUT_NUM)
				3810 *
0001A150	00001F00			3811 DC A(SBFPFLGS)
0001A154	0000D380			3812 DC A(SBFPFLGS_GOOD)
0001A158	00000006			3813 DC A(SBFPFLGS_NUM)
				3814 *
0001A15C	00002000			3815 DC A(SBFPRMO)
0001A160	0000D500			3816 DC A(SBFPRMO_GOOD)
0001A164	00000018			3817 DC A(SBFPRMO_NUM)
				3818 *
0001A168	00002300			3819 DC A(SBFPRMOF)
0001A16C	0000DB00			3820 DC A(SBFPRMOF_GOOD)
0001A170	00000018			3821 DC A(SBFPRMOF_NUM)
				3822 *
0001A174	00004000			3823 DC A(LBFPNFOT)
0001A178	0000E100			3824 DC A(LBFPNFOT_GOOD)
0001A17C	000000C8			3825 DC A(LBFPNFOT_NUM)
				3826 *
0001A180	00004D00			3827 DC A(LBFPNFFL)
0001A184	00011300			3828 DC A(LBFPNFFL_GOOD)
0001A188	00000064			3829 DC A(LBFPNFFL_NUM)
				3830 *
0001A18C	00005400			3831 DC A(LBFPOUT)
0001A190	00012C00			3832 DC A(LBFPOUT_GOOD)
0001A194	0000000C			3833 DC A(LBFPOUT_NUM)
				3834 *
0001A198	00005600			3835 DC A(LBFPFLGS)
0001A19C	00012F00			3836 DC A(LBFPFLGS_GOOD)
0001A1A0	00000006			3837 DC A(LBFPFLGS_NUM)
				3838 *
0001A1A4	00005700			3839 DC A(LBFPRMO)
0001A1A8	00013080			3840 DC A(LBFPRMO_GOOD)
0001A1AC	00000028			3841 DC A(LBFPRMO_NUM)
				3842 *
0001A1B0	00005C00			3843 DC A(LBFPRMOF)
0001A1B4	00013A80			3844 DC A(LBFPRMOF_GOOD)
0001A1B8	00000018			3845 DC A(LBFPRMOF_NUM)

MACRO DEFN REFERENCES

No defined macros

DESC	SYMBOL	SIZE	POS	ADDR
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Entry: 0

Image	IMAGE	107028	00000-1A213	00000-1A213
Region		107028	00000-1A213	00000-1A213
CSECT	BFPADD	107028	00000-1A213	00000-1A213

STMT

FILE NAME

```
1 c:\Users\Fish\Documents\Visual Studio 2008\Projects\MyProjects\ASMA-0\bfp-016-add\bfp-016-add.asm
```

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** NO ERRORS FOUND **
```